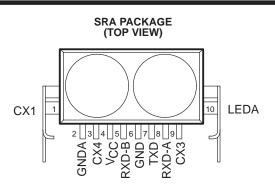
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- Fully Compliant with IrDA 1.1 (4 MBPS)
- Compatible with ASK, HP-SIR and TV Remote
- No Programming Required to Switch Speeds
- Backward Compatible to Slower IrDA Speeds
- Excellent Noise Immunity
- Fully Supportable by all Interface Chips
- Designed to Compensate for Light Loss Caused by Cosmetic Windows



#### description

The TSLM1100 is an infrared transceiver that provides the interface between logic and IR signals for through-air, serial, half-duplex IR data links. The TSLM1100 is compliant with the Infrared Data Association (IrDA) 1.1 physical-layer specification. Additionally, the TSLM1100 is compatible with ASK, HP-SIR and TV Remote standards.

The TSLM1100 is a hybrid device that includes a high-speed AIGaAs 870-nm LED, a silicon intrinsic PN junction (PIN) diode, and a LinCMOS transceiver integrated circuit. This IC has the LED driver and a receiver that provides two output signals: RXD-A for data rates from 2.4 kb/s to 115.2 kb/s and RXD-B for data rates of 576 kb/s to 4.0 Mb/s.

The device is encapsulated in a visible-light-rejecting plastic package that has integral lenses for the LED and the PIN diode. The receiver lens increases the effective area of the PIN diode to increase sensitivity. The LED lens is designed to provide a beam angle of  $\pm 30^{\circ}$ . The receiver outputs pulse low when an IR signal is detected. The power supply for both PIN diode and LED should be filtered to minimize noise from external sources.

This transceiver is well suited for a wide variety of IR interface applications including: PC notebooks, PDAs, pagers, printers, cameras, LANs, telephones and industrial handheld devices.

INP	UTS	C	OUTPUTS					
TXD	Ee	l <sub>e(LED)</sub>	RXD-A	RXD-B				
VIH	Х	High	NV	NV				
VIL	EI(IH)†	Low	Low	NV				
VIL	EI(IH)‡	Low	NV	Low				
VIL	EI(IL)	Low	High	High				

FUNCTION	
	IADLL

X – don't care, NV – not valid † Data rates up to 115.2 kb/s ‡ Data rates > 115.2 kb/s



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### **Terminal Functions**

PIN		DESCRIPTION				
NAME	NO.	DESCRIPTION				
CX1	1	Photodiode bypass capacitor				
GNDA	2	Analog ground				
CX4	3	Averaging capacitor				
VCC	4	Supply voltage				
RXD-B	5	Receiver data output – Channel B				
GND	6	Ground				
TXD	7	Transmitter data input				
RXD-A	8	Receiver data output – Channel A				
CX3	9	Threshold capacitor				
LEDA	10	LED anode				

### absolute maximum ratings over operating free-air temperature (unless otherwise noted)<sup>†</sup>

Supply voltage, V <sub>CC</sub>	7 V
LED anode voltage range, V <sub>I(I EDA)</sub> –0	0.5 V to 7 V
Receiver data output voltage range: V <sub>O(RXD-A)</sub> 0.5 V to V	<sub>CC</sub> + 0.5 V
V <sub>O(RXD-B)</sub> –0.5 V to V	<sub>CC</sub> + 0.5 V
Average LED current, I <sub>I(LED)(avg)</sub> : Direct current	100 mA
Pulsed, $\leq$ 90- $\mu$ s pulse width, $\leq$ 25% duty cycle	
Peak LED current, $I_{I(LED)(PK)}$ : $\leq$ 90- $\mu$ s pulse width, $\leq$ 25% duty cycle	660 mA
$2^{2} \leq 2 - \mu s$ pulse width, $\leq 10\%$ duty cycle	1 A
Transmitter data input current range, I <sub>I(TXD)</sub> –12 m/	
Storage temperature range, T <sub>stg</sub>	°C to 85°C

<sup>†</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

### recommended operating conditions

	CONDITIONS	MIN	MAX	UNITS
Supply voltage, V <sub>CC</sub>		4.75	5.25	V
Logic high transmitter input voltage, VIH		4.25	5.25	V
Logic low transmitter input voltage, VIL		0.0	0.3	V
	For in-band signals ≤ 116 kb/s	0.0036	500	mW/cm <sup>2</sup>
Logic high receiver input irradiance, $E_{e(IH)}$	For in-band signals ≥ 576 kb/s	0.0090	500	mW/cm <sup>2</sup>
Logic low receiver input irradiance, E <sub>e(IL)</sub>	For in-band signals		0.3	μW/cm <sup>2</sup>
LED (logic high) Current pulse amplitude, II(LEDA)		400	660	mA
Receiver setup time	For full sensitivity after transmitting		1.0	ms
Receiver signal rate, RXD-A		2.4	116	kb/s
Receiver signal rate, RXD-B		0.576	4	Mb/s
Ambient light	See IrDA serial infrared physical link specification, 1.1e Appendix A for Ambient levels and Appendix B			
Operating temperature, T <sub>A</sub>	Case to ambient thermal resistance $\leq$ 50°C/W	0	70	°C



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# electrical characteristics at V<sub>CC</sub> = 5 V, T<sub>A</sub> = 25°C (unless otherwise noted); test conditions represent worst-case values for the parameters under test

	PARAMETER		TEST CONDITIONS	MIN	TYP	MAX	UNIT
	Low-level output voltage, receiver data	RXD-A	$\begin{array}{l} I_{O} = 1 \text{ mA}, \\ \text{for in-band } E_{e} \geq 3.6 \ \mu\text{W/cm}^{2}, \\ \varphi^{1/2} \leq 15^{\circ} \end{array}$			0.5	V
VOL		RXD-B	$ \begin{array}{l} I_{O} = 1 \text{ mA}, \\ \text{for in-band } E_{e} \geq 9 \ \mu\text{W/cm}^{2}, \\ \varphi^{1/2} \leq 15^{\circ} \end{array} \end{array} $			0.5	v
Val	High-level output voltage, receiver data	RXD-A	$I_O$ =– 20 $\mu A,$ for in-band E_e $\leq 0.3  \mu \text{W/cm}^2$	V <sub>CC</sub> -0.6			V
Vон		RXD-B	$I_O = -20 \ \mu\text{A},$ for in-band $E_e \le 0.3 \ \mu\text{W/cm}^2$	V <sub>CC</sub> - 1.2			v
١ <sub>L</sub>	Low-level input current, transmitter data	IIL(TXD)	$GND \le V_{IL(TXD)} \le 0.3 V$	-2		2	μA
Ιн	High-level input current, transmitter data	IIH(TXD)	V <sub>IH(TXD)</sub> = 4.25 V		40	250	μA
٧T	On-state voltage LED anode	VT(LEDA)	I <sub>I(LED)</sub> = 400 mA at 25°C V <sub>IH(TXD)</sub> = 4.25 V			2.78	V
I <sub>D(lkg)</sub>	OFF-state leakage current, LED anode	ID(Ikg)(LEDA)	VI(LEDA) = VCC = 5.25 V VIL(TXD) = 0.3 V			250	μΑ
ICC1	Supply current, idle state		$V_{CC} = 5.25 V$ $V_{I}(TXD) = V_{IL}, E_{e} = 0$		3	5.1	mA
I <sub>CC2</sub>	Supply current, active receiver		$V_{CC} = 5.25 V$ $V_{I(TXD)} = V_{IL},$ $E_{e} \le 500 \text{ nW/cm}^{2}$		4	18	mA

#### optical specifications

	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
2¢1/2	Receiver viewing angle		±15			0
	Effective detector area			0.2		cm <sup>2</sup>
		$V_{IH(TXD)} = 4.25 V$ $I_{I(LED)} = 450 mA,$ $\Phi^{1/2} \le 15^{\circ}, T_A = 25^{\circ}C$	100	177		mW/sr
le	Transmitter radiant intensity, logic high	$\begin{split} V_{IH(TXD)} &= 4.25 \ V \\ I_{I}(LED) &= 450 \ mA, \\ \Phi^{1/2} &\leq 15^{\circ}, \ 0^{\circ}C \leq T_{A} \leq 70^{\circ}C \end{split}$	80	177		mW/sr
λp	Transmitter peak-emission wavelength			875		nm
$\Delta\lambda^{1/2}$	Transmitter spectral-line half-width			35		nm
$2\Phi^{1/2}$	Transmitter viewing angle		±15		±30	0
	Receiver peak-emission sensitivity wave length			880		nm



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### switching characteristics

	PARAMETER		TEST CONDITIONS	MIN	TYP	MAX	UNIT
1 (5)10			$I_e(PW)(TXD) = 1.6 \ \mu s at 115.2 k pulses/s$	1.5	1.6	1.8	μs
<sup>I</sup> e(PW)			I <sub>e(PW)(TXD)</sub> = 125 ns at 2M pulses/s	115	125	135	ns
	Tronomittor redient intensity		1 (D(A) (T) (D) = 125  ps at 2M pulsas/s			40	ns
'e	Transmitter radiant intensity	Fall time	l <sub>e</sub> (PW)(TXD) = 125 ns at 2M pulses/s			40	115
PW	Pulse width	RXD-A	$\Phi^{1/2} = < 15^{\circ}$	1		7.5	μs
FVV		RXD-B	Φ 112 ≡≤ 15.	75		185	ns
PW	Pulse width, RXD-B (ASK)		500 kHz, 50% duty cycle carrier ASK	0.7	1	1.3	μs
4.	Receiver latency time	RXD-A			0.5		
tL	Receiver latency tille	RXD-B			0.5		ms

### **APPLICATION INFORMATION**

### schematic

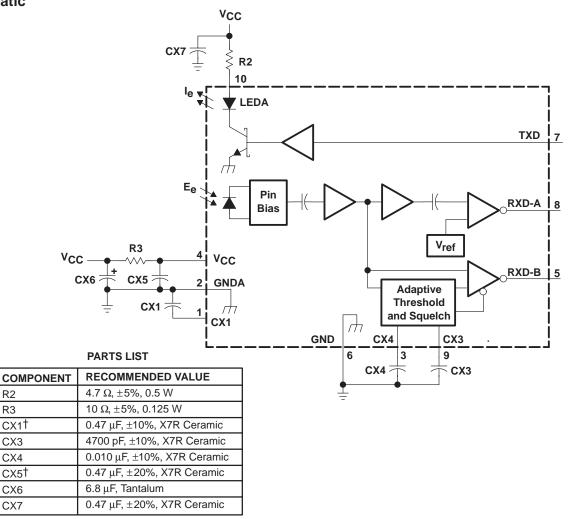
R2 R3

CX3

CX4 CX5†

CX6

CX7



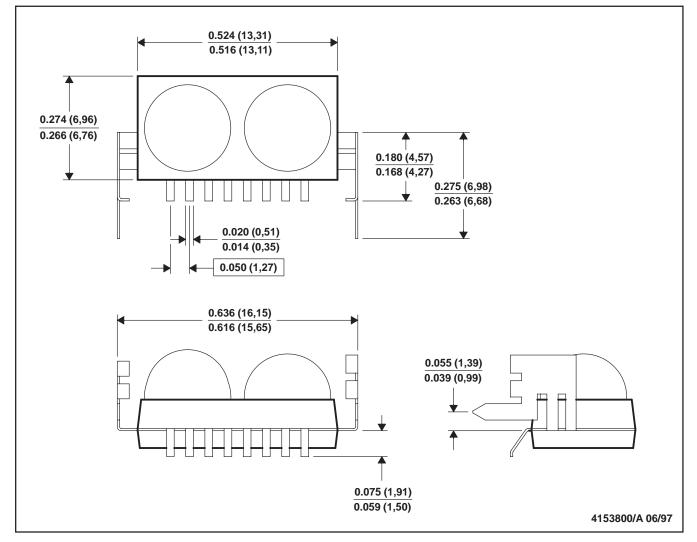
<sup>†</sup>CX1 and CX5 must be placed within 0.7 cm of the TSLM1100 to obtain optimum noise immunity.



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### MECHANICAL DATA

#### PLASTIC SMALL-OUTLINE PACKAGE



NOTES: A. All linear dimensions are in inches (millimeters). B. This drawing is subject to change without notice.

SRA (R-PSIP-T8)





### PACKAGING INFORMATION

Orderable Device	Status <sup>(1)</sup>	Package Type	Package Drawing	Pins Package Qty	Eco Plan <sup>(2)</sup>	Lead/Ball Finish	MSL Peak Temp <sup>(3)</sup>
TSLM1100	OBSOLETE	OPTO	SRA	10	TBD	Call TI	Call TI

<sup>(1)</sup> The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS) or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details. TBD: The Pb-Free/Green conversion plan has not been defined.

**Pb-Free (RoHS):** TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

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<sup>(3)</sup> MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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