



# 40V N-CHANNEL 175°C MOSFET PowerDI

### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> T <sub>C</sub> = +25°C
40V	7.0mΩ @ V <sub>GS</sub> = 10V	110A

#### **Features and Benefits**

- Rated to +175°C Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching Ensures More Reliable and Robust End Application
- Low R<sub>DS(ON)</sub> Minimizes Power Losses
- Low Q<sub>G</sub> Minimizes Switching Losses
- <1.1mm Package Profile Ideal for Thin Applications</li>
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

# **Description and Applications**

This MOSFET is designed to meet the stringent requirements of Automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

PowerDI5060-8

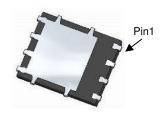
- Engine Management Systems
- Body Control Electronics
- DC-DC Converters

#### **Mechanical Data**

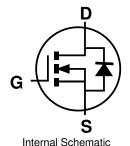
- Case: PowerDI<sup>®</sup>5060-8
- Case Material: Molded Plastic, "Green" Molding Compound.
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (63)
- Weight: 0.097 grams (Approximate)

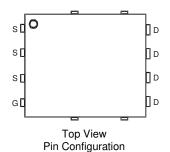


Top View



Bottom View





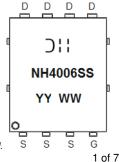
### Ordering Information (Note 5)

Part Number	Case	Packaging
DMNH4006SPSQ-13	PowerDI5060-8	2,500/Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to http://www.diodes.com/product\_compliance\_definitions.html
- $5.\ For\ packaging\ details,\ go\ to\ our\ website\ at\ http://www.diodes.com/products/packages.html.$

## **Marking Information**



☐ H = Manufacturer's Marking
NH4006SS = Product Type Marking Code
YYWW = Date Code Marking
YY = Last Two Digits of Year (ex: 16 = 2016)
WW = Week Code (01 to 53)

April 2016



## **Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			$V_{DSS}$	40	V
Gate-Source Voltage			V <sub>GSS</sub>	20	V
Continuous Drain Current (Note 7) $V_{GS} = 10V$ Steady $T_C = +25^{\circ}C$ State $T_C = +100^{\circ}C$		I <sub>D</sub>	110 80	А	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I <sub>DM</sub>	180	Α
Maximum Continuous Body Diode Forward Current			Is	100	Α
Avalanche Current (Note 8) L=1mH			I <sub>AS</sub>	64	Α
Avalanche Energy (Note 8) L=1mH			Eas	208	mJ

#### **Thermal Characteristics**

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 6)		$P_D$	1.6	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	93	°C/W
Total Power Dissipation (Note 7)		$P_{D}$	3.0	W
Thermal Resistance, Junction to Ambient (Note 7)	Steady State	$R_{\theta JA}$	50	°C/W
Thermal Resistance, Junction to Case		R <sub>eJC</sub>	1.1	3 C/VV
Operating and Storage Temperature Range		T <sub>J,</sub> T <sub>STG</sub>	-55 to +175	°C

## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 9)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	40	_		٧	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current, T <sub>J</sub> = +25°C	I <sub>DSS</sub>	_	_	1	μΑ	$V_{DS} = 40V$ , $V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 9)							
Gate Threshold Voltage	$V_{GS(TH)}$	2	2.4	4	V	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$	
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	_	4.5	7	mΩ	$V_{GS} = 10V, I_D = 50A$	
Diode Forward Voltage	$V_{SD}$	_	0.7	1.2	٧	$V_{GS} = 0V, I_S = 1.0A$	
DYNAMIC CHARACTERISTICS (Note 10)						_	
Input Capacitance	C <sub>ISS</sub>	_	2,280		pF	$V_{DS} = 25V, V_{GS} = 0V,$ f = 1MHz	
Output Capacitance	Coss	_	557	l	рF		
Reverse Transfer Capacitance	C <sub>RSS</sub>	_	283		pF		
Gate Resistance	$R_{G}$	_	1.7	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$	
Total Gate Charge (V <sub>GS</sub> = 10V)	$Q_{G}$	_	50.9	_	nC	V <sub>DS</sub> = 32V, I <sub>D</sub> = 86A	
Gate-Source Charge	Q <sub>GS</sub>	_	9.6	_	nC		
Gate-Drain Charge	$Q_{GD}$	_	20.4	_	nC		
Turn-On Delay Time	t <sub>D(ON)</sub>	_	7.7	_	ns		
Turn-On Rise Time	t <sub>R</sub>	_	9.3	_	ns	$V_{GS} = 10V, V_{DS} = 20V,$ $R_G = 3.5\Omega, I_D = 86A$	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	18.1	_	ns		
Turn-Off Fall Time	t <sub>F</sub>	_	8.1	_	ns		
Body Diode Reverse Recovery Time	t <sub>RR</sub>	_	31.6		ns	$I_F = 50A$ , $di/dt = 100A/\mu s$	
Body Diode Reverse Recovery Charge	Q <sub>RR</sub>	_	27.6		nC	$I_F = 50A$ , $di/dt = 100A/\mu s$	

6. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.

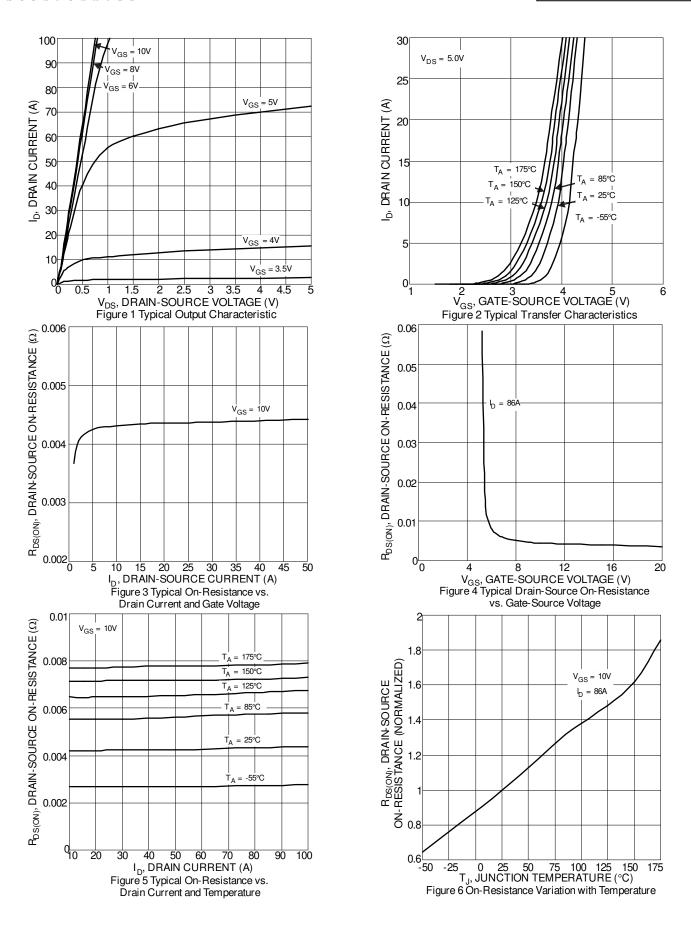
<sup>7.</sup> Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.

<sup>8.</sup> IAS and EAS rating are based on low frequency and duty cycles to keep  $T_J = +25$ °C.

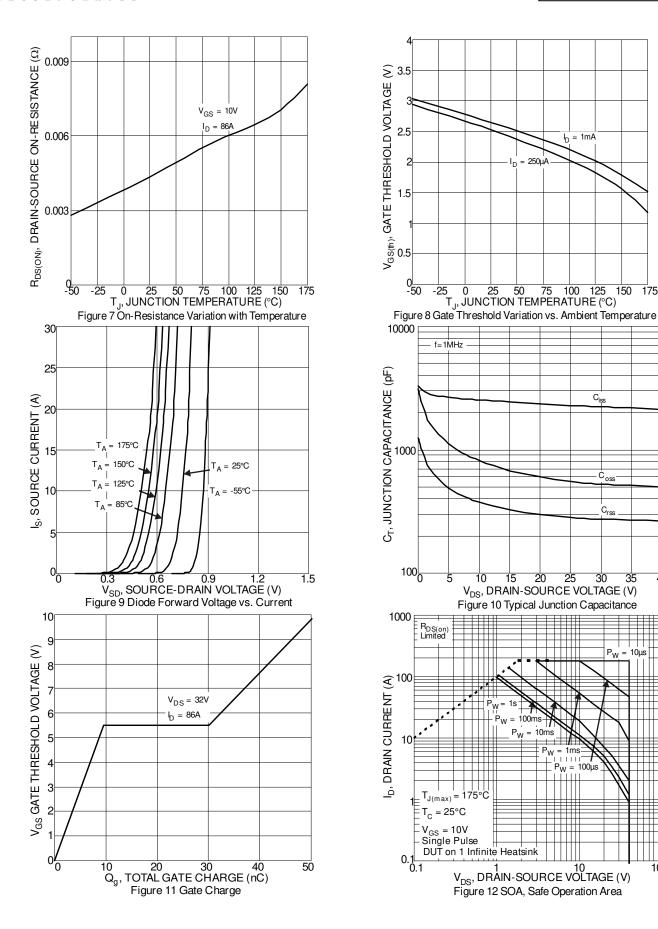
<sup>9.</sup> Short duration pulse test used to minimize self-heating effect.

<sup>10.</sup> Guaranteed by design. Not subject to product testing.



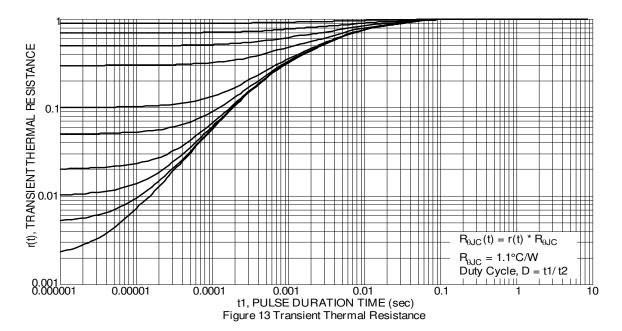






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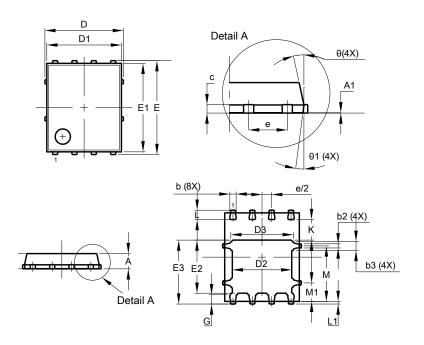




# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### PowerDI5060-8

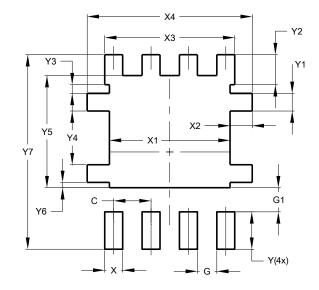


PowerDI5060-8						
Dim	Min	Max	Тур			
Α	0.90	1.10	1.00			
<b>A</b> 1	0.00	0.05	_			
b	0.33	0.51	0.41			
b2	0.200	0.350	0.273			
b3	0.40	0.80	0.60			
C D	0.230	0.330	0.277			
	ļ	5.15 BSC				
D1	4.70	5.10	4.90			
D2	3.70	4.10	3.90			
D3	3.90	4.30	4.10			
Е	(	6.15 BSC				
E1	5.60	5.60 6.00				
E2	3.28	3.68	3.48			
E3	3.99	4.39	4.19			
е	1.27 BSC					
G	0.51	0.71	0.61			
K	0.51	1	_			
L	0.51	0.71	0.61			
L1	0.100	0.200	0.175			
M	3.235	4.035	3.635			
M1	1.00	1.40	1.21			
Θ	10°	12°	11°			
Θ1	6°	8°	7°			
All	All Dimensions in mm					

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### PowerDI5060-8



Dimensions	Value (in mm)
C	1.270
G	0.660
G1	0.820
Х	0.610
X1	4.100
X2	0.755
Х3	4.420
X4	5.610
Υ	1.270
Y1	0.600
Y2	1.020
Y3	0.295
Y4	1.825
Y5	3.810
Y6	0.180
<b>Y</b> 7	6.610



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