Low ESR 50 kΩ Max. 32.768 kHz Crystal Unit: FC3215AN

Features

- Package size: 3.2 x 1.5 mm, t = 0.9 mm Max.
- Nominal frequency range: 32.768 kHz
- Frequency tolerance: $\pm 20 \times 10^{-6} (+25 \text{ °C} \pm 5 \text{ °C})$
- Operating temperature: -40 °C to +105 °C
- ESR:
- . 35 kΩ Typ. (+25 °C)
 - 50 kΩ Max. (-40 °C to +85 °C) 60 kΩ Max. (-40 °C to +105 °C)



Applications

- Wireless Modules for Sub-clock
- Wearable Products
- Low Power MCUs for Sub-clock
- Battery Powered IoT Products

Description

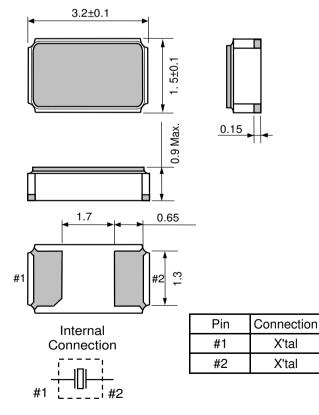
FC3215AN is a low ESR and compact 32.768 kHz crystal unit that has a robust Pb-free metal lid + seam sealed package.

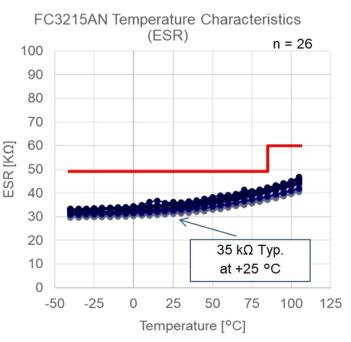
It is equipped with a newly redesigned crystal element based on our in-house design and production technology expertise of tuning-fork crystal devices over the decades. It is ideal for applications that require low current consumption, such battery powered IoT devices .

It also supports an operating temperature range of up to +105 °C.

Outline Drawing and Terminal Assignment

Typical Performance Low ESR





[1] Product Number / Product Name

(1-1) Product Number

X1A000161xxxx16 (Please contact Epson for details)

- (1-2) Product Name (Standard Form)
 - FC3215AN 32.768000kHz 12.5 +20.0-20.0 1

2 3 4

()Model ②Frequency ③Load capacitance (pF) ④Frequency tolerance (x10⁻⁶, +25 °C)

[2] Absolute Maximum Ratings

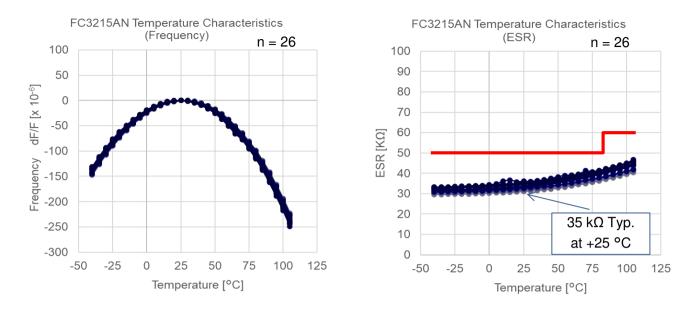
ltem	Symbol		Rating value	!	Unit	Note
item		Min.	Тур.	Max.		Note
Storage temperature range	T_stg	-55	-	+125	°C	
Maximum level of drive	GL	-	-	0.5	μW	

[3] Operating Conditions

ltem	Symbol		Rating value)	Unit	Note
nem		Min.	Тур.	Max.		
Operating temperature range	T_use	-40	-	+105	°C	
Level of drive	DL	0.01	0.1	0.5	μW	
Load capacitance	CL		9, 12.5		pF	Please contact us for other CL values

[4] Static Characteristics

Item	Symbol	Specifications	Unit	Condition / Remarks	
Nominal frequency range	f_nom 32.768		kHz		
Frequency tolerance	f_tol	±20	x10 ⁻⁶	T_use = +25 °C ± 3 °C DL = 0.1 μW This is not include frequency aging	
Turnover temperature	Ti	+25 ± 5	°C		
Parabolic coefficient	В	-0.04 Max.	$x10^{-6} / °C^2$		
Motional resistance (ESR)		35 Typ. (+25 °C)			
	R1	50 Max. (-40 °C to +85 °C)	kΩ	Measuring instrument: Keysight 4294A DL = 0.5 µW	
		60 Max. (-40 °C to +105 °C)			
Motional capacitance	C1	8.4 Тур.	fF		
Shunt capacitance	C0	1.6 Тур.	pF		
Isolation resistance	IR	200 Min.	MΩ		
Frequency aging	f_age	±3	x10 ⁻⁶	T_use = +25 °C, First year, DL = 0.1 μW	



[5] Frequency and ESR vs. Temperature Characteristics

[6] Marking Description

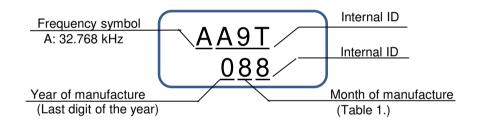
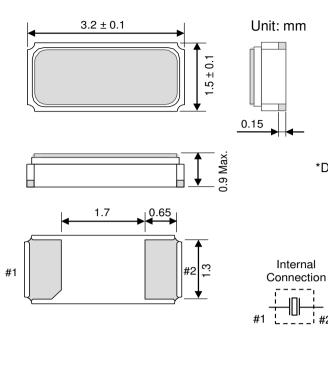
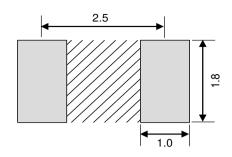


Table 1. Month of manufacture

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	Х	Y	Ζ

[7] Outline Drawing and Recommended Footprint





*Do not design any circuit patterns in the shaded area.

Connection

X'tal

X'tal

Pin

#1

#2

Reference weight Typ.: 13 mg

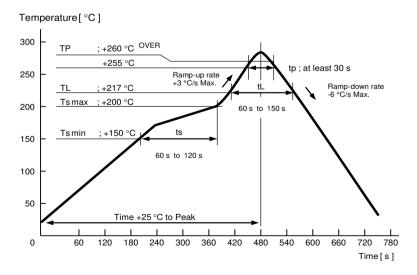
Terminal coating: Au plating

[8] Moisture Sensitivity Level

Parameter	Specification	Conditions
MSL	LEVEL1	IPC/JEDEC J-STD-020D.01

#2

[9] Reflow Profile (IPC/JEDEC J-STD-020D.01)



[10] Packing Information

(1) Packing Quantity

The last two digits of the Product Number (X1A000161xxxx16) are a code that defines the packing quantity The standard is "16" for a 3 000 pcs/Reel.

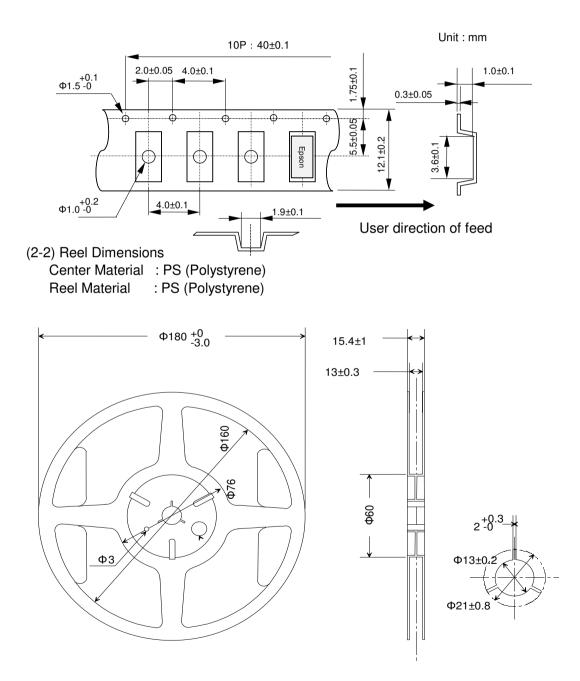
(2) Taping Specification

Compliant to EIA-481, IEC 60286 and JIS C0806

(2-1) Tape Dimensions

Carrier Tape Material : PS (Polystyrene)

Top Tape Material : PET (Polyethylene Terephthalate) +PE (Polyethylene)



[11] Handling Precautions

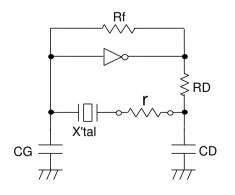
Please review the "Handling Precautions" on our website for proper handling and behavior to ensure the performance of your equipment/product. (https://www5.epsondevice.com/en/information/#precaution) In addition to the "Handling Precautions" on the website, please also pay attention to the following to avoid deterioration of product performance.

1. Max three (3) times reflow is allowed.

In case of rework by soldering iron, its condition should be +350 °C max. + within 5 sec.
Applying excessive shock or vibration to the crystal unit may causes deterioration damage. The product may be damaged depends on the condition such as shock in assembly machine. Please check if your condition is safe in advance. And in case of assembly condition change, please check it again in advance.

- Shortest line pattern on PCB is recommended. Too long line on PCB may causes abnormal oscillation.
- 4. Failures covered by free warranty period are limited to the cases where the product is used under the usage and environment described in the specifications. In addition, products that have been opened (including partially opened, modified, or intended to be opened) are not covered. In order to ensure frequency accuracy and prevent moisture condensation due to sudden temperature changes, it is recommended to store and use in normal room temperature and humidity. If the product is stored for a long period (one year or more), please check solderability of the terminals before use.
- Ultrasonic cleaning may cause resonant damage of the crystal unit depend on its condition.
 Since we are unable to specify the conditions (type of cleaning unit, power, time, condition inside the bath, etc.) at your company, we cannot guarantee the performance of the product when it is cleaned by ultrasonic cleaner.
- Condensation on oscillator circuit board may causes frequency shift or oscillation stop.
- Please use the product under the condition there is no condensation.
- 7. If excessive drive level is applied to the crystal unit, it may cause performance deterioration and damages. Please design appropriate drive level on the circuit.
- 8. Characteristics differences between our measurement and your company's measurement may occur depending on measurement method and conditions. Please check it thoroughly before use.
- 9. Do not place signal lines, power lines, or GND lines in mounting area of the product, its inner layer, or its back side. In order to avoid malfunction due to induction of other signal lines, please do not place signal lines near the product. It may affect product characteristics.
- 10.If there is no margin in negative resistance of the oscillator circuit, the crystal unit may not oscillate or may take a long time to oscillate. Therefore, negative resistance in the oscillator circuit should be at least five times of the crystal unit's equivalent series resistance. Please follow this circuit design rule.
- 11.Aging specifications are estimated value of frequency shift from reliability test results. It does not mean to guarantee product lifecycle.
- 12. If customer wants to use our product contrary to this caution and advice, please use it at your own risk.

<How to check the negative resistance>



1) Insert a pure resistance (r) in series with the crystal oscillator.

2) Adjust (r) to find the maximum (r) value that starts oscillation

3) Look at the value of (r) in the oscillation state of 2).

Negative resistance of the circuit |-R| =

r + crystal unit series resistance value R1

Guideline for negative resistance |-R|:

|-R| > R1 Max. x 5

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At Seiko Epson, all environmental initiatives operate under the Plan-Do-Check-Action (PDCA) cycle designed to achieve continuous improvements. The environmental management system (EMS) operates under the ISO 14001 environmental management standard. All of our major manufacturing and non-manufacturing sites, in

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Explanation of marks used in this datasheet

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IATF 16949 is the international standard that added the sectorspecific supplemental requirements for automotive industry based on ISO9001.

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