



100V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(on) max}	I _D T _C = +25°C
100V	140mΩ @ V _{GS} = 10V	12A
1007	160mΩ @ V _{GS} = 4.5V	11A

Description

This new generation MOSFET has been designed to minimize the onstate resistance (R_{DS(on)}) and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

Applications

- DC-DC Converters
- Power management functions
- Analog Switch

Features

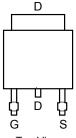
- Low On-Resistance
- Low Input Capacitance
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- **PPAP** Available

Mechanical Data

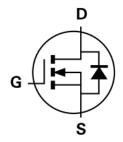
- Case: TO252 (DPAK)
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.33 grams (approximate)







Top View



Internal Schematic

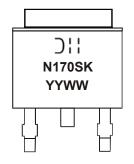
Ordering Information (Note 4 & 5)

ĺ	Part Number	Compliance	Case	Packaging
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	DMN10H170SK3Q-13	Automotive	TO252	2,500/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to http://www.diodes.com/quality/product_grade_definitions/.
- 5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



🕽 🖁 =Manufacturer's Marking N170SK = Product Type Marking Code YYWW = Date Code Marking YY = Last Digit of Year (ex: 14 = 2014) WW = Week Code (01 to 53)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Drain-Source Voltage	V_{DSS}	100	V		
Gate-Source Voltage	V_{GSS}	±20	V		
Continuous Drain Current (Note 6) V _{GS} = 10V	l _D	12 7.5	Α		
Maximum Body Diode Forward Current (Note 6)	Is	4	Α		
Pulsed Drain Current (10μs pulse, duty cycle = 1%)			I _{DM}	16	Α
Avalanche Current (Note 7)			I _{AR}	5.3	Α
Avalanche Energy (Note 7)			E _{AR}	20	mJ

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	_	Symbol	Value	Units
Total Power Dissipation (Note 6)	$T_C = +25^{\circ}C$	D	42	W
Total Power Dissipation (Note 6)	T _C = +100°C	P _D	17	
Thermal Resistance, Junction to Ambient (Note 6)	$R_{\theta JA}$	44	°C/W	
Thermal Resistance, Junction to Case (Note 6)		R _{0JC}	3	C/VV
Operating and Storage Temperature Range		$T_{J_i} T_{STG}$	-55 to +150	°C

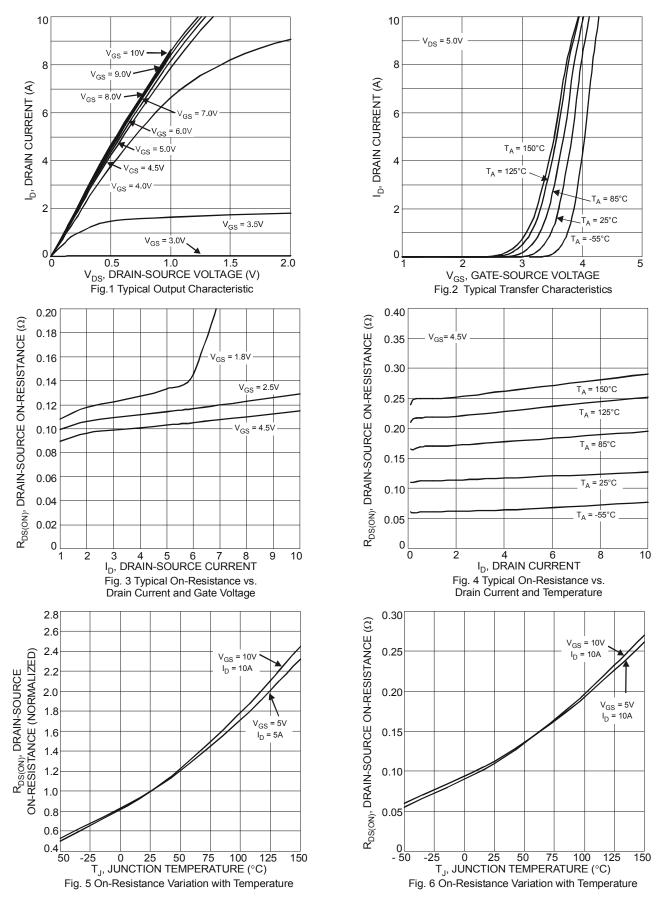
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV _{DSS}	100	_	_	V	V _{GS} = 0V, I _D = 250μA
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1	μΑ	V _{DS} = 100V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	_	_	100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)					_	
Gate Threshold Voltage	V _{GS(th)}	1.0	_	3.0	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$
Static Drain-Source On-Resistance		_	99	140	0	$V_{GS} = 10V, I_D = 5A$
Static Diain-Source On-Resistance	R _{DS (ON)}	_	104	160	mΩ	$V_{GS} = 4.5V, I_D = 5A$
Diode Forward Voltage	V_{SD}	_	0.7	1.0	V	V _{GS} = 0V, I _S = 10A
DYNAMIC CHARACTERISTICS (Note 9)			<u>.</u>			
Input Capacitance	C _{iss}	_	1167	_		V _{DS} = 25V, V _{GS} = 0V, f = 1.0MHz
Output Capacitance	Coss	_	36	_	pF	
Reverse Transfer Capacitance	C _{rss}	_	25	_		
Gate Resistance	R _G	_	1.3	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$
Total Gate Charge (V _{GS} = 4.5V)	Q_g	_	4.9	_		
Total Gate Charge (V _{GS} = 10V)	Q_g	_	9.7	_	nC	V _{DS} = 80V, I _D = 12.8A
Gate-Source Charge	Q_{gs}	_	2.0	_	IIC	
Gate-Drain Charge	Q_{gd}	_	2.0	_		
Turn-On Delay Time	t _{D(on)}	_	10.5	_		
Turn-On Rise Time	t _r	_	11.1	_	no	V_{DD} = 50V, R_G = 25 Ω , I_D = 12.8A
Turn-Off Delay Time	t _{D(off)}	_	42.6	_	ns	
Turn-Off Fall Time	t _f	_	12.8	_		
Body Diode Reverse Recovery Time	t _{rr}	_	30.3	_	ns	$V_{GS} = 0V$, $I_S = 12.8A$, $dI/dt = 100A/\mu s$
Body Diode Reverse Recovery Charge	Qrr	_	35.2	_	nC	$V_{GS} = 0V$, $I_{S} = 12.8A$, $dI/dt = 100A/\mu s$

Notes:

- 6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout.
- 7. UIS in production with L = 1.43mH, TJ = +25°C.
- 8. Short duration pulse test used to minimize self-heating effect
- 9. Guaranteed by design; not subject to production testing







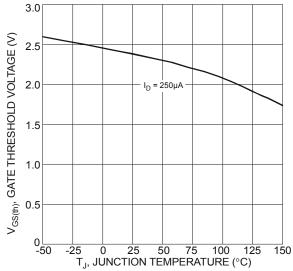


Fig. 7 Gate Threshold Variation vs. Ambient Temperature

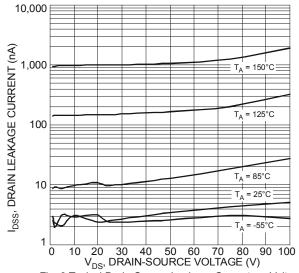
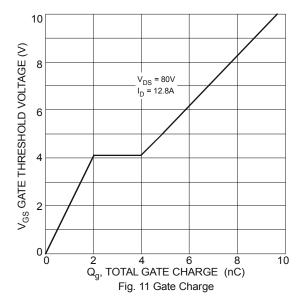
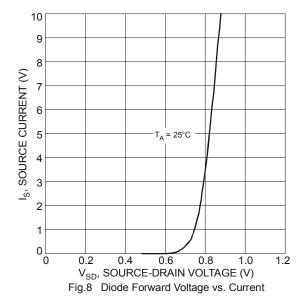
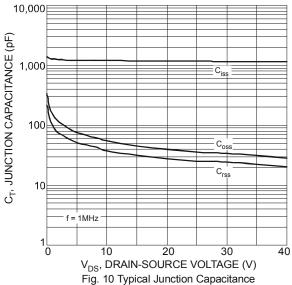


Fig. 9 Typical Drain-Source Leakage Current vs. Voltage



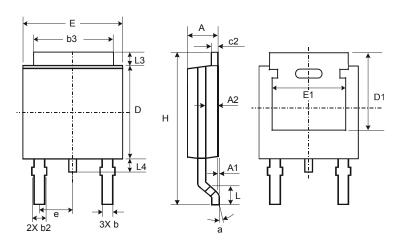






Package Outline Dimensions

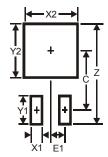
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



TO252					
Dim Min		Max	Тур		
Α	2.19	2.39	2.29		
A1	0.00	0.13	0.08		
A2	0.97	1.17	1.07		
b	0.64	0.88	0.783		
b2	0.76	1.14	0.95		
b3	5.21	5.46	5.33		
c2	0.45	0.58	0.531		
D	6.00	6.20	6.10		
D1	5.21 –		_		
е –		_	2.286		
Ε	6.45 6.70 6		6.58		
E1	4.32	_	-		
Н	9.40	10.41	9.91		
L	1.40	1.78	1.59		
L3	L3 0.88		1.08		
L4	0.64	1.02	0.83		
а	0°	10°	_		
All Dimensions in mm					

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)			
Z	11.6			
X1	1.5			
X2	7.0			
Y1	2.5			
Y2	7.0			
C	6.9			
E1	2.3			



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