

XP233N0501TR-G

N-channel MOSFET 30V, 0.5A

ETR11043-001

■ FEATURES

On-State Resistance : $R_{DS}(on)=1.5\Omega$ @ $V_{GS}=4.5V$

Driving Voltage : 2.5V

Environmentally Friendly : EU RoHS Compliant, Pb Free

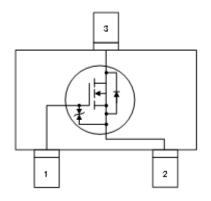
■ APPLICATIONS

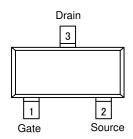
Switching

■EQUIVALENT CIRCUIT

■ PIN CONFIGURATION

●SOT-23(TO-236)





■PRODUCT NAME

PRODUCT NAME	PACKAGE	ORDER UNIT
XP233N0501TR-G *	SOT-23(TO-236)	3,000 pcs/ Reel

^{*} The "-G" suffix denotes Halogen and Antimony free as well as being fully EU RoHS compliant

■ ABSOLUTE MAXIMUM RATINGS

Ta=25°C

PARAMETER	SYMBOL RATINGS		UNITS	
Drain-Source Voltage	V_{DSS}	30	V	
Gate-Source Voltage	V _{GSS}	±20	V	
Drain Current (DC)	ID	0.5	Α	
Drain Current(Pulse) (*1)	I _{DP}	1	Α	
Channel Power Dissipation (*2)	P _d	0.4	W	
Junction Temperature	TJ	150	°C	
Storage Temperature	T _{stg}	-55~+150	°C	

 $^{^{(*1)}}$ PW \leq 10 μ s,duty cycle \leq 1%

^(*2)When implemented on a PCB defined by JESD51-7

XP233N0501TR-G

■ELECTRICAL CHARACTERISTICS

Ta=25°C

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Drain-Source Breakdown Voltage	V _{(BR)DSS}	I _D = 250μA, V _{GS} = 0V	30	-	-	V
Drain-Source Leakage Current	I _{DSS}	V_{DS} = 30V, V_{GS} = 0V	-	-	1	μΑ
Gate-Source Leakage Current	I _{GSS}	$V_{GS} = \pm 15V, V_{DS} = 0V$	-	-	±10	μΑ
Gate Threshold Voltage	$V_{GS(off)}$	I _D = 250uA, V _{DS} = V _{GS}	0.9	1.3	1.7	V
Drain-Source On Resistance	В	V _{GS} = 4.5V, I _D = 100mA	-	0.6	1.5	Ω
	R _{DS(on)}	V _{GS} = 2.5V, I _D = 100mA	-	1.5	4	Ω
Input Capacitance	Ciss	10)/)/ 0)/	-	40	-	pF
Output Capacitance	Coss	V_{DS} = 10V, V_{GS} = 0V f= 1MHz	-	12	-	pF
Reverse Transfer Capacitance	Crss	1 I= 11VIIIZ	-	6	-	pF
Turn-on Delay Time	t _{d(on)}		-	7	-	ns
Rise Time	t _r	V _{DD} = 10V, I _D = 200mA	-	5	-	ns
Turn-off Delay Time	$t_{d(off)}$	V _{GS} = 10V	-	30	-	ns
Fall Time	t _f		-	8	-	ns
Total Gate Charge	Q_g	V 40V I 050 A	-	0.78	-	nC
Gate-Source Charge	Q_{gs}	V _{DS} = 10V, I _D = 250mA	-	0.1	-	nC
Gate-Drain Charge	Q_{gd}	V _{GS} = 10V	-	0.16	-	nC
Diode Forward Voltage	V_{SD}	I _S = 100mA, V _{GS} = 0V	-	0.7	1.1	V

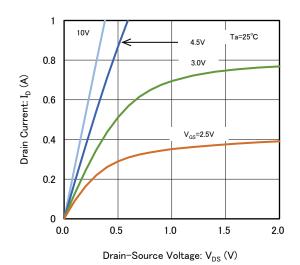
■NOTES ON USE

- 1. Please use this IC within the absolute maximum ratings.

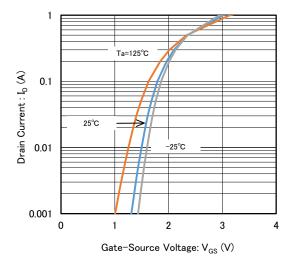
 Even within the ratings, in case of high load use continuously such as high temperature, high voltage, high current and thermal stress may cause reliability degradation of the IC.
- Torex places an importance on improving our products and their reliability.
 We request that users incorporate fail-safe designs and post-aging protection treatment when using Torex products in their systems.

■TYPICAL PERFORMANCE CHARACTERISTICS

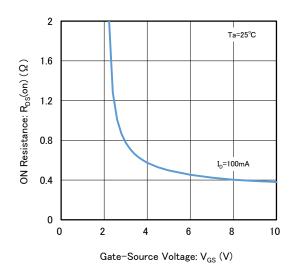
(1) Drain Current vs. Drain-Source Voltage



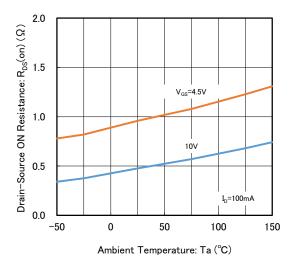
(2) Drain Current vs. Gate-Source Voltage



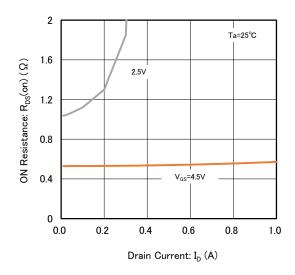
(3) Drain-Source On Resistance vs. Gate-Source Voltage



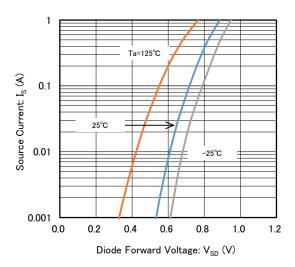
(4) Drain-Source On Resistance vs. Ambient Temperature



(5) Drain-Source On Resistance vs. Drain Current



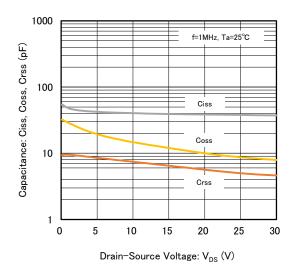
(6) Source Current vs. Diode Forward Voltage



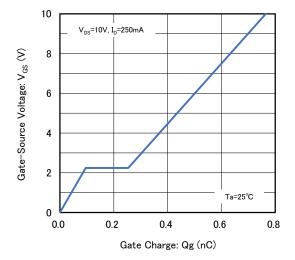
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■TYPICAL PERFORMANCE CHARACTERISTICS

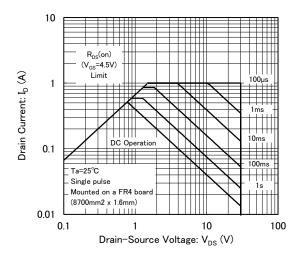
(7) Ciss, Coss, Crss vs. Drain-Source Voltage



(8) Gate-Source Voltage vs. Gate Charge



(9) Area of Safe Operation



■PACKAGING INFORMATION

For the latest package information go to, www.torexsemi.com/technical-support/packages

PACKAGE	OUTLINE / LAND PATTERN	THERMAL CHARACTERISTICS	
SOT-23(TO-236)	SOT-23(TO-236) PKG	JESD51-7 Board	SOT-23(TO-236) PowerDissipation

■MARKING RULE

●SOT-23(TO-236)

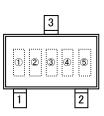
① ②, ③represents product series

	MARK		PRODUCT SERIES
1	2	3	PRODUCT SERIES
3	3	N	XP233N0501**-G

④, ⑤ represents production lot number 01 to 09, 0A to 0Z, 11 to 9Z, A1 to A9, AA to AZ, B1 to ZZ repeated (G, I, J, O, Q, W excluded)

*No character inversion used

SOT-23(TO-236)



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