



N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS} (@ T _J Max)	R _{DS(ON)}	I _D T _C = +25°C
650V	$2.5\Omega @V_{GS} = 10V$	4.5A

Description

This new generation MOSFET features low on-resistance and fast switching, making it ideal for high efficiency power management applications.

Applications

- Motor Control
- Backlighting
- DC-DC Converters
- Power Management Functions

Features

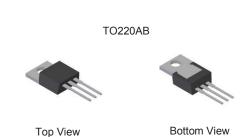
- Low Input Capacitance
- High BV_{DSS} Rating for Power Application
- Low Input/Output Leakage
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

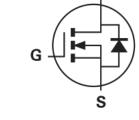
Mechanical Data

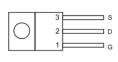
Case: TO220AB

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- Case Material: Molded Plastic, "Green" Molding Compound, UL Flammability Classification Rating 94V-0
- Terminals: Matte Tin Finish Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 [®]
- Terminal Connections: See Diagram Below
- Weight: TO220AB 1.85 grams (Approximate)







Equivalent Circuit

Top View Pin Out Configuration

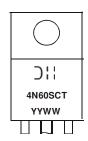
Ordering Information (Note 4)

Part Number	Case	Packaging	
DMG4N60SCT	TO220AB	50 pieces/tube	

Notes:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



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Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	600	V
Gate-Source Voltage			V _{GSS}	±30	V
Continuous Drain Current (Note 5) V _{GS} = 10V	Steady State	T_{C} = +25°C T_{C} = +100°C	I _D	4.5 3	А
Maximum Body Diode Forward Current (Note 5)		I _S	6	Α	
Pulsed Drain Current (10μs pulse, duty cycle = 1%)			I _{DM}	6	Α
Avalanche Current, L = 60mH (Note 6)			I _{AS}	1.7	Α
Avalanche Energy, L = 60mH (Note 6)			E _{AS}	90	mJ

Thermal Characteristics

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)	T _C = +25°C	D	113	- W
Total Power Dissipation (Note 5)	T _C = +100°C	P _D	45	
Thermal Resistance, Junction to Ambient (Note 5)	$R_{\theta JA}$	58	°C/W	
Thermal Resistance, Junction to Case (Note 5)	$R_{ heta JC}$	1.1	C/VV	
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C

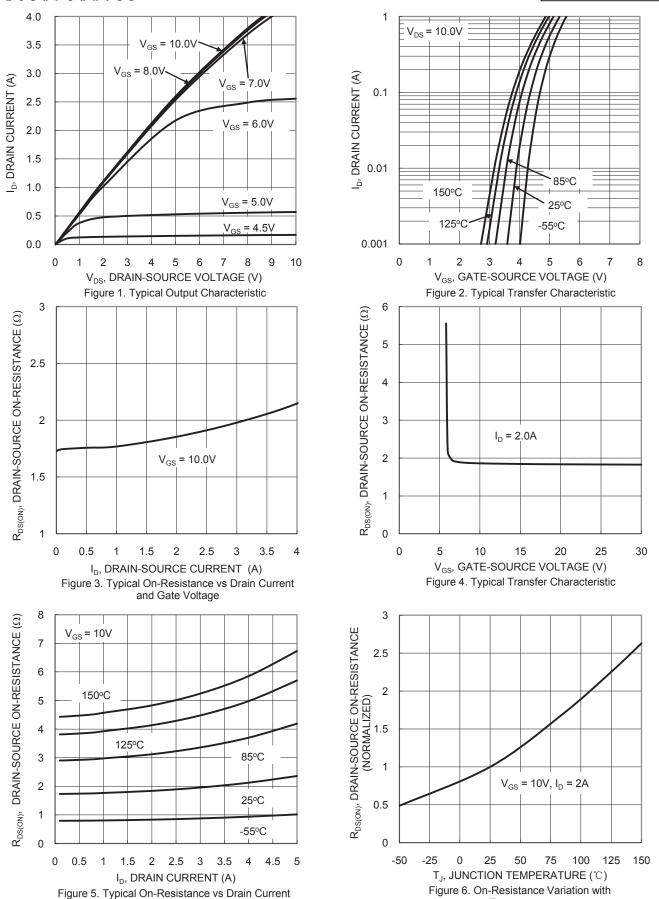
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	600	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}			1	μΑ	V _{DS} = 600V, V _{GS} = 0V	
Gate-Source Leakage	I _{GSS}		_	100	nA	$V_{GS} = \pm 30V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(TH)}	2.5	_	4.5	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Static Drain-Source On-Resistance	R _{DS(ON)}		2.0	2.5	Ω	$V_{GS} = 10V, I_D = 2A$	
Diode Forward Voltage	V _{SD}	_	_	1.4	V	V _{GS} = 0V, I _S = 1A	
DYNAMIC CHARACTERISTICS (Note 6)							
Input Capacitance	C _{iss}		532	_		V _{DS} = 25V, f = 1.0MHz, V _{GS} = 0	
Output Capacitance	Coss		47	_	pF		
Reverse Transfer Capacitance	Crss	_	4	_			
Gate Resistance	R _G		3.3	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1.0MHz$	
Total Gate Charge (V _{GS} = 10V)	Qg		14.3	_		\/ - 400\/ L - 4A	
Gate-Source Charge	Q_{gs}	_	3.3	_	nC	$V_{DD} = 480V, I_D = 4A,$ $V_{GS} = 10V$	
Gate-Drain Charge	Q_{gd}	_	6.9	_			
Turn-On Delay Time	t _{D(ON)}	_	14	_			
Turn-On Rise Time	t _R	_	34	_	no	V_{DD} = 300V, R_{G} = 25 Ω , I_{D} = 4A, V_{GS} = 10V	
Turn-Off Delay Time	t _{D(OFF)}	_	32	_	ns		
Turn-Off Fall Time	t _F	_	25	_			
Body Diode Reverse Recovery Time	t _{RR}	_	229	_	ns	dI/dt = 100A/µs, V _{DS} = 100V,	
Body Diode Reverse Recovery Charge	Q _{RR}		1564	_	nC	I _F = 4A	

- 5. Device mounted on an infinite heatsink.
- Guaranteed by design. Not subject to production testing.
 Short duration pulse test used to minimize self-heating effect.





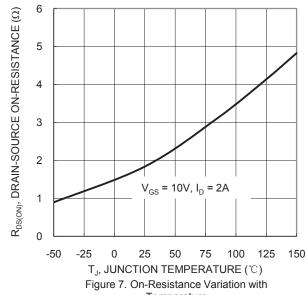


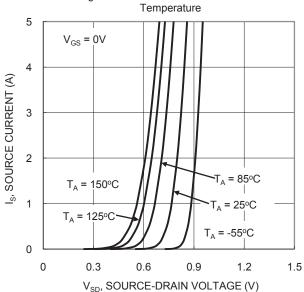
and Temperature

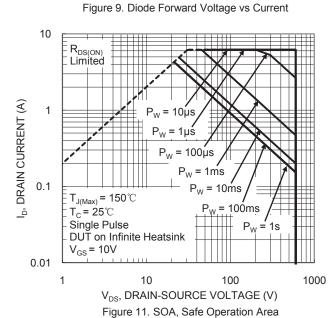
Temperature

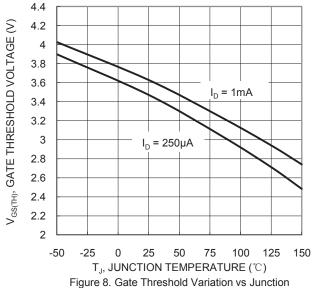












Temperature

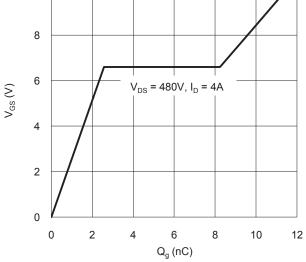


Figure 10. Gate Charge



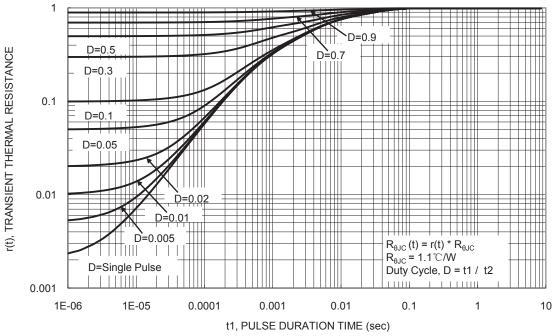
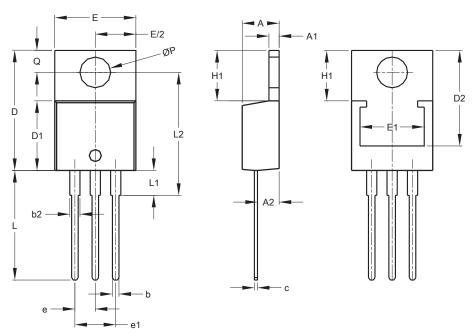


Figure 12. Transient Thermal Resistance

Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

TO220AB



TO220AB						
Dim	Min	Max	Тур			
Α	3.56	4.82	-			
A1	0.51	1.39	-			
A2	2.04	2.92	-			
b	0.39	1.01	0.81			
b2	1.15	1.77	1.24			
С	0.356	0.61	ı			
D	14.22	16.51	-			
D1	8.39	9.01	ı			
D2	11.45	12.87	-			
е	ı	ı	2.54			
e1	ı	ı	5.08			
Е	9.66	10.66	ı			
E1	6.86	8.89	-			
H1	5.85	6.85	ı			
L	12.70	14.73	ı			
L1	-	6.35	-			
L2	15.80	16.20	16.00			
Р	3.54	4.08	-			
Q	2.54	3.42	-			
All Dimensions in mm						

TOSSOAD



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