VS-FC420SA15

Vishay Semiconductors



SOT-227 Power Module Single Switch - Power MOSFET, 400 A



PRIMARY CHARACTERISTICS				
V _{DSS} 150 V				
R _{DS(on)} at 200 A	1.93 mΩ			
Ι _D	300 A at 90 °C			
Туре	Modules - MOSFET			
Package	SOT-227			

FEATURES

- I_D = 400 A, T_C = 25 °C
- ThunderFET Power MOSFET
- Excellent gate charge x R_{DS(on)} product (FOM)
- · Reduced switching and conduction losses
- Ultra low gate charge (Q_q)
- Maximum 175 °C junction temperature
- UL approved file E78996
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

- DC/DC conversions
- Motor drives
- DC/AC inverter
- Power supplies
- Uninterruptible power supplies
- AC/DC switch-mode power supplies

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
MOSFET					
Drain to source voltage	V _{DSS}		150	V	
Continuous dusin surrent V 10.V	1	T _C = 25 °C	400		
Continuous drain current, $V_{GS at} 10 V$	ID	T _C = 90 °C	300	А	
Pulsed drain current	I _{DM} ⁽¹⁾		860		
Power dissipation	PD	T _C = 25 °C	909	W	
Gate to source voltage	V _{GS}		± 20	V	
Single pulse avalanche current	E _{AS}		720	J	
Avalanche current	I _{AS}	T_{C} = 25 °C, L = 10 mH, V_{GS} = 10 V	120	А	
MODULE	•		· · ·		
Operating junction temperature range	TJ		-55 to +175	- °C	
Operating storage temperature range	T _{Stg}		-40 to +150		
Insulation voltage (RMS)	VISOL	any terminal to case, t = 1 min	2500	V	

Note

⁽¹⁾ Limited at max. junction temperature

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COMPLIANT



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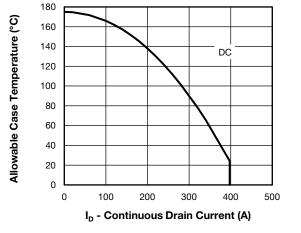
THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Operating junction temperature range		TJ		-55	-	175	°C
Operating storage temperature range		T _{Stg}		-40	-	150	U
Junction to case	MOSFET	R _{thJC}		-	-	0.165	°C/W
Case to heatsink	Module	R _{thCS}	Flat, greased surface	-	0.1	-	C/W
Weight				-	30	-	g
Mounting torque			Torque to terminal	-	-	1.1 (9.7)	Nm (lbf. in)
			Torque to heatsink	-	-	1.3 (11.5)	Nm (lbf. in)
Case style					SOT-227		

ELECTRICAL CHARACTERISTICS ($T_J = 25 \text{ °C}$ unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Drain to source breakdown voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, \text{ I}_{D} = 500 \mu\text{A}$	150	-	-	V
Breakdown voltage temperature coefficient	$\Delta V_{(BR)DSS} / \Delta T_J$	Reference to 25 °C, $I_D = 1.0$ mA	-	9.0	-	mV/°C
Static drain to source on-resistance	R _{DS(on)}	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 200 \text{ A}$	-	1.93	2.75	mΩ
Gate threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 1.0 \text{ mA}$	1.80	3.46	5.4	V
Temperature coefficient of threshold voltage	$\Delta V_{GE(th)} / \Delta T_J$	V _{DS} = V _{GS} , I _D = 1.0 mA (25 °C to 125 °C)	-	9.6	-	mV/°C
Forward transconductance	g _{fs}	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 100 \text{ A}, \text{ V}_{GS} = 10 \text{ V}$	-	200	-	S
Drain to source leakage current	I _{DSS}	$V_{DS} = 150 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	-	0.5	10.0	μA
		$V_{DS} = 150 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 150 ^{\circ}\text{C}$	-	19	-	
Gate to source leakage	I _{GSS}	$V_{GS} = \pm 20 V$	-	-	± 200	nA
Total gate charge	Qg	I _D = 250 A	-	250	-	
Gate to source charge	Q _{gs}	$V_{DS} = 75 V$	-	79	-	nC
Gate to drain ("Miller") charge	$V_{GS} = 10 V$		-	82	-	
Turn-on delay time	$\begin{array}{c c} t_{d(on)} & V_{DD} = 75 \text{ V} \\ \hline t_r & I_D = 100 \text{ A} \\ \hline t_{d(off)} & R_g = 1 \Omega \end{array}$		-	139	-	
Rise time			-	285	-	
Turn-off delay time			-	120	-	ns
Fall time	t _f	$V_{GS} = 10 V$		142	-	
Input capacitance	$\begin{tabular}{c c c c c c } \hline C_{iss} & V_{GS} = 0 \ V \\ \hline C_{oss} & V_{DS} = 25 \ V \\ \hline C_{rss} & f = 1 \ MHz \end{tabular}$		-	13.7	-	
Output capacitance			-	2.2	-	nF
Reverse transfer capacitance			-	0.104	-	1

SOURCE-DRAIN RATINGS AND CHARACTERISTICS (T _J = 25 $^{\circ}$ C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Continuous source current (body diode)	I _S		-	-	476	
Pulsed source current (body diode)	I _{SM}	MOSFET symbol showing the integral reverse p-n junction diode	-	-	850	A
Diode forward voltage	V _{SD}	$I_{\rm S} = 250$ A, $V_{\rm GS} = 0$ V	-	0.95	-	V
Reverse recovery time	t _{rr}		-	171	-	ns
Reverse recovery charge	Q _{rr}	T _J = 25 °C, I _F = I _S = 50 A, dl/dt = 100 A/µs, V _B = 50 V	-	1032	-	nC
Reverse recovery current	I _{RM}			-	А	

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Fig. 1 - Maximum Continuous Drain Current vs. Case Temperature

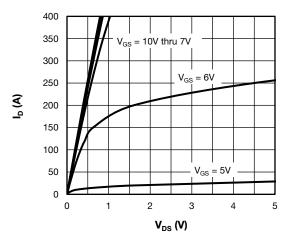


Fig. 2 - Typical Drain to Source Current Output Characteristics at T_J = 25 $^\circ C$

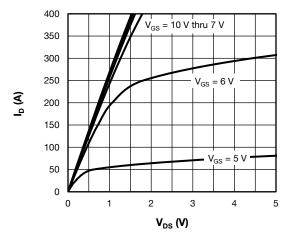


Fig. 3 - Typical Drain to Source Current Output Characteristics at T_J = 125 $^\circ\text{C}$

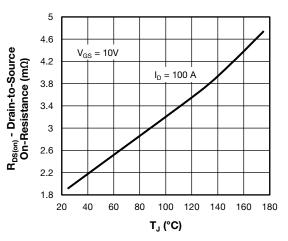


Fig. 4 - Typical Drain-to-Source On-Resistance vs. Temperature

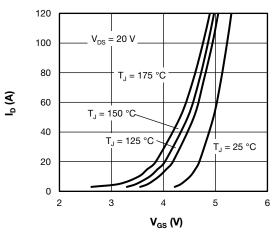


Fig. 5 - Typical Transfer Characteristics

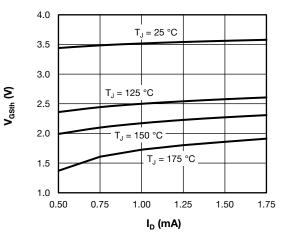


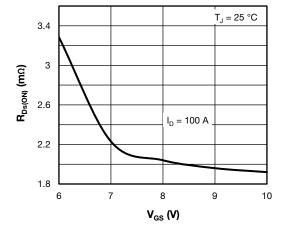
Fig. 6 - Typical Gate Threshold Voltage Characteristics

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Fig. 7 - Typical Drain - State Resistance vs. Gate to Source Voltage

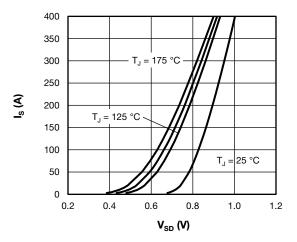


Fig. 8 - Typical Body Diode Source-to-Drain Current Characteristics

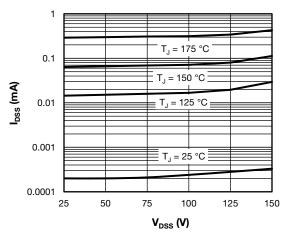


Fig. 9 - Typical Zero Gate Voltage Drain Current

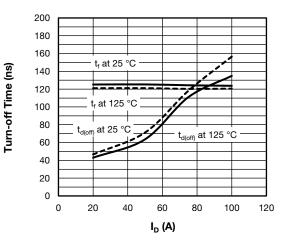
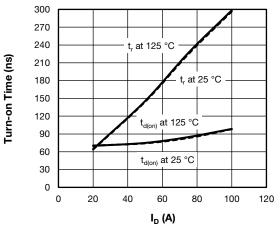


Fig. 10 - Typical Turn-off Switching Time vs. I_D





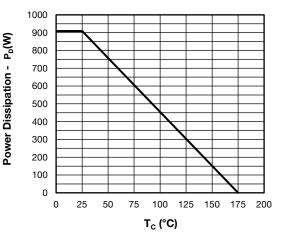


Fig. 12 - Power Dissipation Curve

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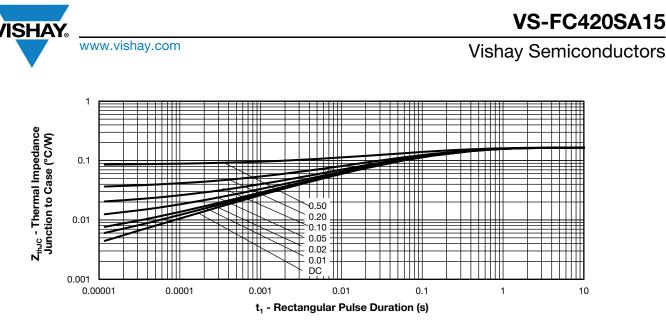


Fig. 13 - Maximum Thermal Impedance Junction-to-Case Characteristics

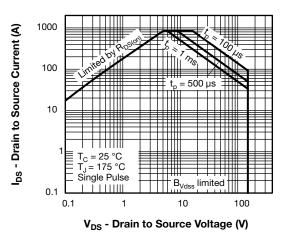


Fig. 14 - Safe Operating Area

ORDERING INFORMATION TABLE

Device code vs-F С 420 S Α 15 2 3 (6) (7)1 (4 5 Vishay Semiconductors product 1 2 MOSFET module MOSFET die generation 3 4 Current rating (420 = 420 A) 5 Circuit configuration (S = single switch)

- 6 Package indicator (SOT-227)
- **7** Voltage rating (15 = 150 V)



CIRCUIT CONFIGURATION				
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING		
Single switch	S	$\begin{array}{c} \begin{array}{c} & & \\ & & \\ & \\ & \\ & \\ & \\ & \\ & \\ & $		

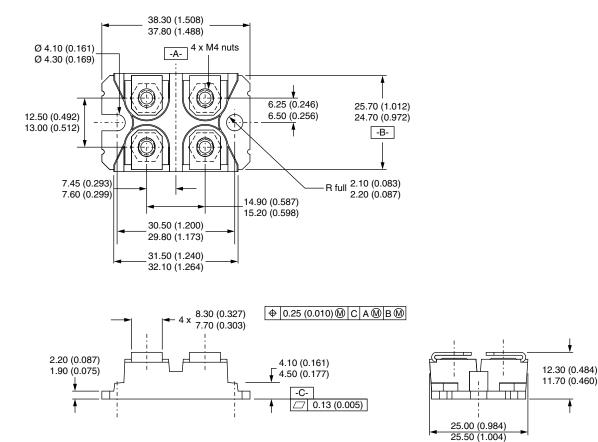


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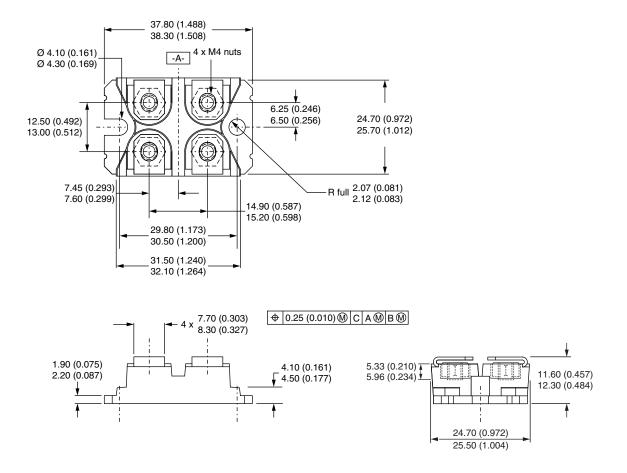
DIMENSIONS in millimeters





SOT-227 Generation 2

DIMENSIONS in millimeters (inches)



Note

• Controlling dimension: millimeter



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