

## PVI1050NPbF PVI1050NS/TPbF PVI5050NPbF PVI5050NSPbF

Photovoltaic Isolator Single and Dual Channel 5-10 Volt Output

### **General Description**

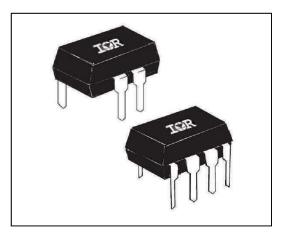
The PVI Series Photovoltaic Isolator generates an electrically isolated DC voltage upon receipt of a DC input signal. It is capable of directly driving gates of power MOSFETs or IGBTs. It utilizes a monolithic integrated circuit photovoltaic generator of novel construction as its output. The output is controlled by radiation from a GaAlAs light emitting diode (LED), which is optically isolated from the photovoltaic generator.

The PVI Series is ideally suited for applications requiring high-current and/or high-voltage switching with optical isolation between the low-level driving circuitry and high-energy or high-voltage load circuits. It can be used for directly driving gates of power MOSFETs. The dual-channel device allows its outputs to drive independent discrete power MOSFETs, or be connected in parallel or in series to provide higher current drive for power MOSFETs or higher voltage drive for IGBTs. The PVI Series Photovoltaic isolators employ fast turn-off circuitry.

These PVI Series Photovoltaic Isolators are packaged in 8-pin, molded DIP packages and available with either thru-hole or surface-mount ("gull-wing") leads, in plastic shipping tubes.

### Features

- Isolated Voltage Source
- Monolithic Construction
- Up to 5µA Output
- Single or Dual Output
- Solid-State Reliability



### Part Identification

PVI1050NPbF PVI5050NPbF	thru-hole thru-hole
PVI1050NSPbF	Surface-mount (gull-wing)
PVI5050NSPbF	(gull-wing) Surface-mount (gull-wing)
PVI1050NS-TPbF	Surface-mount, tape and reel

#### Applications

- Load Distribution
- Industrial Controls
- Current-to-Voltage Conversion
- Custom Solid-State Relay



**Electrical Specifications** (-40°C  $\leq$  T<sub>A</sub>  $\leq$  +85°C unless otherwise specified)

INPUT CHARACTERISTICS	Limits	Units
Input Current Range (see figure 4)	2.0 to 50	mA <sub>(DC)</sub>
Maximum Forward Voltage Drop @ 10mA, 25°C (see figure 5)	1.4	V (DC)
Maximum Reverse Voltage	6.0	V (DC)
Maximum Reverse Current @ -6.0V (DC), 25°C	100	μA <sub>(DC)</sub>
Maximum Pulsed Input Current @ 25°C (see figure 6)	1.0	A <sub>(peak)</sub>

OUTPUT CHARACTERISTICS	Limits	Units
Maximum Forward Voltage @ 10µA	8.0 per channel	V <sub>(DC)</sub>
Maximum Reverse Current @ -10V <sub>DC</sub>	10	μA <sub>(DC)</sub>

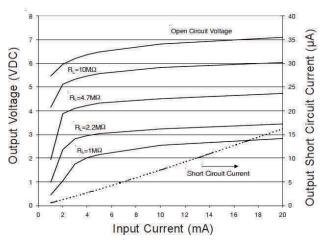
COUPLED CHARACTERISTICS	Limits PVI5050N	Limits PVI1050N	Units
Minimum Open Circuit Voltage @ ILED = 10mA, 25°C, RL = >10M $\Omega$ (see figures 1 to 2)	5.0	5.0/channel 10 series	V <sub>(DC)</sub>
Minimum Short Circuit Current @ ILED = 10mA, 25°C (see figures 1 to 2)	5.0	5.0/channel 10 series	$\mu A_{(DC)}$
Maximum Capacitance (Input/Output)	1.0	2.0	pF
Maximum Ton Time @ ILED=10mA, CLOAD=10pF (See Figure7) RL > 20M $\Omega$	300		μS
RL=10ΜΩ	16	0	μS
<b>RL=4.7M</b> Ω	90	)	μS
Maximum Toff Time @ ILED=10mA, CLOAD=10pF (See Figure7)	22	0	μS

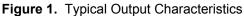
GENERAL CHARACTERISTICS		Limits PVI5050N	Limits PVI1050N	Units
Minimum Dielectric Strength, Input-Output		4000	2500	V <sub>RMS</sub>
Minimum Dielectric Strength, Output-to-Output		120	1200	
Minimum Insulation Resistance, Input-to-Output, @ $T_A$ =+25°C, 50%RH, 100V <sub>DC</sub>		10	10 <sup>12</sup>	
Maximum Pin Soldering Temperature (10 seconds maximum)		+26	50	
Ambient Temperature Range:	Operating	-40 to	o 85	°C
	Storage	-40 to	125	

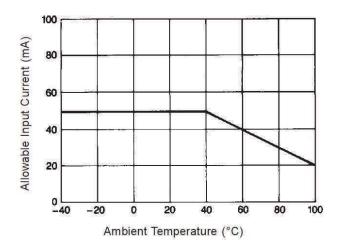
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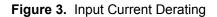


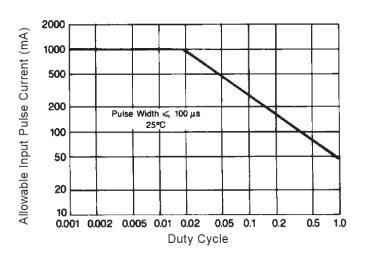














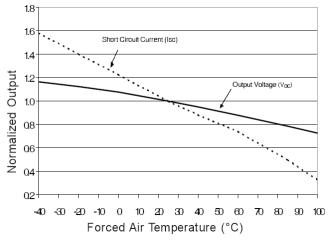


Figure 2. Typical Variation of Output

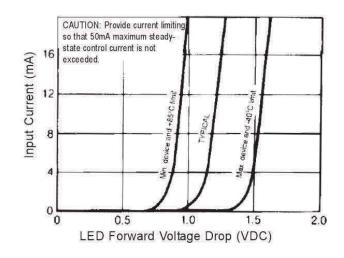


Figure 4. Input Characteristics

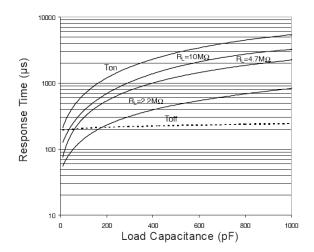
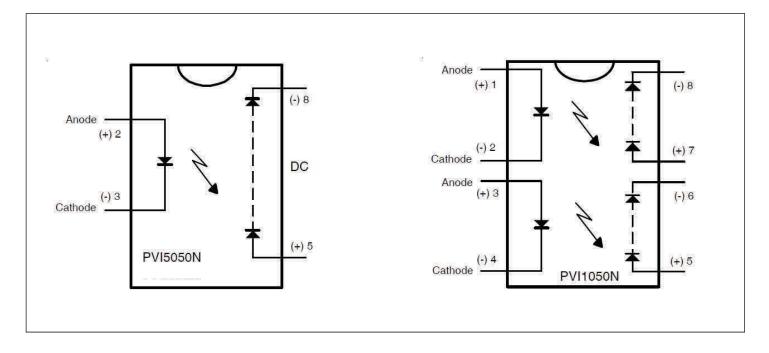


Figure 6. Typical Response Time



## Wiring Diagram

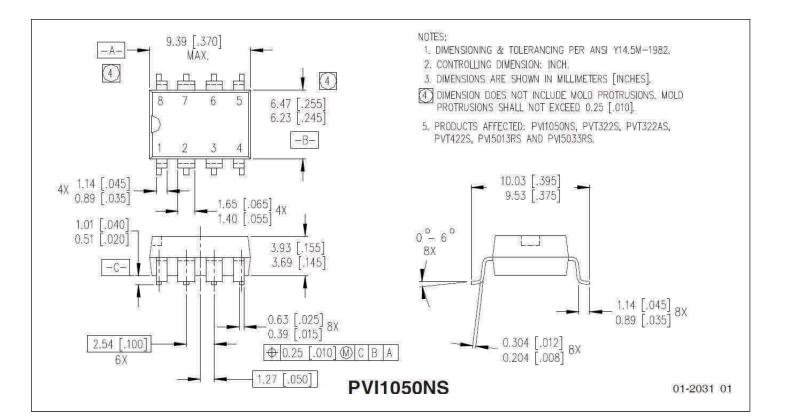


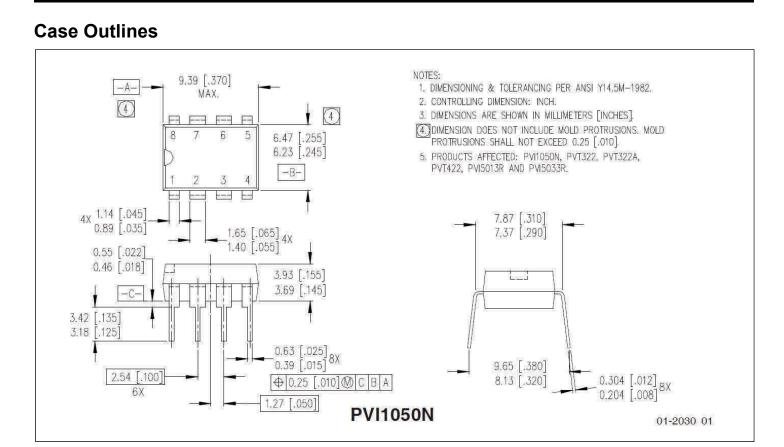
### **Application Note:**

The outputs of the PVI1050N (pins 5-6 and 7-8) may be placed in series connection to produce a 10-volt output with a  $5\mu$ A minimum short circuit current. Alternatively, the two ouptut of the PVI1050 may be connected in parallel to produce a 5.0-volt ouput with a  $10\mu$ A minimum short circuit current.

The two outputs of the PVI1050N may be applied separately with a maximum 1200VDC between the outputs. Input-tooutput isolation to either output is 2500V (RMS).

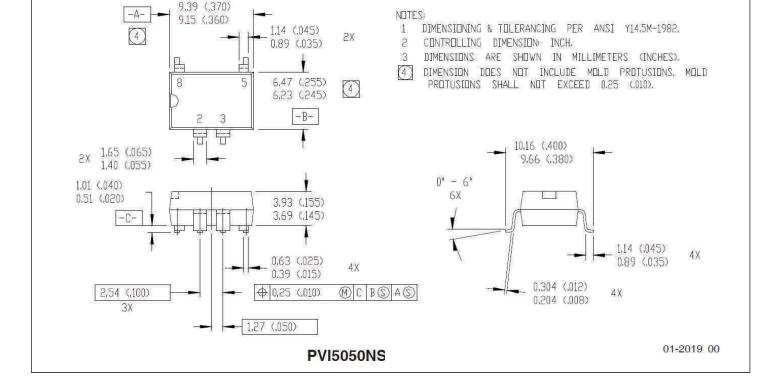


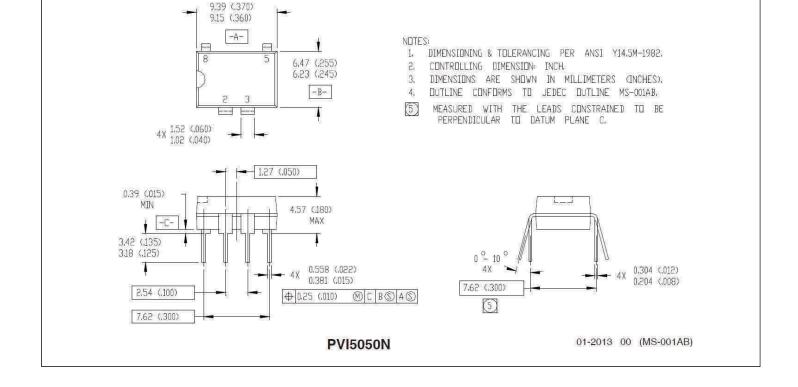




# PVI1050NPbF/PVI1050NS/TPbF PVI5050NPbF/PVI5050NSPbF







# Case Outlines





### **Qualifiction Information**

Qualification Level	Industrial (per JEDEC JESD47F <sup>†</sup> guidelines)	
Moisture Sensitivity Level	PVI1050NPbF	N/A
	PVI5050NPbF	N/A
	PVI1050NSPbF	MSL4
	PVI5050NSPbF	WICE+
	PVI1050NS-TPbF	(per JEDEC J-STD-020E & JEDEC J-STD-033C) $^{\dagger}$
RoHS Compliant	Yes	

† Applicable version of JEDEC standard at the time of product release.



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