Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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MOS FIELD EFFECT TRANSISTOR

μ PA672T

(G2)

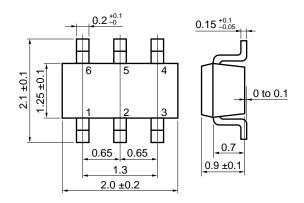
N-CHANNEL MOS FET ARRAY FOR SWITCHING

The μ PA672T is a super-mini-mold device provided with two MOS FET elements. It achieves high-density mounting and saves mounting costs.

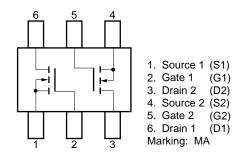
FEATURES

- · Two MOS FET circuits in package the same size as
- · Automatic mounting supported

PACKAGE DIMENSIONS (in millimeters)



PIN CONNECTION



ABSOLUTE MAXIMUM RATINGS $(T_A = 25 \degree C)$

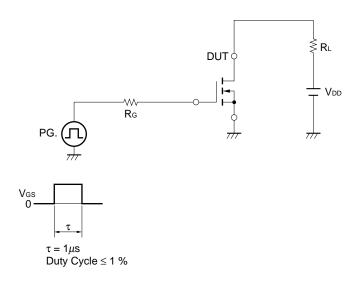
PARAMETER	SYMBOL	TEST CONDITIONS	RATINGS	UNIT
Drain to Source Voltage	VDSS		50	V
Gate to Source Voltage	Vgss		±7.0	V
Drain Current (DC)	I _{D(DC)}		100	mA
Drain Current (pulse)	D(pulse)	PW ≤ 10 ms, Duty Cycle ≤ 50 %	200	mA
Total Power Dissipation	Рт		200 (Total)	mW
Channel Temperature	Tch		150	°C
Storage Temperature	T _{stg}		-55 to +150	°C

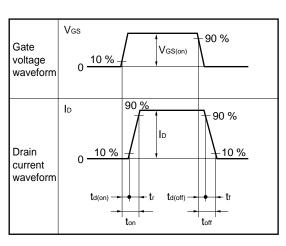


ELECTRICAL CHARACTERISTICS (TA = 25 °C)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Drain Cut-off Current	Ipss	V _{DS} = 50 V, V _{GS} = 0			10	μΑ
Gate Leakage Current	Igss	$V_{GS} = \pm 7.0 \text{ V}, V_{DS} = 0$			±5.0	μΑ
Gate Cut-off Voltage	V _{GS(off)}	$V_{DS} = 3.0 \text{ V}, \text{ ID} = 1.0 \ \mu\text{A}$	0.7	1.0	1.5	V
Forward Transfer Admittance	y _{fs}	V _{DS} = 3.0 V, I _D = 10 mA	20			mS
Drain to Source On-State Resistance	RDS(on)1	V _G S = 2.5 V, I _D = 10 mA		20	40	Ω
Drain to Source On-State Resistance	RDS(on)2	V _G S = 4.0 V, I _D = 10 mA		15	20	Ω
Input Capacitance	Ciss	V _{DS} = 3.0 V, V _{GS} = 0, f = 1.0 MHz		6		pF
Output Capacitance	Coss			8		pF
Reverse Transfer Capacitance	Crss			1.2		pF
Turn-On Delay Time	td(on)	$V_{DD} = 3 \text{ V, ID} = 20 \text{ mA, V}_{GS(on)} = 3 \text{ V,}$ $R_G = 10 \Omega, \text{ RL} = 120 \Omega$		9		ns
Rise Time	tr			50		ns
Turn-Off Delay Time	td(off)			20		ns
Fall Time	t f			40		ns

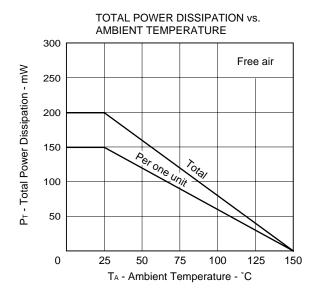
SWITCHING TIME MEASUREMENT CIRCUIT AND CONDITIONS

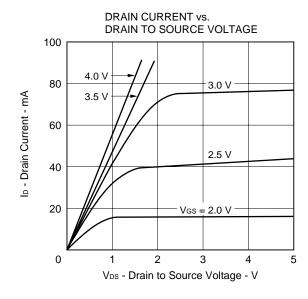


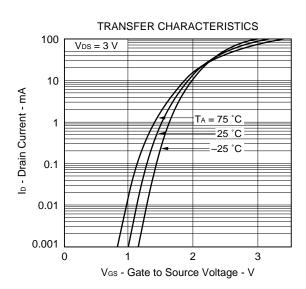


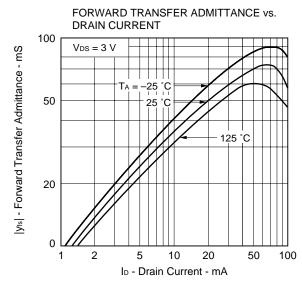


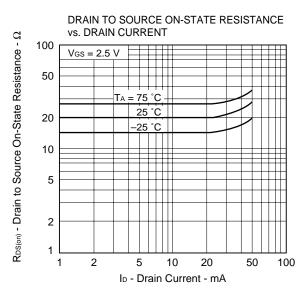
TYPICAL CHARACTERISTICS (TA = 25 °C)

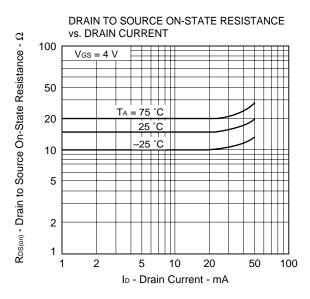




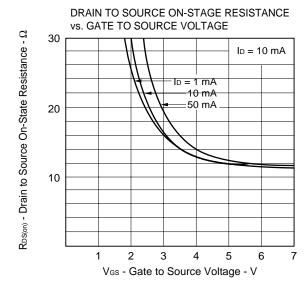


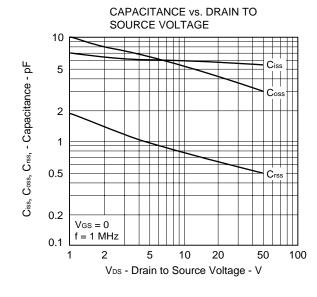


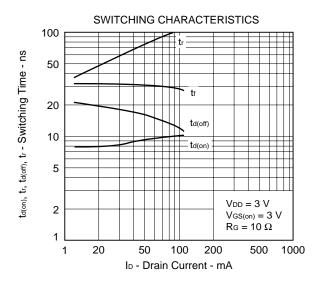


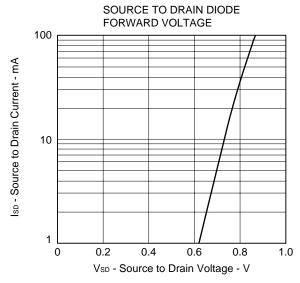














REFERENCE

Document Name	Document No.		
NEC semiconductor device reliability/quality control system	TEI-1202		
Quality grade on NEC semiconductor devices	IEI-1209		
Semiconductor device mounting technology manual	C10535E		
Guide to quality assurance for semiconductor devices	MEI-1202		
Semiconductor selection guide	X10679E		

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Special: Transportation equipment (automobiles, trains, ships, etc.), traffic control systems, anti-disaster systems, anti-crime systems, safety equipment and medical equipment (not specifically designed for life support)

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Anti-radioactive design is not implemented in this product.

M4 94.11