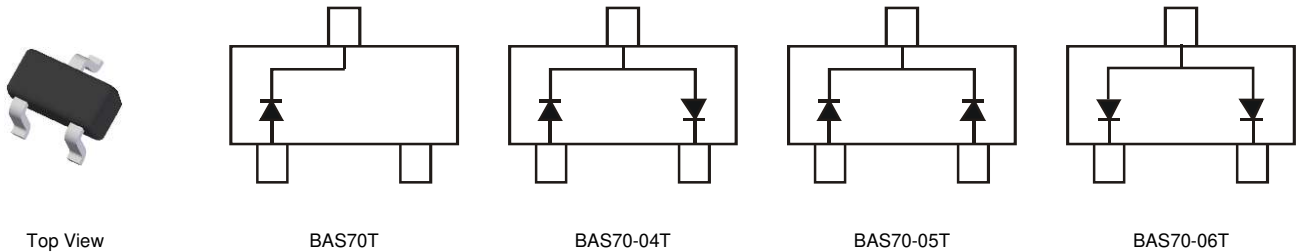


## Features

- Low Turn-on Voltage
- Fast Switching
- PN Junction Guard Ring for Transient and ESD Protection
- Ultra-Small Surface Mount Package
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please [contact us](https://www.diodes.com/quality/product-definitions/) or your local Diodes representative. <https://www.diodes.com/quality/product-definitions/>**

## Mechanical Data

- Package: SOT523
- Package Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 (e3)
- Polarity: See Diagrams Below
- Weight: 0.002 grams (Approximate)

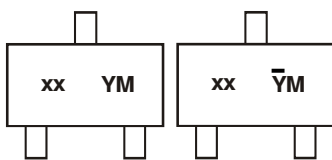


## Ordering Information (Note 4)

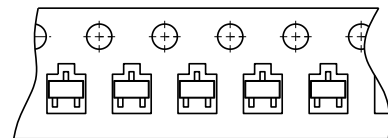
Part Number	Package	Packing	
		Qty.	Carrier
BAS70T-7-F	SOT523	3,000	Tape & Reel
BAS70-04T-7-F	SOT523	3,000	Tape & Reel
BAS70-05T-7-F	SOT523	3,000	Tape & Reel
BAS70-06T-7-F	SOT523	3,000	Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
  2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

## Marking Information



xx = Product Type Marking Code  
 7C = BAS70T  
 7D = BAS70-04T  
 7E = BAS70-05T  
 7F = BAS70-06T  
 YM &  $\bar{Y}M$  = Date Code Marking  
 Y &  $\bar{Y}$  = Year (ex: J = 2022)  
 M = Month (ex: 9 = September)



### Date Code Key

Year	2002	...	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031
Code	O	...	J	K	L	M	N	O	P	R	S	T

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

**Maximum Ratings** @T<sub>A</sub> = +25°C, unless otherwise specified.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V <sub>RRM</sub>	70	V
Working Peak Reverse Voltage	V <sub>RWM</sub>		
DC Blocking Voltage	V <sub>R</sub>		
RMS Reverse Voltage	V <sub>R(RMS)</sub>	49	V
Forward Continuous Current (Note 5)	I <sub>FM</sub>	70	mA
Non-Repetitive Peak Forward Surge Current @ t <sub>p</sub> < 1.0s	I <sub>FSM</sub>	100	mA

**Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P <sub>D</sub>	150	mW
Thermal Resistance Junction to Ambient Air (Note 5)	R <sub>θJA</sub>	833	°C/W
Operating Temperature Range	T <sub>J</sub>	-55 to +125	°C
Storage Temperature Range	T <sub>STG</sub>	-65 to +150	°C

**Electrical Characteristics** @T<sub>A</sub> = +25°C, unless otherwise specified.

Characteristic	Symbol	Min	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 6)	V <sub>(BR)R</sub>	70	—	V	I <sub>R</sub> = 10μA
Forward Voltage	V <sub>F</sub>	—	410 1000	mV	t <sub>p</sub> < 300μs, I <sub>F</sub> = 1.0mA t <sub>p</sub> < 300μs, I <sub>F</sub> = 15mA
Leakage Current (Note 6)	I <sub>R</sub>	—	100	nA	t <sub>p</sub> < 300μs, V <sub>R</sub> = 50V
Total Capacitance	C <sub>T</sub>	—	2.0	pF	V <sub>R</sub> = 0V, f = 1.0MHz
Reverse Recovery Time	t <sub>rr</sub>	—	5.0	ns	I <sub>F</sub> = I <sub>R</sub> = 10mA to I <sub>R</sub> = 1.0mA, I <sub>rr</sub> = 0.1 x I <sub>R</sub> , R <sub>L</sub> = 100Ω

- Notes:
5. Device mounted on FR-4 PC board with recommended pad layout, which can be found on our website at <http://www.diodes.com/package-outlines.html>.
  6. Short duration pulse test used to minimize self-heating effect.

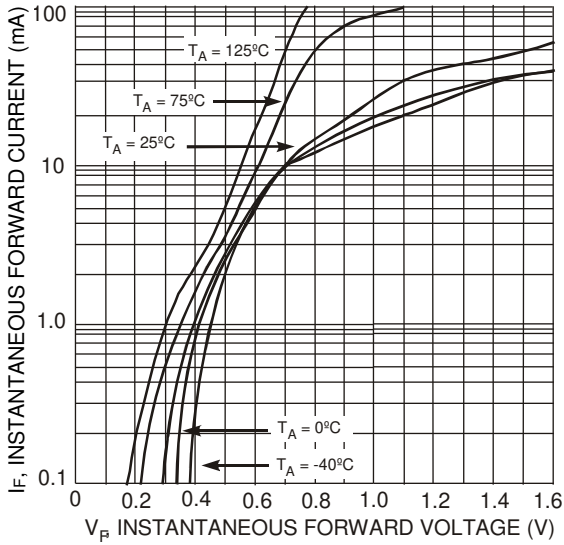


Fig. 1 Typical Forward Characteristics

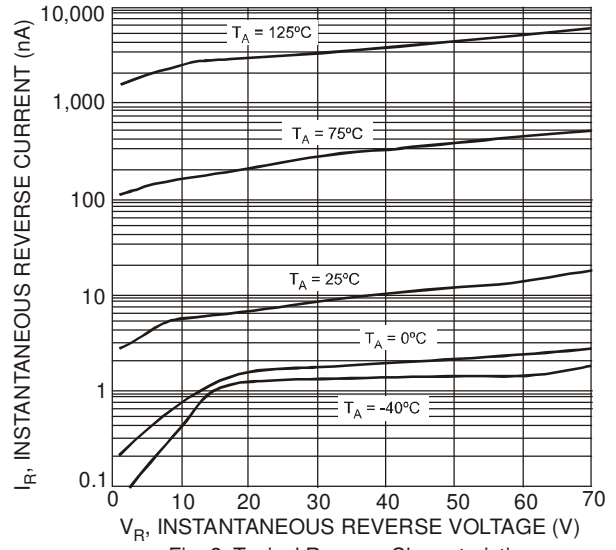


Fig. 2 Typical Reverse Characteristics

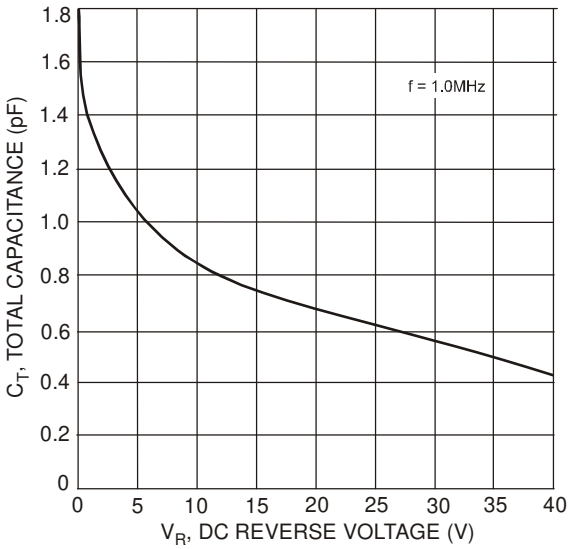


Fig. 3 Total Capacitance vs. Reverse Voltage

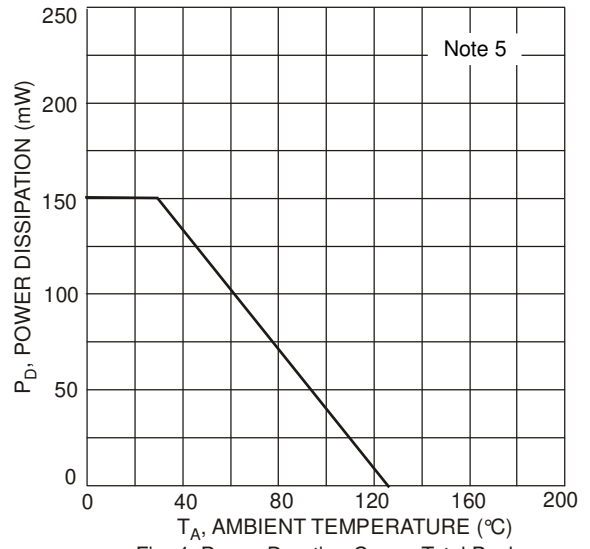
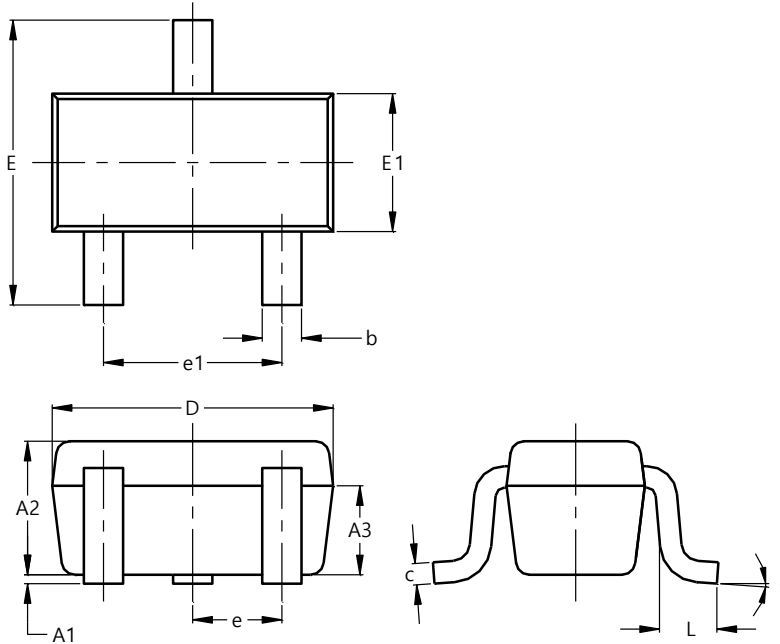


Fig. 4 Power Derating Curve, Total Package

**Package Outline Dimensions**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**SOT523**

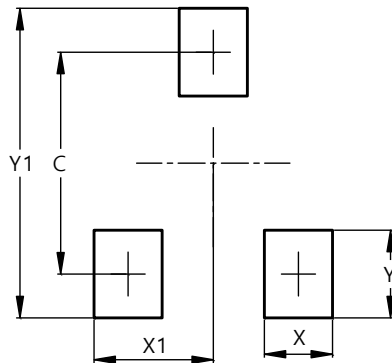


SOT523			
Dim	Min	Max	Typ
A1	0.00	0.10	0.05
A2	0.60	0.80	0.75
A3	0.45	0.65	0.50
b	0.15	0.30	0.22
c	0.10	0.20	0.12
D	1.50	1.70	1.60
E	1.45	1.75	1.60
E1	0.75	0.85	0.80
e	0.50 BSC		
e1	0.90	1.10	1.00
L	0.20	0.40	0.33
a	0°	--	8°
All Dimensions in mm			

**Suggested Pad Layout**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**SOT523**



Dimensions	Value (in mm)
C	1.29
X	0.40
X1	0.70
Y	0.51
Y1	1.80

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