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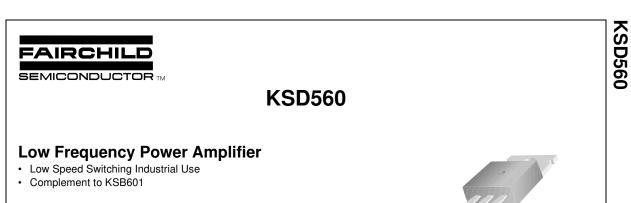


ON Semiconductor®

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NPN Epitaxial Silicon Darlington Transistor

Absolute Maximum Ratings T_C=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage	150	V
V _{CEO}	Collector-Emitter Voltage	100	V
V _{EBO}	Emitter-Base Voltage	7	V
I _C	Collector Current (DC)	5	Α
I _{CP}	*Collector Current (Pulse)	8	Α
I _B	Base Current	0.5	Α
P _C	Collector Dissipation (T _a =25°C)	1.5	W
P _C P _C	Collector Dissipation (T _C =25°C)	30	W
TJ	Junction Temperature	150	°C
T _{STG}	Storage Temperature	- 55 ~ 150	°C

* PW≤10ms, Duty Cycle≤50%

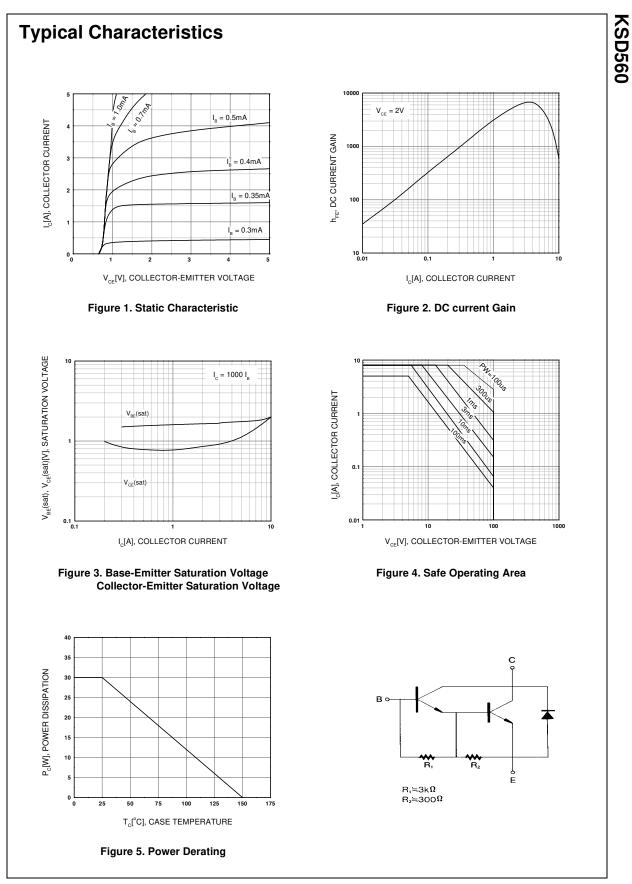
Electrical Characteristics T_C=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
I _{CBO}	Collector Cut-off Current	$V_{CB} = 100V, I_E = 0$			1	μA
h _{FE1} h _{FE2}	*DC Current Gain	$V_{CE} = 2V, I_C = 3A$ $V_{CE} = 2V, I_C = 5A$	2K 500	6K	15K	
V _{CE} (sat)	*Collector-Emitter Saturation Voltage	I _C = 3A, I _B = 3mA		0.9	1.5	V
V _{BE} (sat)	*Base-Emitter SaturationVoltage	I _C = 3A, I _B = 3mA		1.6	2	V
t _{ON}	Turn ON Time	$V_{CC} = 50V, I_{C} = 3A$		1		μs
t _{STG}	Storage Time	$I_{B1} = -I_{B2} = 3mA$		3.5		μs
f _T	Fall Time	R _L = 16.7Ω		1.2		μs

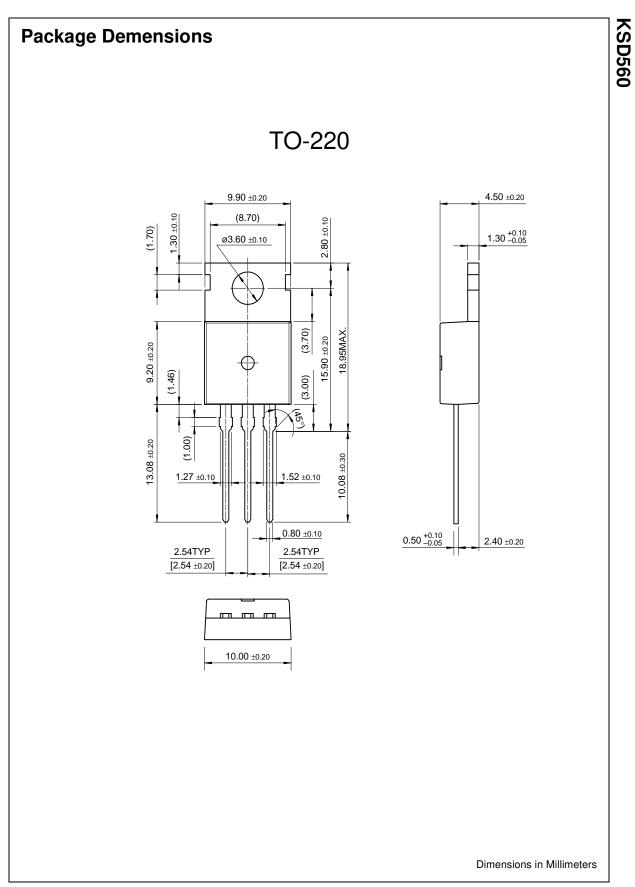
* Pulse Test: PW≤350µs, Duty Cycle≤2% Pulsed

h_{FE} Classification

Classification	R	0	Y
h _{FE1}	2000 ~ 5000	3000 ~ 7000	5000 ~ 15000



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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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