

BUK9Y4R4-40E

N-channel 40 V, 4.4 m Ω logic level MOSFET in LFPAK56 Product data sheet

1. **General description**

Logic level N-channel MOSFET in an LFPAK56 (Power SO8) package using TrenchMOS technology. This product has been designed and qualified to AEC Q101 standard for use in high performance automotive applications.

2. **Features and benefits**

- Q101 compliant
- Repetitive avalanche rated
- Suitable for thermally demanding environments due to 175 °C rating
- True logic level gate with V_{GS(th)} rating of greater than 0.5 V at 175 °C

Applications 3.

- 12 V Automotive systems
- Motors, lamps and solenoid control
- Transmission control
- Ultra high performance power switching

Quick reference data

Quick reference data Table 1.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V_{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C		-	-	40	V
I _D	drain current	V _{GS} = 5 V; T _{mb} = 25 °C; <u>Fig. 1</u>	[1]	-	-	100	Α
P _{tot}	total power dissipation	T _{mb} = 25 °C; <u>Fig. 2</u>		-	-	147	W
Static characte	eristics						
R _{DSon}	drain-source on-state resistance	$V_{GS} = 5 \text{ V}; I_D = 25 \text{ A}; T_j = 25 \text{ °C}; Fig. 11$		-	3.7	4.4	mΩ
Dynamic characteristics							
Q_{GD}	gate-drain charge	$V_{GS} = 5 \text{ V}; I_D = 25 \text{ A}; V_{DS} = 32 \text{ V};$ $T_j = 25 \text{ °C}; \underline{\text{Fig. 13}}; \underline{\text{Fig. 14}}$		-	8.7	-	nC

^[1] Continuous current is limited by package.



5. Pinning information

Table 2. Pinning information

		<u> </u>		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	S	source	mb	D I
2	S	source		
3	S	source	[d	G A
4	G	gate	<u>o o o o</u>	mbb076 S
mb	D	mounting base; connected to drain	1 2 3 4 LFPAK56; Power- SO8 (SOT669)	

6. Ordering information

Table 3. Ordering information

Type number	Package				
	Name	Description	Version		
BUK9Y4R4-40E	LFPAK56; Power-SO8	Plastic single-ended surface-mounted package (LFPAK56; Power-SO8); 4 leads	SOT669		

7. Marking

Table 4. Marking codes

Type number	Marking code
BUK9Y4R4-40E	94E440

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C		-	40	V
V_{DGR}	drain-gate voltage	$R_{GS} = 20 \text{ k}\Omega$		-	40	V
V _{GS}	gate-source voltage	T _j ≤ 175 °C; DC		-10	10	V
		T _j ≤ 175 °C; Pulsed	[1][2]	-15	15	V
I _D	drain current	T _{mb} = 25 °C; V _{GS} = 5 V; <u>Fig. 1</u>	[3]	-	100	Α
		T _{mb} = 100 °C; V _{GS} = 5 V; <u>Fig. 1</u>		-	91	Α
I _{DM}	peak drain current	T_{mb} = 25 °C; pulsed; $t_p \le 10 \mu s$; Fig. 4		-	516	Α
P _{tot}	total power dissipation	T _{mb} = 25 °C; <u>Fig. 2</u>		-	147	W

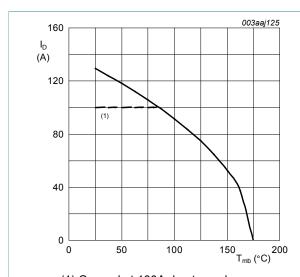
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Symbol	Parameter	Conditions		Min	Max	Unit
T _{stg}	storage temperature			-55	175	°C
T _j	junction temperature			-55	175	°C
Source-drain	diode					-
I _S	source current	T _{mb} = 25 °C	[3]	-	100	Α
I _{SM}	peak source current	pulsed; $t_p \le 10 \ \mu s$; $T_{mb} = 25 \ ^{\circ}C$		-	516	Α
Avalanche ruggedness						
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	I_D = 100 A; $V_{sup} \le$ 40 V; R_{GS} = 50 Ω; V_{GS} = 5 V; $T_{j(init)}$ = 25 °C; unclamped; Fig. 3	[4][5]	-	100	mJ

- Accumulated pulse duration up to 50 hours delivers zero defect ppm Significantly longer life times are achieved by lowering $\rm T_j$ and or $\rm V_{GS}$
- [2]
- Continuous current is limited by package. [3]
- Single-pulse avalanche rating limited by maximum junction temperature of 175 °C. [4]
- Refer to application note AN10273 for further information.



(1) Capped at 100A due to package Continuous drain current as a function of

mounting base temperature

 $V_{GS} \ge 5V$

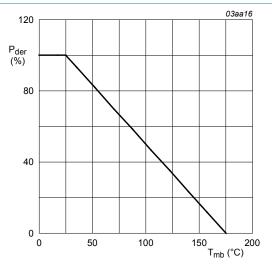


Fig. 2. Normalized total power dissipation as a function of mounting base temperature

$$P_{der} = \frac{P_{tot}}{P_{tot(25^{\circ}C)}} \times 100\%$$

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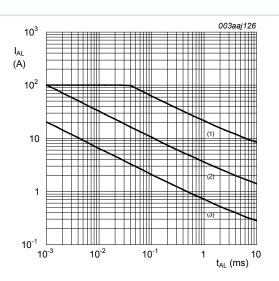
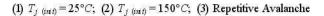


Fig. 3. Avalanche rating; avalanche current as a function of avalanche time



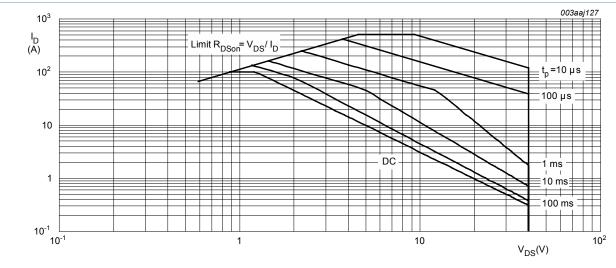


Fig. 4. Safe operating area; continuous and peak drain currents as a function of drain-source voltage

 $T_{mb} = 25^{\circ}C$; I_{DM} is a single pulse

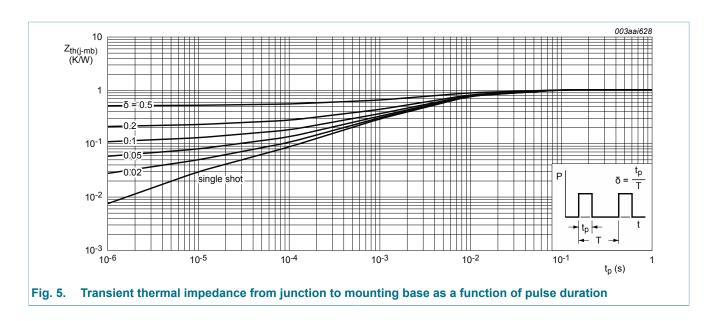
9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-mb)}	thermal resistance from junction to mounting base	Fig. 5	-	-	1.02	K/W

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10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics		'			
$V_{(BR)DSS}$	drain-source	$I_D = 250 \mu A; V_{GS} = 0 V; T_j = 25 °C$	40	-	-	V
	breakdown voltage	I _D = 250 μA; V _{GS} = 0 V; T _j = -55 °C	36	-	-	V
V _{GS(th)}	gate-source threshold voltage	$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = 25 \text{ °C};$ Fig. 9; Fig. 10	1.4	1.7	2.1	V
		I_D = 1 mA; V_{DS} = V_{GS} ; T_j = -55 °C; Fig. 9	-	-	2.45	V
		$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = 175 \text{ °C};$ Fig. 9	0.5	-	-	V
I _{DSS}	drain leakage current	V _{DS} = 40 V; V _{GS} = 0 V; T _j = 25 °C	-	0.07	10	μΑ
		V _{DS} = 40 V; V _{GS} = 0 V; T _j = 175 °C	-	-	500	μΑ
I _{GSS}	gate leakage current	V _{GS} = 10 V; V _{DS} = 0 V; T _j = 25 °C	-	2	100	nA
		V_{GS} = -10 V; V_{DS} = 0 V; T_j = 25 °C	-	2	100	nA
R _{DSon}	drain-source on-state	V _{GS} = 5 V; I _D = 25 A; T _j = 25 °C; <u>Fig. 11</u>	-	3.7	4.4	mΩ
	resistance	V _{GS} = 10 V; I _D = 25 A; T _j = 25 °C; Fig. 11	-	3.1	3.7	mΩ
		V _{GS} = 5 V; I _D = 25 A; T _j = 175 °C; Fig. 11; Fig. 12	-	-	8.8	mΩ
Dynamic ch	naracteristics		'	'		
Q _{G(tot)}	total gate charge	I _D = 25 A; V _{DS} = 32 V; V _{GS} = 5 V;	-	26.8	-	nC
Q _{GS}	gate-source charge	T _j = 25 °C; <u>Fig. 13</u> ; <u>Fig. 14</u>	-	7.7	-	nC

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N-channel 40 V, 4.4 m Ω logic level MOSFET in LFPAK56

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Q_{GD}	gate-drain charge			-	8.7	-	nC
C _{iss}	input capacitance	V _{GS} = 0 V; V _{DS} = 25 V; f = 1 MHz;		-	3058	4077	pF
C _{oss}	output capacitance	T _j = 25 °C; <u>Fig. 15</u>		-	422	506	pF
C _{rss}	reverse transfer capacitance			-	190	260	pF
t _{d(on)}	turn-on delay time	V_{DS} = 30 V; R_{L} = 1.2 Ω ; V_{GS} = 5 V; $R_{G(ext)}$ = 5 Ω ; T_{j} = 25 °C		-	16	-	ns
t _r	rise time			-	31	-	ns
t _{d(off)}	turn-off delay time			-	36	-	ns
t _f	fall time			-	25	-	ns
Source-dra	ain diode						,
V_{SD}	source-drain voltage	$I_S = 25 \text{ A}$; $V_{GS} = 0 \text{ V}$; $T_j = 25 ^{\circ}\text{C}$; Fig. 16		-	0.83	1.2	V
t _{rr}	reverse recovery time	$I_S = 20 \text{ A}; dI_S/dt = -100 \text{ A/}\mu\text{s}; V_{GS} = 0 \text{ V};$		-	23.6	-	ns
Q _r	recovered charge	V _{DS} = 25 V; T _j = 25 °C		-	16.5	-	nC

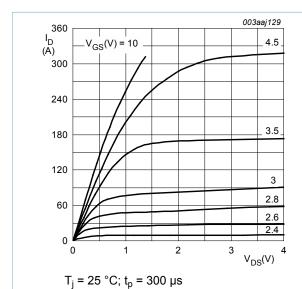


Fig. 6. Output characteristics; drain current as a function of drain-source voltage; typical values

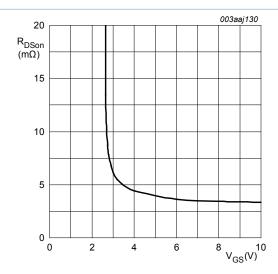


Fig. 7. Drain-source on-state resistance as a function of gate-source voltage; typical values

$$T_j = 25^{\circ}C; I_D = 25A$$

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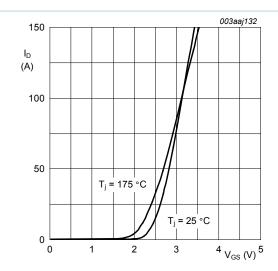


Fig. 8. Transfer characteristics; drain current as a function of gate-source voltage; typical values



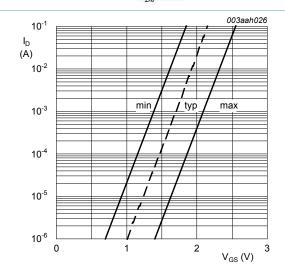


Fig. 10. Sub-threshold drain current as a function of gate-source voltage

$$T_j = 25^{\circ}C; \ V_{DS} = 5V$$

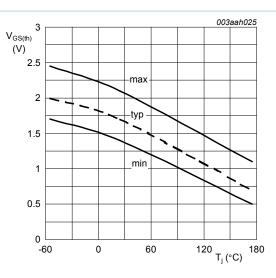
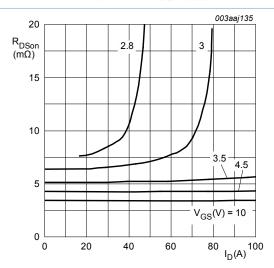


Fig. 9. Gate-source threshold voltage as a function of junction temperature

$$I_D = 1 \text{ mA}; \ V_{DS} = V_{GS}$$



 $T_j = 25 \,^{\circ}\text{C}; t_p = 300 \,\mu\text{s}$

Fig. 11. Drain-source on-state resistance as a function of drain current; typical values

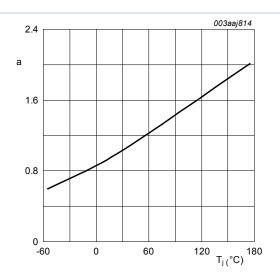


Fig. 12. Normalized drain-source on-state resistance factor as a function of junction temperature

$$a = \frac{R_{DSon}}{R_{DSon (25^{\circ}C)}}$$

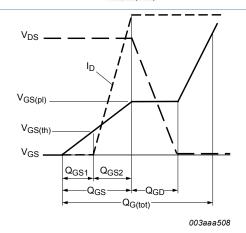


Fig. 14. Gate charge waveform definitions

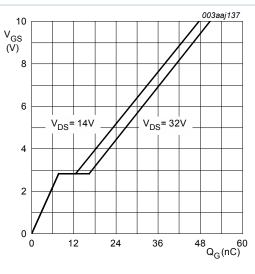


Fig. 13. Gate-source voltage as a function of gate charge; typical values

$$T_j = 25^{\circ}C; I_D = 25A$$

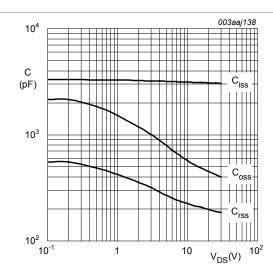


Fig. 15. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

$$V_{GS} = \mathbf{0}V; f = \mathbf{1}MHz$$

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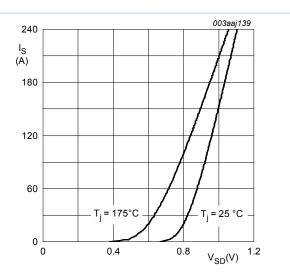


Fig. 16. Source-drain (diode forward) current as a function of source-drain (diode forward) voltage; typical values

$$V_{GS} = 0V$$

11. Package outline

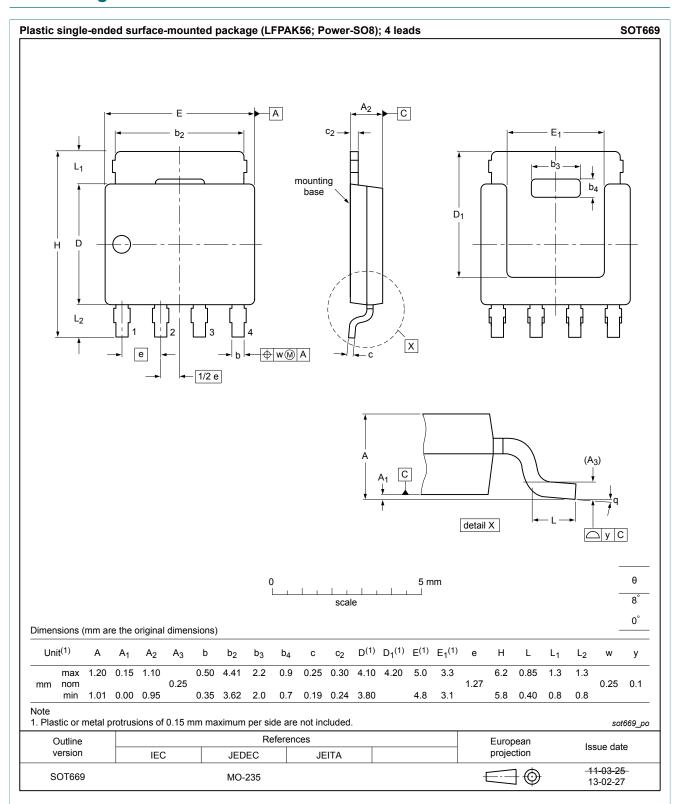


Fig. 17. Package outline LFPAK56; Power-SO8 (SOT669)

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