

PMV50XP 20 V, P-channel Trench MOSFET 19 November 2014

Product data sheet

1. General description

P-channel enhancement mode Field-Effect Transistor (FET) in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

2. Features and benefits

- Low threshold voltage
- Low on-state resistance
- Trench MOSFET technology
- Enhanced power dissipation capability of 1096 mW

3. Applications

- Relay driver
- High-speed line driver
- High-side loadswitch
- Switching circuits

4. Quick reference data

Table 1. Qui	ck reference data						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	-	-20	V
V _{GS}	gate-source voltage			-12	-	12	V
I _D	drain current	V _{GS} = -4.5 V; T _{amb} = 25 °C; t ≤ 5 s	[1]	-	-	-4.4	А
Static characteristics							
R _{DSon}	drain-source on-state resistance	V_{GS} = -4.5 V; I _D = -3.6 A; T _j = 25 °C		-	48	60	mΩ

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm².

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5. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	3	D
2	S	source		
3	D	drain	1 2 TO-236AB (SOT23)	G S 017aaa257

6. Ordering information

Table 3. Ordering in	formation		
Type number	Package		
	Name	Description	Version
PMV50XP	TO-236AB	plastic surface-mounted package; 3 leads	SOT23

7. Marking

Table 4. Marking codes	
Type number	Marking code
	[1]
PMV50XP	%2M

[1] % = placeholder for manufacturing site code

8. Limiting values

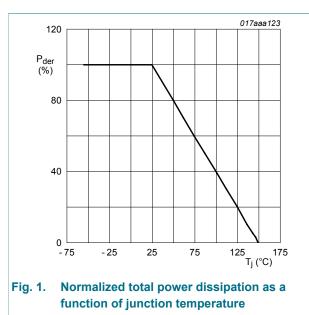
Table 5.Limiting values

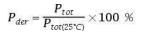
In accordance with the Absolute Maximum Rating System (IEC 60134).

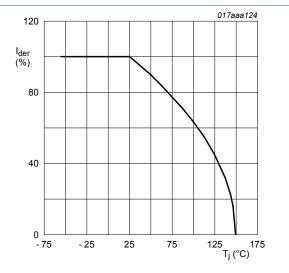
Symbol	Parameter	Conditions		Min	Мах	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	-20	V
V _{GS}	gate-source voltage			-12	12	V
I _D	drain current	V_{GS} = -4.5 V; T_{amb} = 25 °C; t ≤ 5 s	[1]	-	-4.4	А
		V _{GS} = -4.5 V; T _{amb} = 25 °C	[1]	-	-3.6	А
		V _{GS} = -4.5 V; T _{amb} = 100 °C	[1]	-	-2.3	А
I _{DM}	peak drain current	T_{amb} = 25 °C; single pulse; $t_p \le 10 \ \mu s$		-	-14.5	А
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	490	mW
			[1]	-	1096	mW
		T _{sp} = 25 °C		-	4630	mW
Tj	junction temperature			-55	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
Source-dra	in diode	· · · · · · · · · · · · · · · · · · ·				
I _S	source current	T _{sp} = 25 °C	[1]	-	-1	А

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm².

[2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.



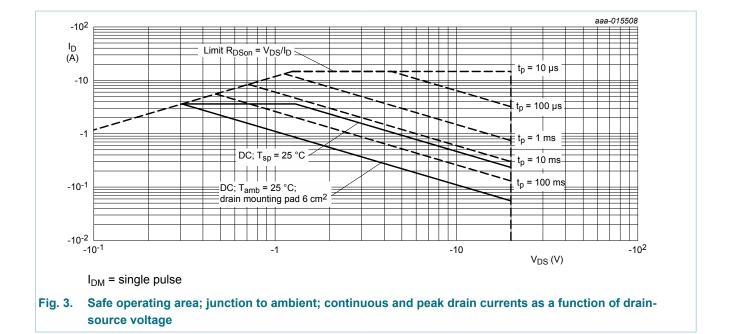






$$I_{der} = \frac{I_D}{I_{D(25^{\circ}C)}} \times 100 \%$$

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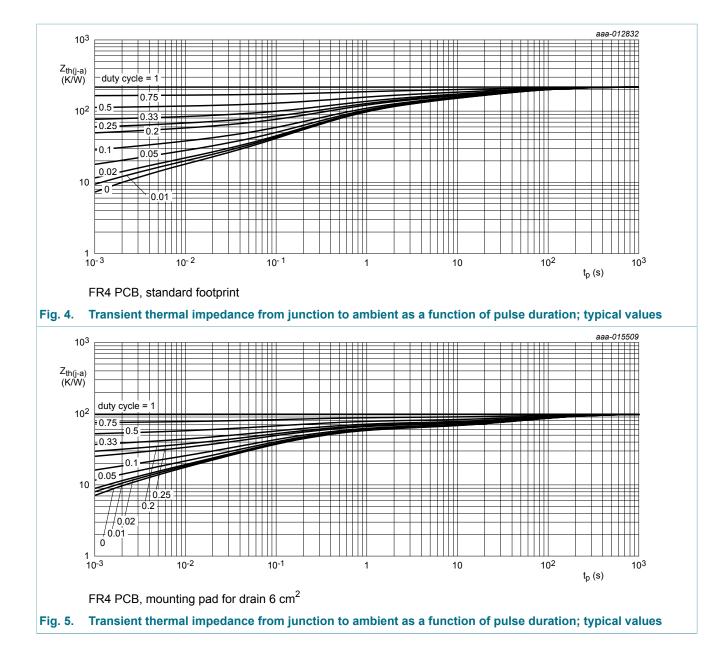


9. Thermal characteristics

Table 6. T	Thermal characteristics						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance	in free air	[1]	-	217	255	K/W
	from junction to ambient		[2]	-	97	114	K/W
	ambient	in free air; t ≤ 5 s	[2]	-	65	76	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	23	27	K/W

Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
 Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm².



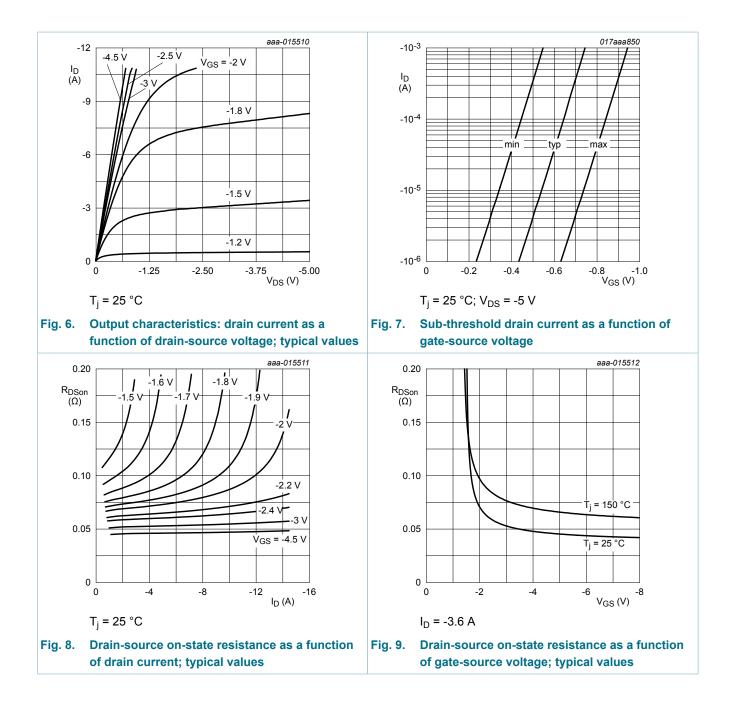


10. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara						
V _{(BR)DSS}	drain-source breakdown voltage	I_D = -250 µA; V_{GS} = 0 V; T_j = 25 °C	-20	-	-	V
V _{GSth}	gate-source threshold voltage	I_D = -250 µA; V_{DS} = V_{GS} ; T_j = 25 °C	-0.47	-0.65	-0.9	V
I _{DSS}	drain leakage current	V_{DS} = -20 V; V_{GS} = 0 V; T_j = 25 °C	-	-	-1	μA
I _{GSS}	gate leakage current	V_{GS} = -12 V; V_{DS} = 0 V; T_j = 25 °C	-	-	-100	nA
		V_{GS} = 12 V; V_{DS} = 0 V; T_j = 25 °C	-	-	100	nA
Doon	drain-source on-state	V_{GS} = -4.5 V; I _D = -3.6 A; T _j = 25 °C	-	48	60	mΩ
	resistance	V_{GS} = -4.5 V; I _D = -3.6 A; T _j = 150 °C	-	68	86	mΩ
		V_{GS} = -2.5 V; I _D = -3.1 A; T _j = 25 °C	-	60	80	mΩ
		V_{GS} = -1.8 V; I _D = -0.8 A; T _j = 25 °C	-	82	121	mΩ
		V_{GS} = -1.5 V; I _D = -0.1 A; T _j = 25 °C	-	116	250	mΩ
9 _{fs}	forward transconductance	V_{DS} = -10 V; I _D = -2 A; T _j = 25 °C	-	9	-	S
Dynamic ch	naracteristics	· · · ·	I			
Q _{G(tot)}	total gate charge	V_{DS} = -6 V; I _D = -2.8 A; V _{GS} = -4.5 V;	-	7.7	12	nC
Q _{GS}	gate-source charge	T _j = 25 °C	-	1	-	nC
Q _{GD}	gate-drain charge		-	1.65	-	nC
C _{iss}	input capacitance	V_{DS} = -20 V; f = 1 MHz; V_{GS} = 0 V;	-	744	-	pF
C _{oss}	output capacitance	T _j = 25 °C	-	65	-	pF
C _{rss}	reverse transfer capacitance		-	53	-	pF
t _{d(on)}	turn-on delay time	V_{DS} = -6 V; V_{GS} = -4.5 V; $R_{G(ext)}$ = 6 Ω ;	-	7	-	ns
t _r	rise time	T _j = 25 °C; I _D = -1 A	-	18	-	ns
t _{d(off)}	turn-off delay time	1	-	135	-	ns
t _f	fall time	1	-	68	-	ns
Source-dra	in diode	· · · · ·	1			
V _{SD}	source-drain voltage	I _S = -1 A; V _{GS} = 0 V; T _i = 25 °C	-	-0.74	-1.2	V

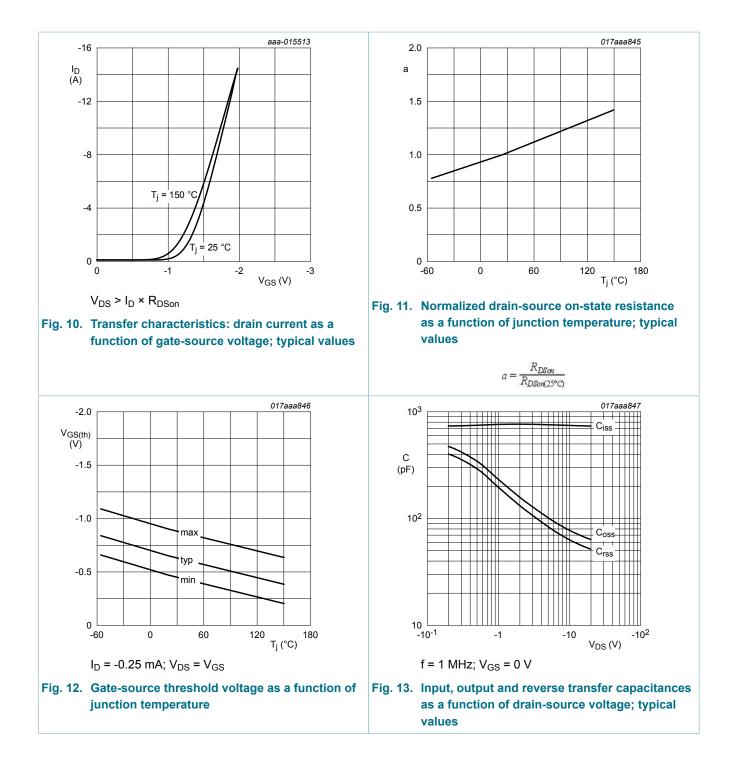
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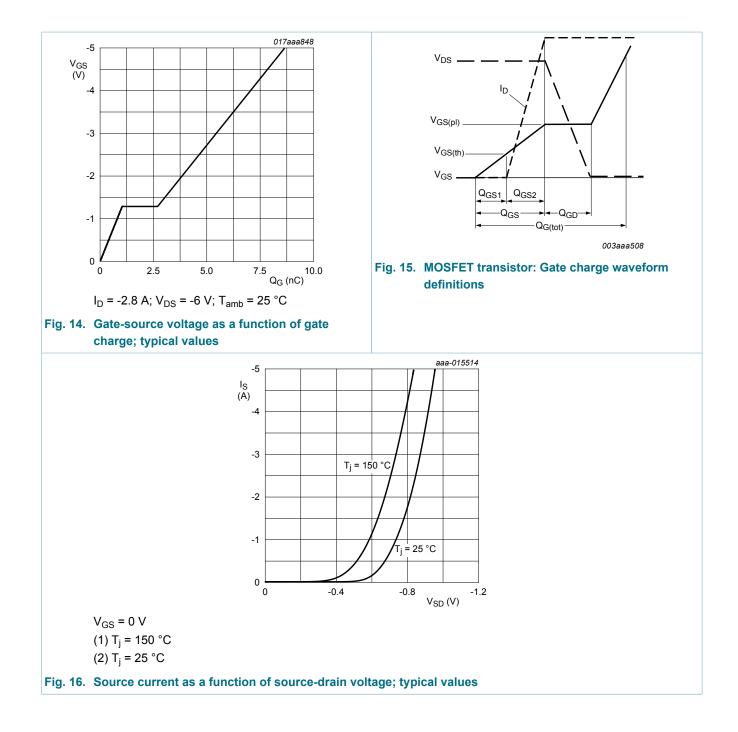
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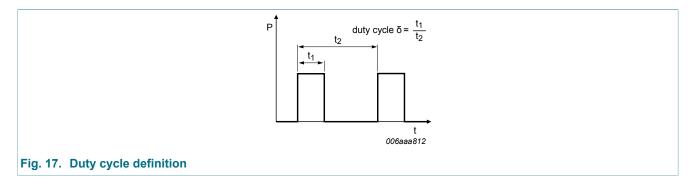


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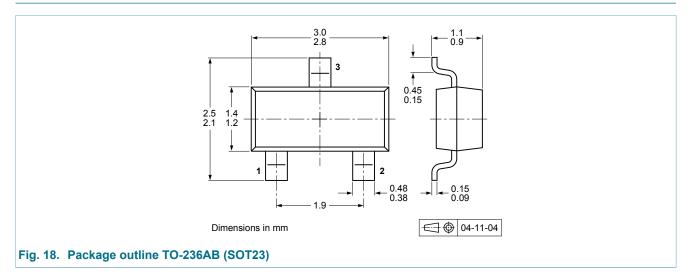
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11. Test information

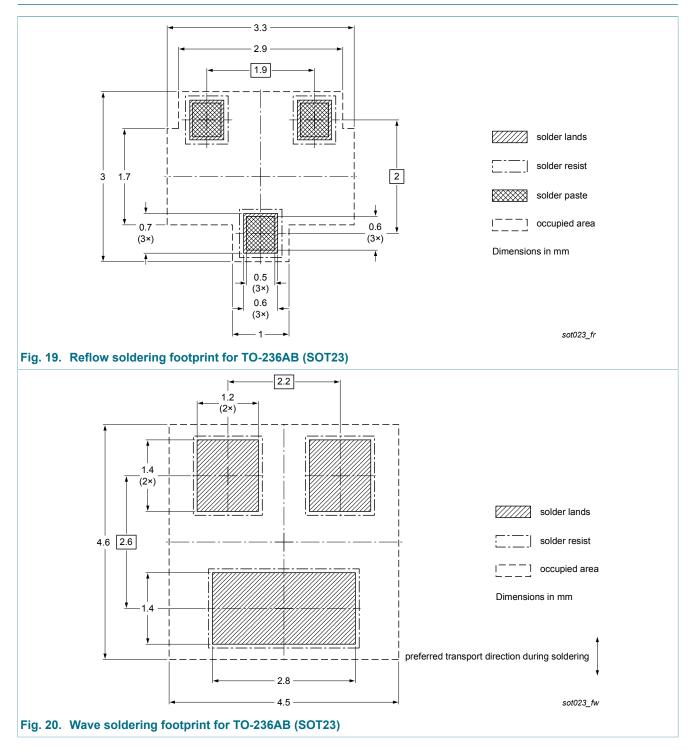


12. Package outline



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13. Soldering



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14. Revision history

Table 8. Revision his	story			
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMV50XP v.2	20141119	Product data sheet	-	PMV50XP v.1
Modifications:	Table 7: R _{DSon} unit corre	ected		
PMV50XP v.1	20141111	Product data sheet	-	-

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15. Legal information

15.1 Data sheet status

Document status [1][2]	Product status [<u>3]</u>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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The term 'short data sheet' is explained in section "Definitions". [2]

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