Unit: mm

TOSHIBA Field-Effect Transistor Silicon P-Channel MOS Type

SSM3J118TU

High-Speed Switching Applications

• 4 V drive

• Low ON-resistance: R_{on} = 480 m Ω (max) (@V_{GS} = -4 V) R_{on} = 240 m Ω (max) (@V_{GS} = -10 V)

Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unit	
Drain-source voltage		V _{DS}	-30	V	
Gate-source voltage		V _{GSS}	± 20	V	
Drain current	DC	I _D	-1.4	Α	
	Pulse	I _{DP}	-2.8	^	
Drain power dissipation		P _{D (Note 1)}	800	mW	
		P _{D (Note 2)}	500	11100	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	–55 to 150	°C	

Note:

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Mounted on a ceramic board.

 $(25.4 \text{ mm} \times 25.4 \text{ mm} \times 0.8 \text{ t}, \text{ Cu Pad: } 645 \text{ mm}^2)$

Note 2: Mounted on an FR4 board.

 $(25.4 \text{ mm} \times 25.4 \text{ mm} \times 1.6 \text{ t}, \text{ Cu Pad: } 645 \text{ mm}^2)$

2.1±0.1 1.7±0.1 1.0±0.7 20.0+2.0 20.0+2.0 1: Gate 2: Source

2-2U1A

Weight: 6.6 mg (typ.)

3: Drain

UFM

JEDEC

JEITA

TOSHIBA

Electrical Characteristics (Ta = 25°C)

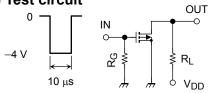
Characte	eristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Drain–source breakdown voltage		V (BR) DSS	$I_D = -1 \text{ mA}, V_{GS} = 0$	-30	_	_	V
		V (BR) DSX	$I_D = -1 \text{ mA}, V_{GS} = +20 \text{ V}$	-15	_		
Drain cutoff current		I _{DSS}	$V_{DS} = -30 \text{ V}, V_{GS} = 0$		_	-1	μΑ
Gate leakage curre	nt	I _{GSS}	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$	_	_	±1	μА
Gate threshold volta	age	V _{th}	$V_{DS} = -5 \text{ V}, I_D = -1 \text{ mA}$	-1.2	_	-2.6	V
Forward transfer ad	mittance	Y _{fs}	$V_{DS} = -5 \text{ V}, I_D = -0.65 \text{ A}$ (Note 3)	0.8	1.5	_	S
Drain-source ON-resistance		R _{DS (ON)}	$I_D = -0.65 \text{ A}, V_{GS} = -10 \text{ V}$ (Note 3)	_	180	240	mΩ
			$I_D = -0.4 \text{ A}, V_{GS} = -4 \text{ V}$ (Note 3)		360	480	
Input capacitance		C _{iss}	$V_{DS} = -15 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$	_	137	_	pF
Output capacitance		Coss	$V_{DS} = -15 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$	_	39	_	pF
Reverse transfer capacitance		C _{rss}	$V_{DS} = -15 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$	_	20	_	pF
Switching time	Turn-on time	t _{on}	$V_{DD} = -15 \text{ V}, I_D = -0.65 \text{ A},$ $V_{GS} = 0 \text{ to } -4 \text{ V}, R_G = 10 \Omega$	_	15	_	ns
	Turn-off time	t _{off}		_	14	_	
Drain-source forward voltage		V _{DSF}	$I_D = 1.4 \text{ A}, V_{GS} = 0 \text{ V}$ (Note 3)	_	0.85	1.2	V

Note 3: Pulse test

Start of commercial production 2005-12

Switching Time Test Circuit

(a) Test circuit



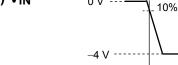
 $V_{DD} = -15 \text{ V}$ $R_G = 10 \Omega$

Duty ≤ 1%

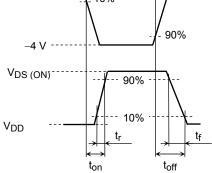
 V_{IN} : t_r , $t_f < 5$ ns Common Source

 $Ta = 25^{\circ}C$

(b) V_{IN}

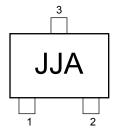


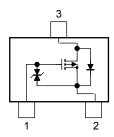
(c) Vout



Marking

Equivalent Circuit (top view)





Precaution

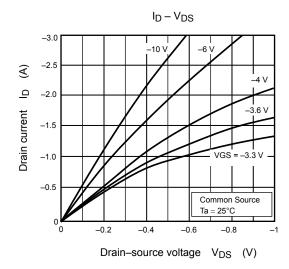
V_{th} can be expressed as the voltage between gate and source when the low operating current value is I_D = −1 mA for this product. For normal switching operation, V_{GS (on)} requires a higher voltage than V_{th} and V_{GS (off)} requires a lower voltage than Vth.

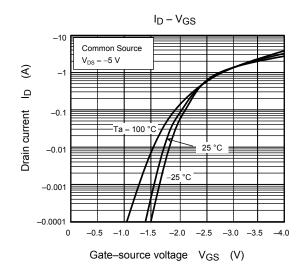
(The relationship can be established as follows: V_{GS} (off) < V_{th} < V_{GS} (on).)

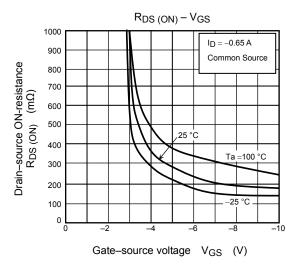
Take this into consideration when using the device.

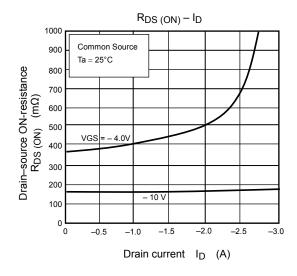
Handling Precaution

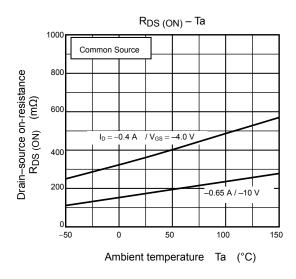
When handling individual devices that are not yet mounted on a circuit board, make sure that the environment is protected against electrostatic discharge. Operators should wear antistatic clothing, and containers and other objects that come into direct contact with devices should be made of antistatic materials.

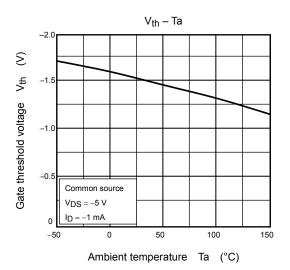




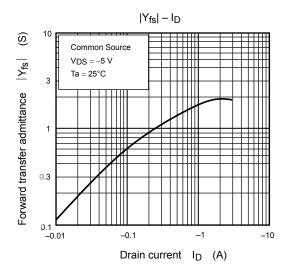


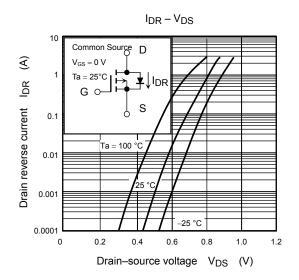


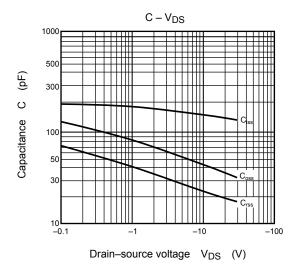


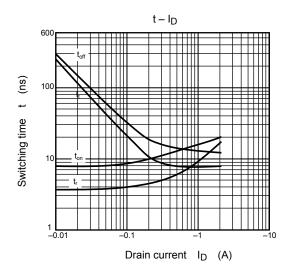


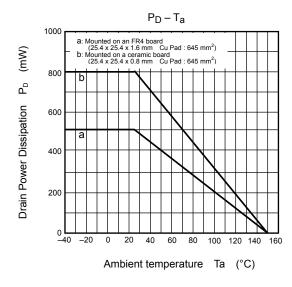
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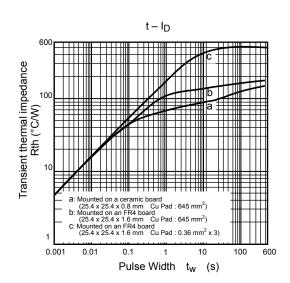












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