

RSJ400N10FRA

Nch 100V 40A Power MOSFET

V _{DSS}	100V
R _{DS(on)} (Max.)	27mΩ
I _D	±40A
P _D	50W

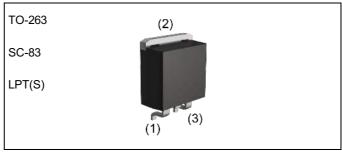
Features

- 1) Low on-resistance
- 2) Fast switching speed
- 3) High power small mold package
- 4) Pb-free plating; RoHS compliant
- 5) AEC-Q101 Qualified

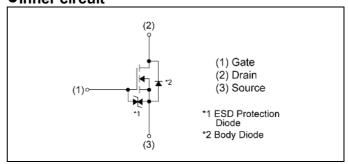
Application

Switching

Outline



•Inner circuit



Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	330
Туре	Tape width (mm)	24
	Quantity (pcs)	1000
	Taping code	TL
	Marking	RSJ400N10

● **Absolute maximum ratings** (T_a = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain - Source voltage	V_{DSS}	100	V
Continuous drain current	I _D *1	±40	Α
Pulsed drain current	I _{DP} *2	±80	Α
Gate - Source voltage	V _{GSS}	±20	V
Avalanche current, single pulse	I _{AS} *3	10	Α
Avalanche energy, single pulse	E _{AS} *3	73	mJ
Power dissipation	P _D *1	50	W
Junction temperature	T _j	150	°C
Operating junction and storage temperature range	T _{stg}	-55 to +150	°C

●Thermal resistance

Parameter	Cymahal	Values			Lleit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R _{thJC} *1	-	ı	2.5	°C/W

● Electrical characteristics (T_a = 25°C)

Davamatav	Cymahal	Conditions			Values		
Parameter	Symbol Conditions		Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 1mA	100	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	I _D = 1mA referenced to 25°C	-	116.9	-	mV/°C	
Zero gate voltage drain current	I _{DSS}	V _{DS} = 100V, V _{GS} = 0V	-	-	1	μA	
Gate - Source leakage current	I _{GSS}	$V_{GS} = \pm 20V$, $V_{DS} = 0V$	-	-	±10	μΑ	
Gate threshold voltage	V _{GS(th)}	V _{DS} = 10V, I _D = 1mA	1.0	-	2.5	V	
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_j}$	I _D = 1mA referenced to 25°C	-	-3.6	-	mV/°C	
Static drain - source	D *4	V _{GS} = 10V, I _D = 20A	-	19	27	O	
on - state resistance	R _{DS(on)} *4	V _{GS} = 4V, I _D = 20A	-	21	30	mΩ	
Gate resistance R_G $f = 1MHz$, open drain		f = 1MHz, open drain	-	2.9	-	Ω	
Forward Transfer Admittance	Y _{fs} *4	V _{DS} = 10V, I _D = 20A	23	-	-	S	

^{*1} T_c =25°C, Limited only by maximum temperature allowed.



^{*2} Pw ≤ 10µs, Duty cycle ≤ 1%

^{*3} L \simeq 1mH, V_{DD} = 50V, R_G = 25 Ω , Starting T_j = 25 $^{\circ}$ C Fig.3-1,3-2

^{*4} Pulsed

●Electrical characteristics (T_a = 25°C)

Daramatar	Cymahal	Conditions	Values			Unit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	UIIIL	
Input capacitance	C _{iss}	V _{GS} = 0V	-	3600	-	_	
Output capacitance	C _{oss}	V _{DS} = 25V	-	270	-	pF	
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	180	-		
Turn - on delay time	t _{d(on)} *4	V _{DD} ≈ 50V,V _{GS} = 10V	-	25	-		
Rise time	t _r *4	I _D = 20A	-	80	-	no	
Turn - off delay time	t _{d(off)} *4	$R_L \simeq 2.5\Omega$	-	205	-	ns	
Fall time	t _f *4	$R_G = 10\Omega$	-	250	-		

• Gate charge characteristics $(T_a = 25^{\circ}C)$

	\ α	,				
Parameter	Symbol	Conditions	Values			l leit
raiametei	Symbol	Conditions	Min.	Тур.	Max.	Unit
Total gate charge	Qg*4	V _{DD} ≃ 50V.	-	90	-	
Gate - Source charge	Q _{gs} *4	$V_{DD} \approx 50V$, $I_D = 40A$,	-	12	-	nC
Gate - Drain charge	Q _{gd} *4	V _{GS} = 10V	-	18	-	

●Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Darameter	Symbol	Conditions	Values			l leit
Parameter	Symbol Conditions		Min.	Тур.	Max.	Unit
Continuous forward current	I _S	T = 25°C	-	-	40	Α
Pulse forward current	I _{SP} *2	T _a = 25°C	-	-	80	Α
Forward voltage	V _{SD} *4	V _{GS} = 0V, I _S = 40A	-	-	1.5	V

Fig.1 Power Dissipation Derating Curve

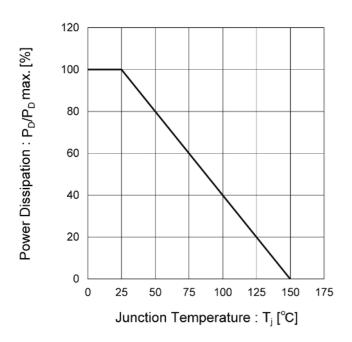
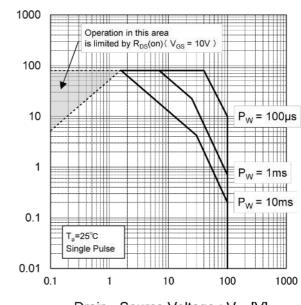


Fig.2 Maximum Safe Operating Area



Drain Current: Ip [A]

Drain - Source Voltage : V_{DS} [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

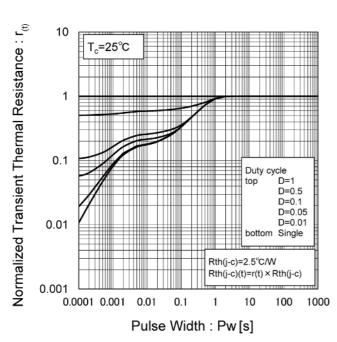


Fig.4 Single Pulse Maximum Power Dissipation

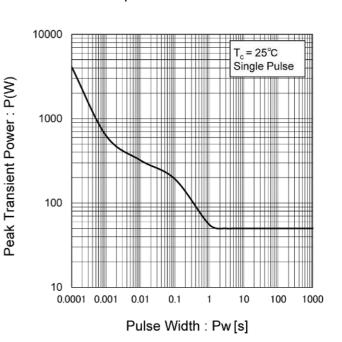
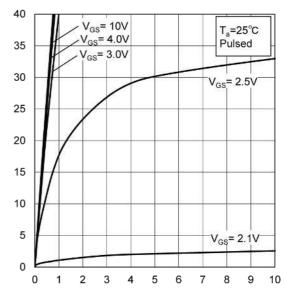


Fig.5 Typical Output Characteristics(I)

40 V_{GS}= 10V T_a=25°C V_{GS}= 4.0V 35 Pulsed 30 Drain Current: Ip [A] V_{GS}= 3.0V 25 20 15 V_{GS}= 2.5V 10 5 V_{GS}= 2.1V 0 0.2 0.3 0.4 0.5 0.6 0.7 0.8 0.9

Drain - Source Voltage: VDS [V]

Fig.6 Typical Output Characteristics(II)



Drain Current: Ip [A]

Drain - Source Voltage: V_{DS} [V]

Fig.7 Breakdown Voltage vs. **Junction Temperature**

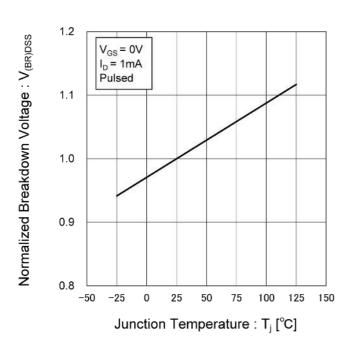


Fig.8 Typical Transfer Characteristics

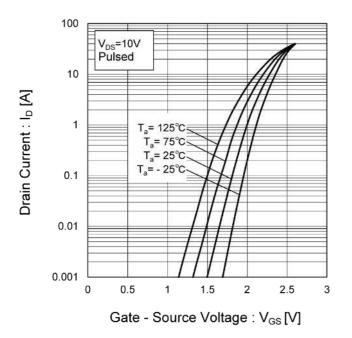


Fig.9 Gate Threshold Voltage vs.
Junction Temperature

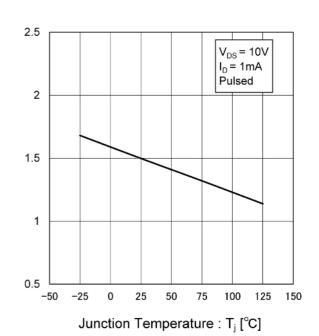
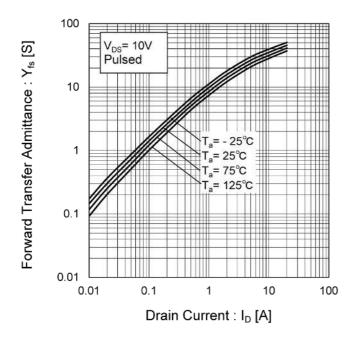


Fig.10 Forward Transfer Admittance vs.
Drain Current



Gate Threshold Voltage: VGS(th) [V]

Fig.11 Drain Current Derating Curve

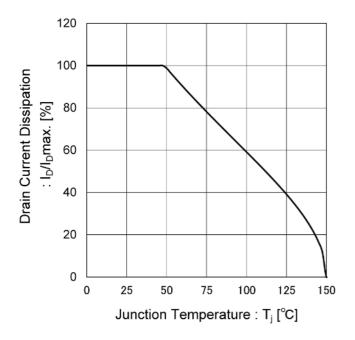


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

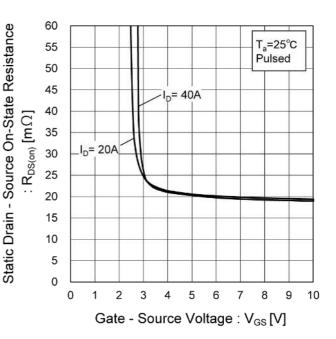


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

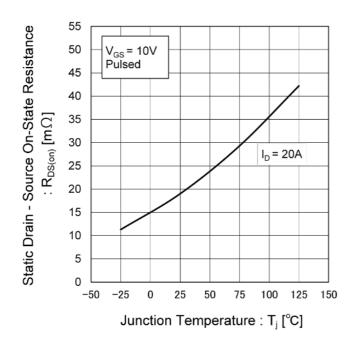


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (I)

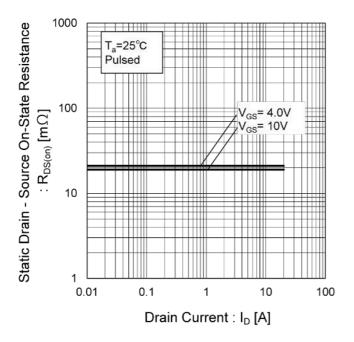


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

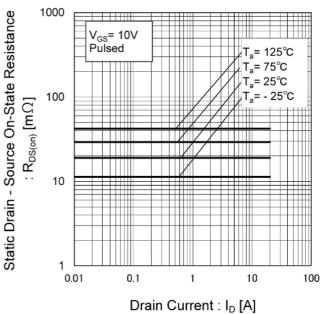


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)

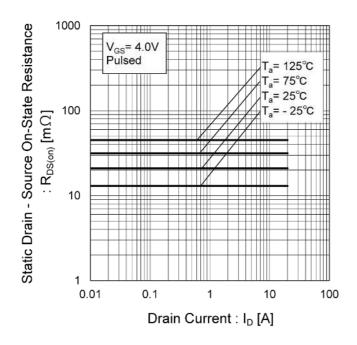


Fig.17 Typical Capacitances vs.

Drain - Source Voltage

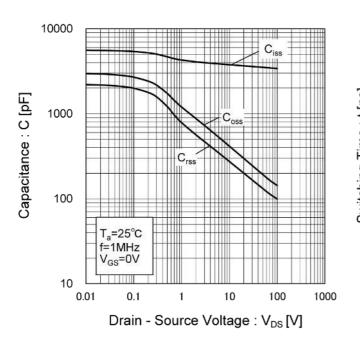


Fig.18 Switching Characteristics

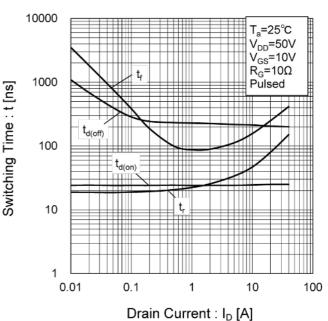


Fig.19 Typical Gate Charge

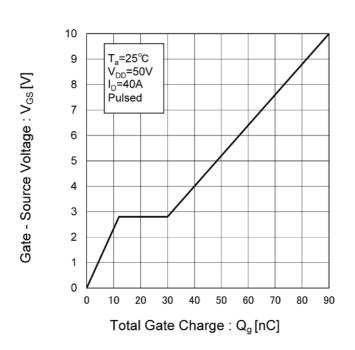
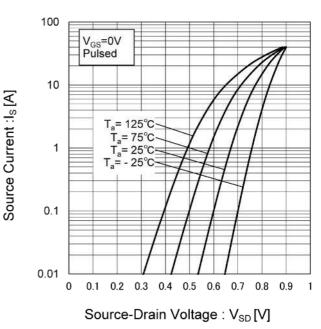


Fig.20 Source Current vs.

Source Drain Voltage



Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

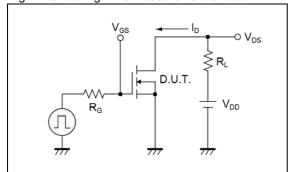


Fig.2-1 Gate Charge Measurement Circuit

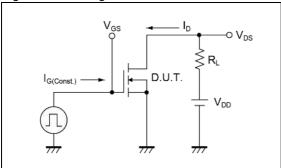


Fig.3-1 Avalanche Measurement Circuit

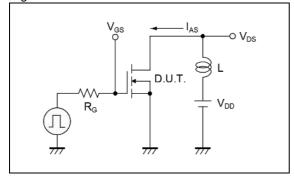


Fig.1-2 Switching Waveforms

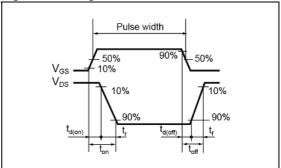


Fig.2-2 Gate Charge Waveform

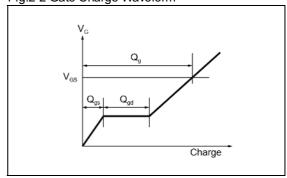
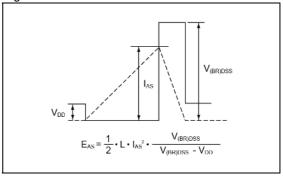
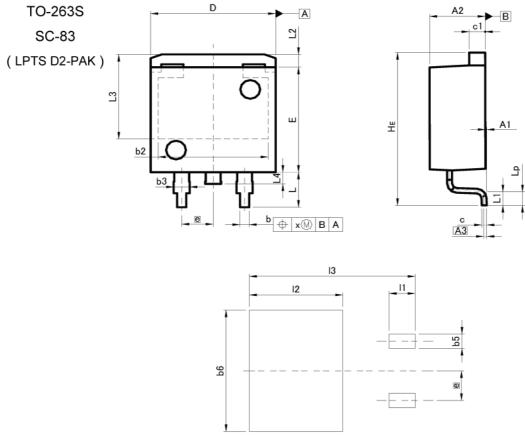


Fig.3-2 Avalanche Waveform



Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
A1	0.00	0.30	0.000	0.012
A2	4.30	4.70	0.169	0.185
A3	0.	25	0.0	10
b	0.68	0.98	0.027	0.039
b2	8.	90	0.3	50
ь3	1.14	1.44	0.045	0.057
С	0.30	0.60	0.012	0.024
c1	1.10	1.50	0.043	0.059
D	9.80	10.40	0.386	0.409
E	8.80	9.20	0.346	0.362
е	2.54		0.1	00
HE	12.80	13.40	0.504	0.528
L	2.70	3.30	0.106	0.130
L1	1.	20	0.047	47
L2	1.	10	0.0	43
L3	7.	7.25		85
L4	1.	00	0.0	39
Lp	0.90	1.50	0.035	0.059
Х	<i>=1</i>	0.25	3=	0.010
	NATI TNA	-TEDO	INC	

DIM	MILIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
bb	H.:	1.23	-	0.049
b6		10.40	_	0.409
11	227	2.10	, 12	0.083
12		7.55	-	0.297
13	-	13.40	-	0.528

Dimension in mm/inches



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JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSⅢ	CLASSIIb	CLASSⅢ
CLASSIV	CLASSIII	CLASSⅢ	CLASSIII

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 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
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- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

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- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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