**Vishay Semiconductors** 

## High Performance Schottky Rectifier, 1 A

Anode



- · Small foot print, surface mountable
- Low forward voltage drop
- High frequency operation
- · Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912
- · Designed and qualified for industrial level

#### DESCRIPTION

The VS-10BQ100PbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES UN			
I <sub>F(AV)</sub>	Rectangular waveform	1.0	A		
V <sub>RRM</sub>		100	V		
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	780	A		
V <sub>F</sub>	1.0 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.62	V		
TJ	Range	-55 to +175	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-10BQ100PbF	UNITS	
Maximum DC reverse voltage	V <sub>R</sub>	100	V	
Maximum working peak reverse voltage	V <sub>RWM</sub>	100	v	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I <sub>F(AV)</sub>	50 % duty cycle at $T_L$ = 152 °C, rectangular waveform		1.0	А
Maximum peak one cycle	I <sub>FSM</sub>	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with	780	A
non-repetitive surge current		10 ms sine or 6 ms rect. pulse	rated $V_{RRM}$ applied	38	
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 0.5 A, L = 8 mH		1.0	mJ
Repetitive avalanche current	I <sub>AR</sub>			А	

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PRODUCT SUMMARY				
Package	SMB			
I <sub>F(AV)</sub>	1.0 A			
V <sub>R</sub>	100 V			
V <sub>F</sub> at I <sub>F</sub>	0.78 V			
I <sub>RM</sub>	1 mA at 125 °C			
T <sub>J</sub> max.	175 °C			
Diode variation	Single die			
E <sub>AS</sub>	1.0 mJ			

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RoHS COMPLIANT

## VS-10BQ100PbF



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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop See fig. 1	V <sub>FM</sub> <sup>(1)</sup>	1 A	T <sub>J</sub> = 25 °C	0.78	V
		2 A		0.89	
		1 A	- T <sub>J</sub> = 125 °C	0.62	
		2 A		0.72	
Maximum reverse leakage current See fig. 2	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	0.5	mA
		T <sub>J</sub> = 125 °C		1	
Typical junction capacitance	CT	$V_R = 5 V_{DC}$ , (test signal range 100 kHz to 1 MHz), 25 °C		42	pF
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body		2.0	nH
Maximum voltage rate of charge	dV/dt	Rated V <sub>R</sub>		10 000	V/µs

#### Note

 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2  $\,\%$ 

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> <sup>(1)</sup> , T <sub>Stg</sub>		-55 to +175	°C
Maximum thermal resistance, junction to lead	R <sub>thJL</sub> <sup>(2)</sup>	DC operation	36	°C/W
Maximum thermal resistance, junction to ambient	R <sub>thJA</sub>		80	0/10
Approximate weight			0.10	g
			0.003	oz.
Marking device		Case style SMB (similar DO-214AA)	V	IJ

#### Notes

 $\frac{dP_{tot}}{T} < \frac{R_{thJA}}{R_{thJA}}$ (1) thermal runaway condition for a diode on its own heatsink

(2) Mounted 1" square PCB

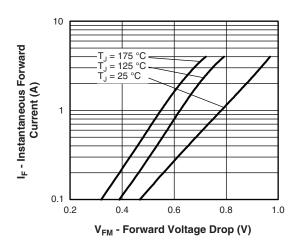


Fig. 1 - Maximum Forward Voltage Drop Characteristics

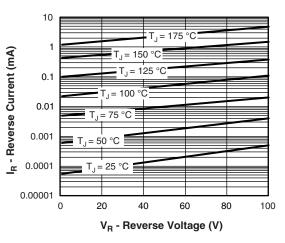


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

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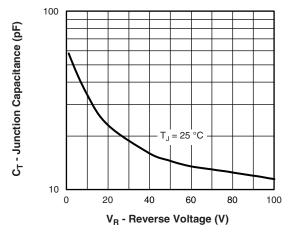


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

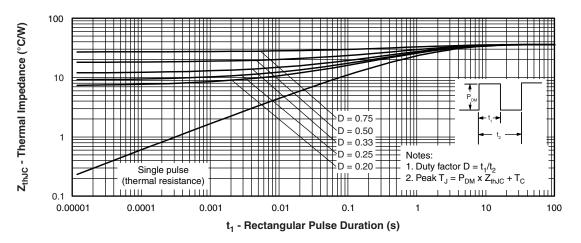
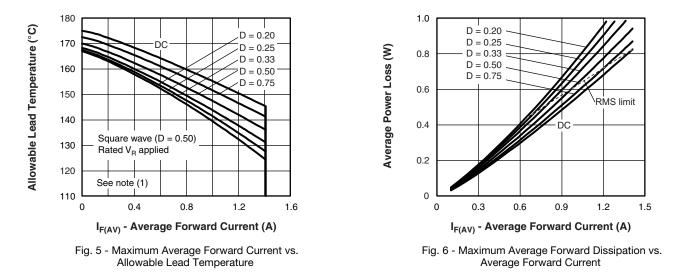


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics (Per Leg)



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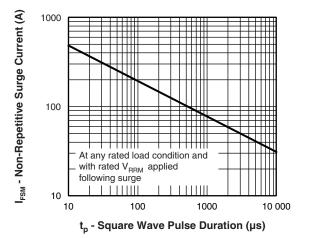
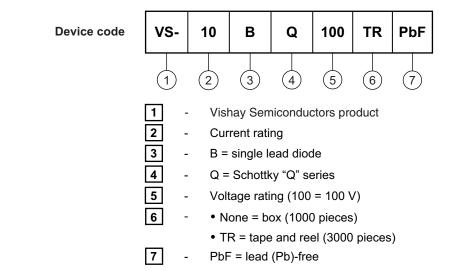


Fig. 7 - Maximum Peak Surge Forward Current vs. Pulse Duration

#### Note

 $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$ 

#### **ORDERING INFORMATION TABLE**



LINKS TO RELATED DOCUMENTS				
Dimensions	ms <u>www.vishay.com/doc?95017</u>			
Part marking information		www.vishay.com/doc?95029		
Deckering information	Tape and reel	www.vishay.com/doc?95034		
Packaging information	Bulk	www.vishay.com/doc?95397		
SPICE model		www.vishay.com/doc?95276		

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<sup>&</sup>lt;sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;

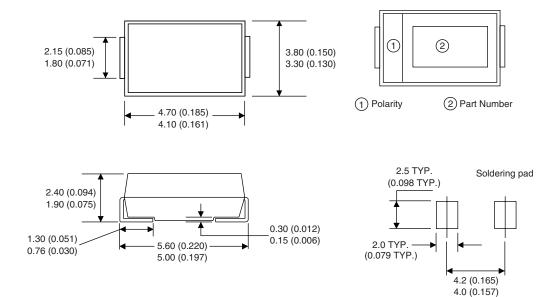


### **Outline Dimensions**

Vishay High Power Products

SMB

#### **DIMENSIONS** in millimeters (inches)





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