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3-Terminal Negative Regulators

General Description

The LM320L/LM79LXXAC dual marked series of 3-terminal negative voltage regulators features fixed output voltages of $-5V$, $-12V$, and $-15V$ with output current capabilities in excess of $100mA$. These devices were designed using the latest computer techniques for optimizing the packaged IC thermal/electrical performance. The LM79LXXAC series, even when combined with a minimum output compensation capacitor of $0.1\mu F$, exhibits an excellent transient response, a maximum line regulation of $0.07\% V_O/V$, and a maximum load regulation of $0.01\% V_O/mA$.

The LM320L/LM79LXXAC series also includes, as self-protection circuitry: safe operating area circuitry for output transistor power dissipation limiting, a temperature independent short circuit current limit for peak output current limiting, and a thermal shutdown circuit to prevent excessive junction temperature. Although designed primarily as fixed voltage regulators, these devices may be combined with simple external circuitry for boosted and/or adjustable voltages and currents. The LM79LXXAC series is available in the 3-lead TO-92

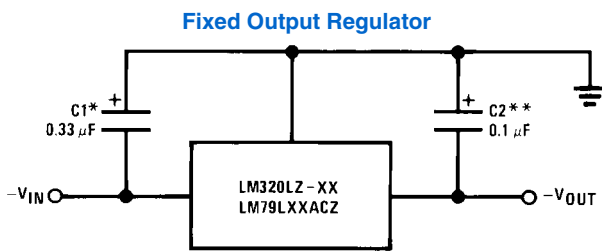
package, 8-lead SOIC package, and the 6-Bump micro SMD package. The LM320L series is available in the 3-lead TO-92 package.

For output voltage other than $-5V$, $-12V$ and $-15V$, the LM137L series provides an output voltage range from $1.2V$ to $47V$.

Features

- Preset output voltage error is less than $\pm 5\%$ overload, line and temperature
- Specified at an output current of $100mA$
- Easily compensated with a small $0.1\mu F$ output capacitor
- Internal short-circuit, thermal and safe operating area protection
- Easily adjustable to higher output voltages
- Maximum line regulation less than $0.07\% V_{OUT}/V$
- Maximum load regulation less than $0.01\% V_{OUT}/mA$
- See AN-1112 for micro SMD considerations

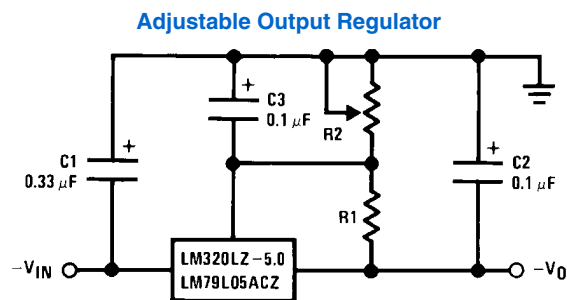
Typical Applications



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*Required if the regulator is located far from the power supply filter. A $1\mu F$ aluminum electrolytic may be substituted.

**Required for stability. A $1\mu F$ aluminum electrolytic may be substituted.



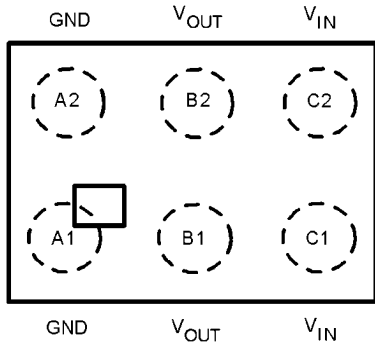
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$$-V_0 = -5V - (5V/R1 + I_O) \cdot R2,$$

$$5V/R1 > 3 I_O$$

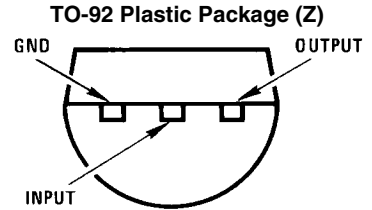
Connection Diagrams

6-Bump micro SMD



Bump Side Down

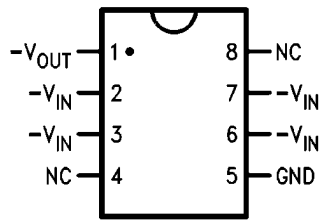
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Bottom View

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SO-8 Plastic (Narrow Body)



Top View

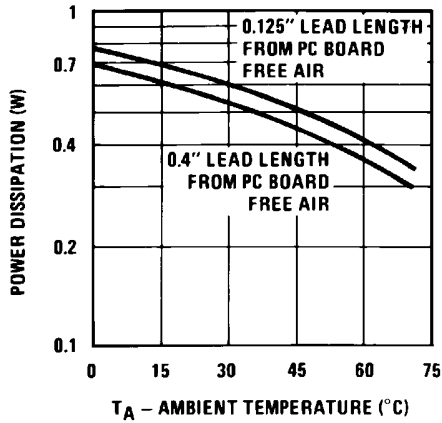
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Ordering Information

Package	Part Number	Package Marking	Transport Media	NSC Drawing
8-Lead SOIC	LM79L05ACM	LM79L05ACM	95 Units/Rail	M08A
	LM79L05ACMX		2.5k Units Tape and Reel	
	LM79L12ACM	LM79L12ACM	95 Units/Rail	
	LM79L12ACMX		2.5k Units Tape and Reel	
	LM79L15ACM	LM79L15ACM	95 Units/Rail	
	LM79L15ACMX		2.5k Units Tape and Reel	
3-Pin TO-92	LM79L05ACZ	320L79L05	1800 Units Per Box	Obsolete
	LM79L12ACZ	320L79L12	1800 Units Per Box	
	LM79L15ACZ	320L79L15	1800 Units Per Box	
6-Bump micro SMD	LM79L15ACTL	XTPB	250 Units Tape and Reel	TLA06AMA
	LM79L05ACTLX		3k Units Tape and Reel	

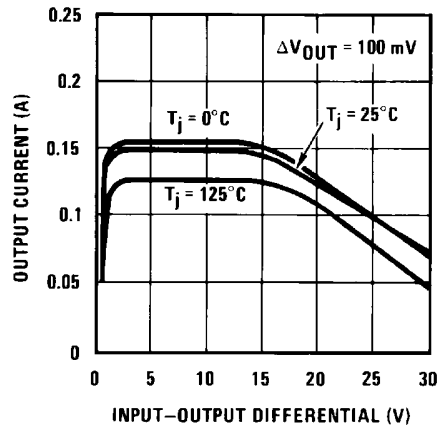
Typical Performance Characteristics

Maximum Average Power Dissipation (TO-92)



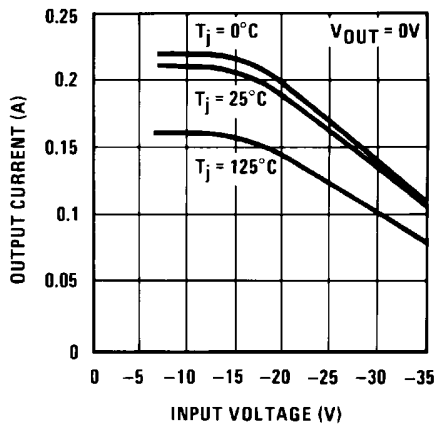
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Peak Output Current



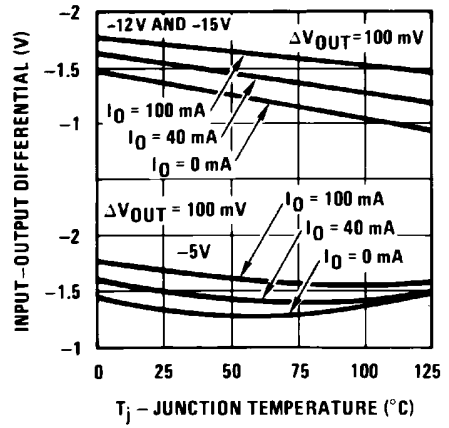
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Short Circuit Output Current



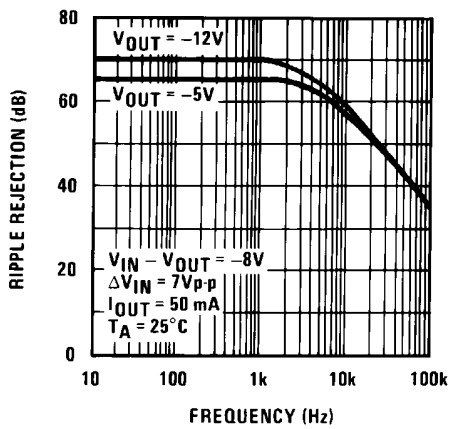
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Dropout Voltage



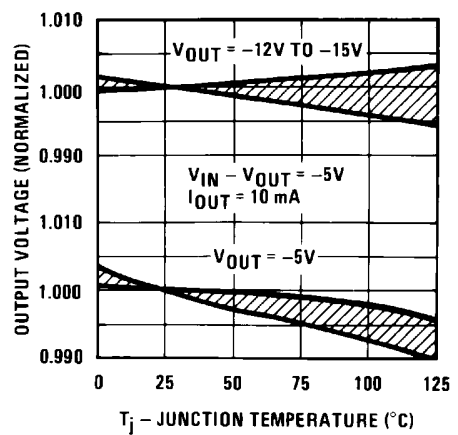
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Ripple Rejection

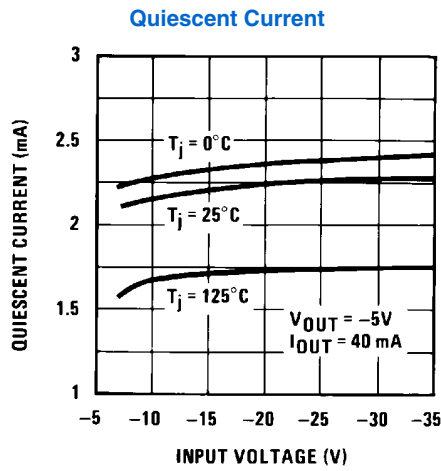


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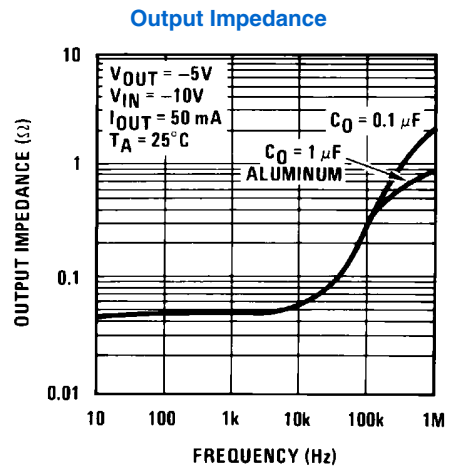
Output Voltage vs. Temperature (Normalized to 1V @ 25°C)



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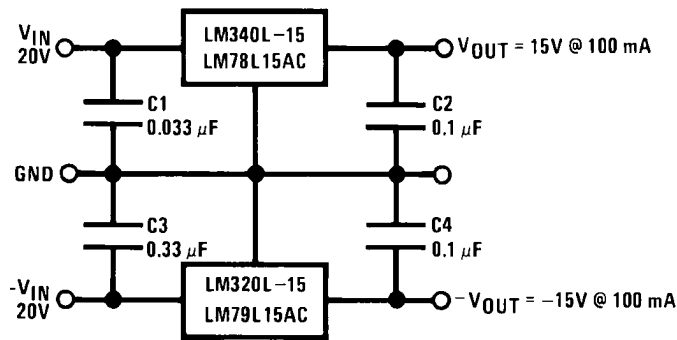
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Typical Applications

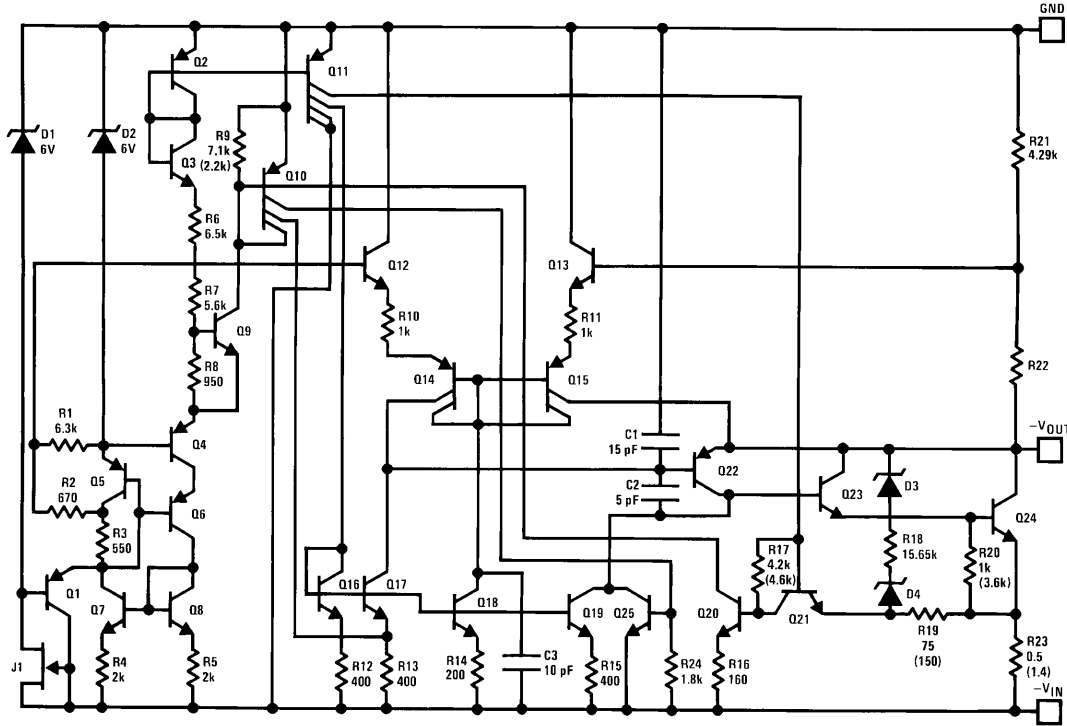
±15V, 100mA Dual Power Supply



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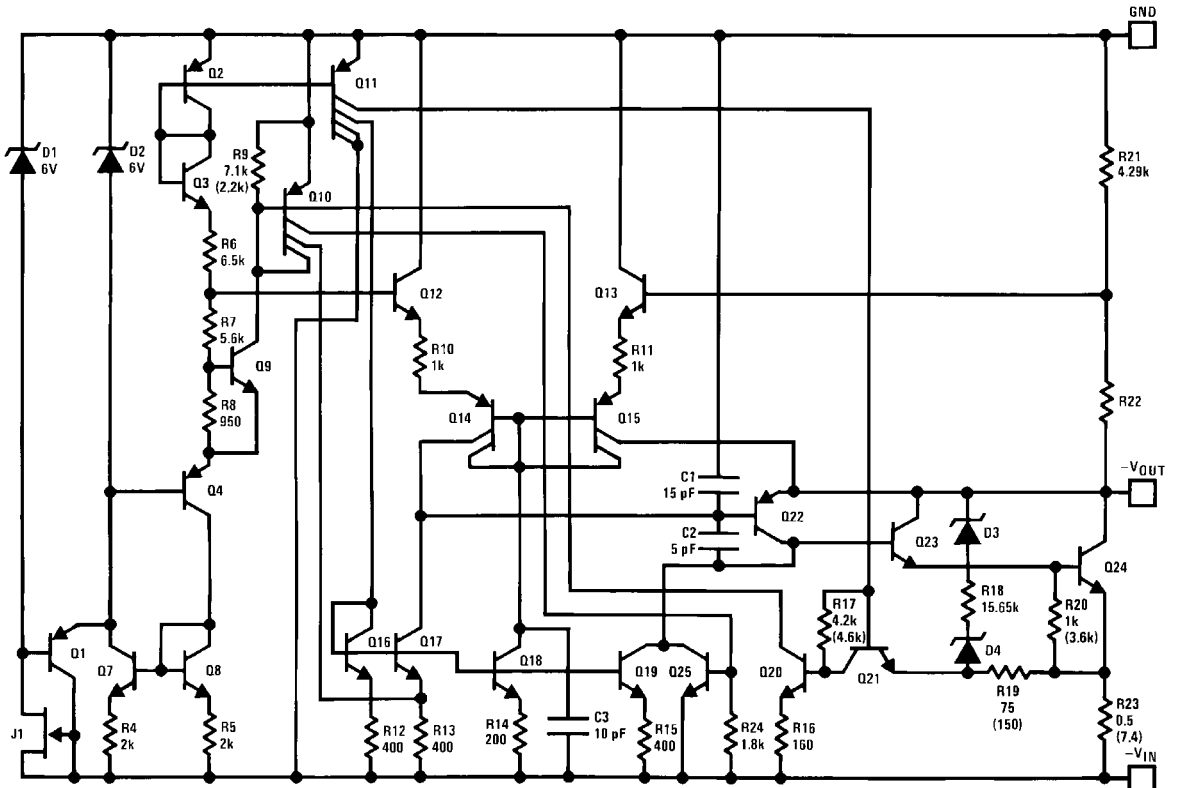
Schematic Diagrams

-5V



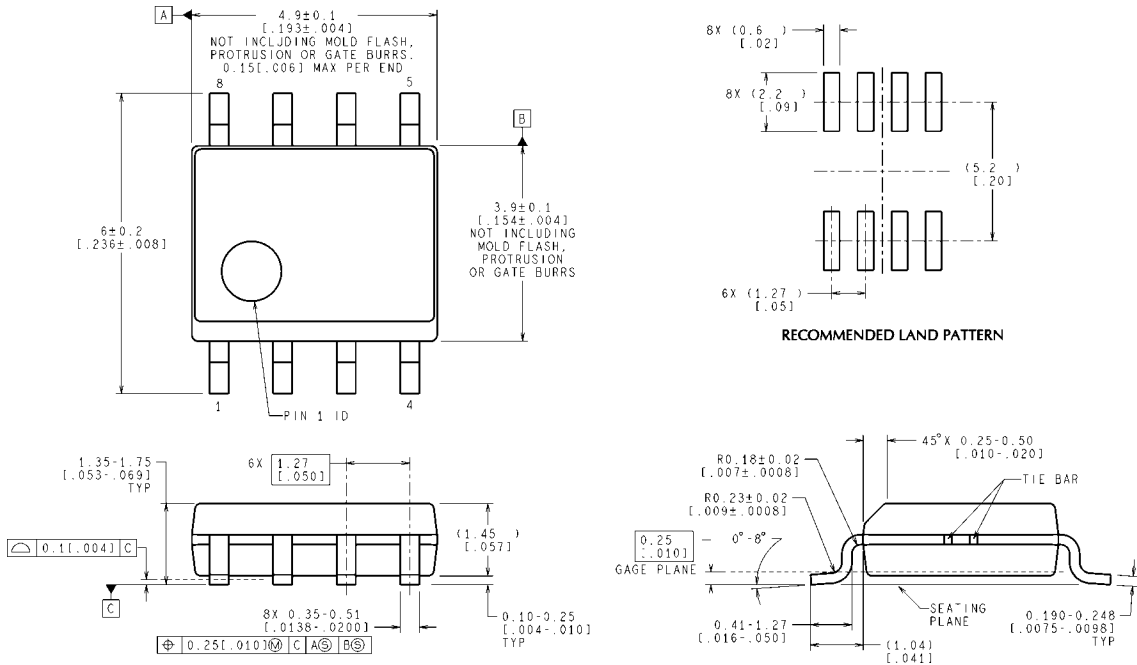
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-12V and -15V



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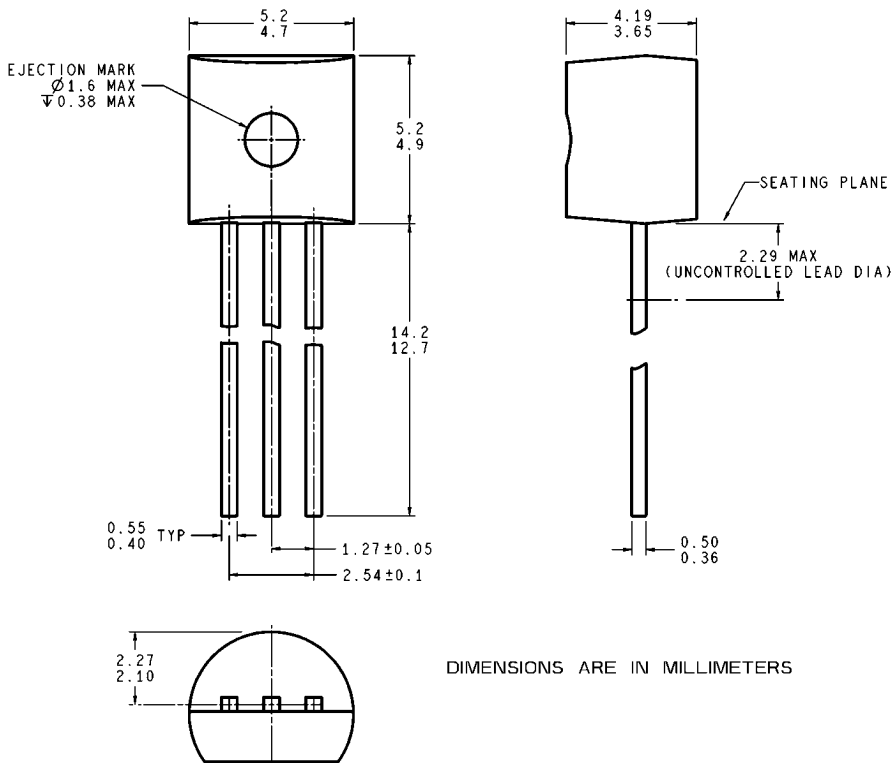
Physical Dimensions inches (millimeters) unless otherwise noted



CONTROLLING DIMENSION IS MILLIMETER
 VALUES IN [] ARE INCHES
 DIMENSIONS IN () FOR REFERENCE ONLY

SOIC Package (M)
NS Package Number M08A

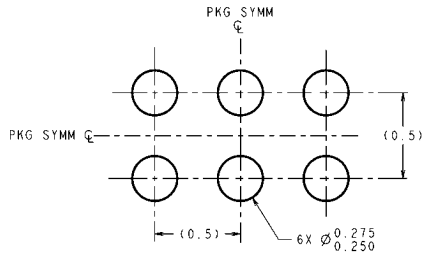
M08A (Rev M)



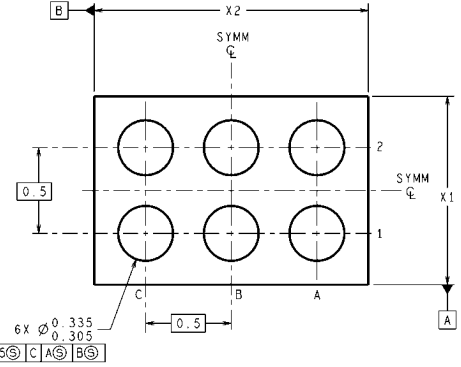
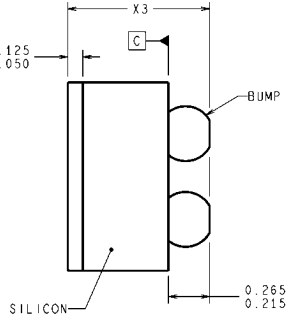
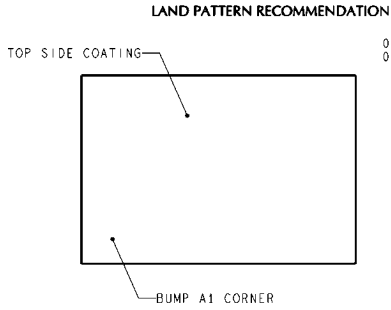
DIMENSIONS ARE IN MILLIMETERS

Molded Offset TO-92 (Z)
NS Package Number Z03A

Z03A (Rev G)



DIMENSIONS ARE IN MILLIMETERS
DIMENSIONS IN () FOR REFERENCE ONLY



TLA06XXX (Rev C)

NOTES: UNLESS OTHERWISE SPECIFIED

1. EPOXY COATING.
2. 63Sn/67Pb EUTECTIC BUMP.
3. RECOMMEND NON-SOLDER MASK DEFINED LANDING PAD.
4. PIN A1 ESTABLISHED BY LOWER LEFT CORNER WITH RESPECT TO TEXT ORIENTATION.
5. XXX IN DRAWING NUMBER REPRESENTS PACKAGE SIZE VARIATION WHERE X1 IS PACKAGE WIDTH, X2 IS PACKAGE LENGTH AND X3 IS PACKAGE HEIGHT.
6. REFERENCE JEDEC REGISTRATION MO-211, VARIATION BC.

6-Bump micro SMD
NS Package Number TLA06AMA
X₁ = 1006µm X₂ = 1793µm X₃ = 600µm

Notes

Notes

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