

NC7SZ125 TinyLogic[®] UHS Buffer with Three-State Output

Features

- Ultra-High Speed: t_{PD} 2.6 ns (Typical) into 50 pF at 5 V V_{CC}
- High Output Drive: ±24 mA at 3 V V_{CC}
- Broad V_{CC} Operating Range: 1.65 V to 5.5 V
- Matches Performance of LCX Operated at 3.3 V V_{CC}
- Pow er Dow n High-Impedance Inputs/Outputs
- Over-Voltage Tolerance Inputs Facilitate 5 V to 3 V Translation
- Proprietary Noise/EMI Reduction Circuitry
- Ultra-Small MicroPak™ Packages
- Space-Saving SOT23 and SC70 Packages

Description

The NC7SZ125 is a single buffer with three-state output from ON Semiconductor's Ultra-High Speed (UHS) of TinyLogic®. The device is fabricated with advanced CMOS technology to achieve ultra high speed with high output drive while maintaining low static power dissipation over a very broad $V_{\rm CC}$ operating range. The device is specified to operate over the 1.65 V to 5.5 V range. The inputs and output are high impedance above ground when $V_{\rm CC}$ is 0 V. Inputs tolerate voltages up to 6 V independent of $V_{\rm CC}$ operating voltage. The output tolerates voltages above $V_{\rm CC}$ when in the 3-STATE condition.

Ordering Information

Part Number	Top Mark	Package	Packing Method
NC7SZ125M5X	7Z25	5-Lead SOT23, JEDEC MO-178 1.6 mm	3000 Units on Tape & Reel
NC7SZ125P5X	Z25	5-Lead SC70, EIAJ SC-88a, 1.25 mm Wide	3000 Units on Tape & Reel
NC7SZ125L6X	DD	6-Lead MicroPak™, 1.00 mm Wide	5000 Units on Tape & Reel
NC7SZ125FHX	DD	6-Lead, MicroPak2, 1x1 mm Body, .35mm Pitch	5000 Units on Tape & Reel

Connection Diagrams

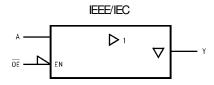
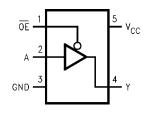


Figure 1. Logic Symbol

Pin Configurations





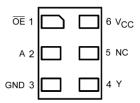


Figure 3. MicroPak™ (Top Through View)

Pin Definitions

Pin # SC70 / SOT23	Pin # MicroPak	Name	Description
1	1	OE	Input
2	2	А	Input
3	3	GND	Ground
4	4	Υ	Output
5	6	V _{CC}	Supply Voltage
	5	NC	No Connect

Function Table

Inputs		Output
/OE	In A	Out Y
L	L	L
L	Н	Н
Н	Х	Z

H = HIGH Logic Level

L = LOW Logic Level

X = HIGH or LOW Logic Level

Z = HIGH Impedance State

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parame	Min.	Max.	Unit	
V _{CC}	Supply Voltage		-0.5	6.0	V
V _{IN}	DC Input Voltage		-0.5	6.0	V
V _{OUT}	DC Output Voltage		-0.5	6.0	V
1	DC Input Diada Current	V _{IN} < -0.5 V		-50	™ A
l _{ik}	DC Input Diode Current $V_{IN} > 6.0 \text{ V}$			+20	mA
1	DC Output Diada Current	V _{OUT} < -0.5 V		-50	mΛ
l _{ok}	DC Output Diode Current	$V_{OUT} > 6 \text{ V}, V_{CC} = GND$		+20	mA
l _{out}	DC Output Current		±50	mA	
I _{CC} or I _{GND}	DC V _{CC} or Ground Current		±50	mA	
T_{STG}	Storage Temperature Range		-65	+150	°C
T_J	Junction Temperature Under Bias			+150	°C
TL	Junction Lead Temperature (Soldering	g, 10 Seconds)		+260	°C
		SOT-23		200	
В	Dow or Discipation at 1959C	SC70-5		150	
P_D	Pow er Dissipation at +85°C	MicroPak-6		130	mW
		MicroPak2-6		120	
ESD	Human Body Model, JESD22-A114			4000	V
EDD	Charged Device Model, JESD22-C101			2000	

Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. ON Semiconductor does not recommend exceeding them or designing to Absolute Maximum Ratings.

Symbol	Parameter	Conditions	Min.	Max.	Unit	
V	Supply Voltage Operating		1.65	5.50		
V _{CC}	Supply Voltage Data Retention		1.50	5.50]	
V _{IN}	Input Voltage		0	5.5	V	
V	V _{OUT} Output Voltage	Active State	0	V _{CC}	V	
V OUT		Three-State	0	5.5	7 °	
T _A	Operating Temperature		-40	+85	°C	
		V _{CC} at 1.8 V, 2.5 V ±0.2 V	0	20		
t_r, t_f	Input Rise and Fall Times	V _{CC} at 3.3 V ±0.3 V	0	10	ns/V	
		V _{CC} at 5.0 V ±0.5 V	0	5		
θ_{JA}	Thermal Resistance	SOT-23		300	°C/W	

SC70-5	425	
MicroPak-6	500	
MicroPak2-6	560	

Note:

1. Unused inputs must be held HIGH or LOW. They may not float.

DC Electrical Characteristics

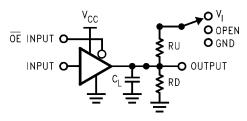
Symbol	Parameter	V _{cc}	Conditions	Т,	₄ =+25°	°C		40 to 5°C	Units
-				Min.	Тур.	Max.	Min.	Max.	
.,	HIGH Level	1.65 to 1.95		0.75V _∞			0.75V _∞		.,
V_{IH}	Input Voltage	2.30 to 5.50		0.70V _{CC}			0.70V _{CC}		V
	LOW Level	1.65 to 1.95				0.25V _{CC}		0.25V _{CC}	
V_{IL}	Input Voltage	2.30 to 5.50				0.30V _{CC}		0.30V _{CC}	V
		1.65		1.55	1.65		1.55		
		1.80		1.70	1.80		1.70		
		2.30	V _{IN} =V _{IH} , I _{OH} =-100 μA	2.20	2.30		2.20		٧
		3.00		2.90	3.00		2.90		
V	HIGH Level	4.50		4.40	4.50		4.40		
V_{OH}	Output Voltage	1.65	I _{OH} =-4 mA	1.29	1.52		1.29		
		2.30	I _{OH} =-8 mA	1.90	2.15		1.90		
		3.00	I _{OH} =-16 mA	2.40	2.80		2.40		
		3.00	I _{OH} =-24 mA	2.30	2.68		2.30		
		4.50	I _{ОН} =-32 mA	3.80	4.20		3.80		
		1.65			0.00	0.10		0.00	
		1.80			0.00	0.10		0.10	
		2.30	$V_{IN}=V_{IL},\ I_{OL}=100\ \mu A$		0.00	0.10		0.10	
		3.00			0.00	0.10		0.10	
V_{OL}	LOW Level	4.50			0.00	0.10		0.10	V
V OL	Output Voltage	1.65	I _{OL} =4 mA		0.80	0.24		0.24	V
		2.30	I _{OL} =8 mA		0.10	0.30		0.30	
		3.00	I _{OL} =16 mA		0.15	0.40		0.40	
		3.00	I _{OL} =24 mA		0.22	0.55		0.55	
		4.50	I _{OL} =32 mA		0.22	0.55		0.55	
I _{IN}	Input Leakage Current	0 to 5.5	$0 \ge V_{IN} \ge 5.5 \text{ V}$			±1		±10	μΑ
l _{oz}	3-STATE Output Leakage	0 to 5.5	$V_{IN}=V_{IH} \text{ or } V_{IL}$ $0 \ge V_O \ge 5.5 \text{ V}$			±1		±10	μΑ
l _{OFF}	Power Off Leakage Current	0	V _{IN} or V _{OUT} =5.5 V			1		10	μА
Icc	Quiescent Supply Current	1.65 to 5.50	V _{IN} =5.5 V, GND			2		20	μΑ

AC Electrical Characteristics

Cumbal	Doromotor	V	Conditions	Т	A=+25°	°C	T _A =-40 1	to +85°C	Units	Eiguro
Symbol	Parameter	V _{cc}	v _{cc} Collabora		Тур.	Max.	Min.	Max.	Units	Figure
		1.65		2.0	6.4	13.2	2.0	13.8		
		1.80	C _∟ =15 pF,	2.0	5.3	11.0	2.0	11.5		
		2.50 ±0.20	$R_D=1 M\Omega$	0.8	3.4	7.5	0.8	8.0		
t _{PLH} ,t _{PHL}	Propagation Delay	3.30 ±0.30	S ₁ =OPEN	0.5	2.5	5.2	0.5	5.5	ns	Figure 4
VI EI, VI IIE	opagaon zo.ay	5.00 ±0.50		0.5	2.1	4.5	0.5	4.8		Figure 6
		3.30 ±0.30	C _L =50 pF,	1.5	3.2	5.7	1.5	6.0		
		5.00 ±0.50	$R_D=500 \Omega$ $S_1=OPEN$	0.8	2.6	5.0	0.8	5.3		
		1.65	R _D =500 Ω RU=500 Ω S ₁ =GND for t _{PZH} S ₁ =V _{IN} for t _{PZL}	2.0	8.4	15.0	2.0	15.6	ns	
		1.80		2.0	7.0	12.5	2.0	13.0		
$t_{PZL,}t_{PZH}$	Output Enable Time	2.50 ±0.20		1.5	4.6	8.5	1.5	9.0		
		3.30 ±0.30		1.5	3.5	6.2	1.5	6.5		
		5.00 ±0.50		0.8	2.8	5.5	0.8	5.8		Figure 4
		1.65	C _L =50 pF,	2.0	6.5	13.2	2.0	14.5	IIS	Figure 6
		1.80	R _D =500 Ω	2.0	5.4	11.0	2.0	12.0		
$t_{\text{PLZ},}t_{\text{PHZ}}$	Output Disable Time	2.50 ±0.20	RU=500 Ω S ₁ =GND for t _{PHZ}	1.5	3.5	8.0	1.5	8.5		
		3.30 ±0.30	$S_1 = V_{IN}$ for t_{PLZ}	1.0	2.8	5.7	1.0	6.0		
		5.00 ±0.50	V _{IN} =2•V _{CC}	0.5	2.1	4.7	0.5	5.0		
C _{IN}	Input Capacitance	0.00			4				pF	
C _{OUT}	Output Capacitance	0.00			8					
	Power Dissipation	3.30			17					-
C_{PD}	Capacitance ⁽²⁾	5.00			24				pF	Figure 5

Note:

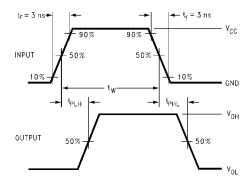
2. C_{PD} is defined as the value of the internal equivalent capacitance which is derived from dynamic operating current consumption (I_{CCD}) at no output lading and operating at 50% duty cycle. C_{PD} is related to I_{CCD} dynamic operating current by the expression: $I_{CCD} = (C_{PD})(V_{CC})(f_{IN}) + (I_{CC} \text{static})$.

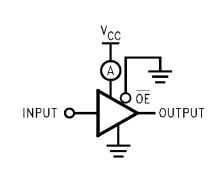


Note:

3. C_L includes load and stray capacitance. Input PRR=1.0 MHz, t_W =500 ns.

Figure 4. AC Test Circuit





Note:

4. Input=AC Waveform; $t_r=t_f=1.8$ ns; PRR=10 MHz; Duty Cycle=50%.

Figure 5. I_{CCD} Test Circuit

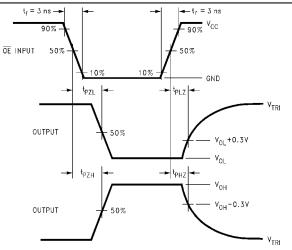


Figure 6. AC Waveforms

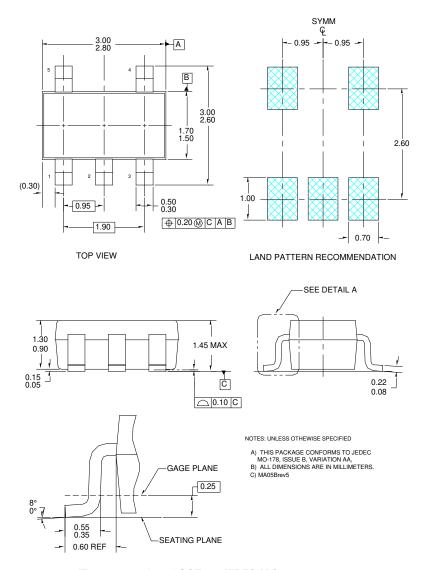


Figure 7. 5-Lead SOT23, JEDEC MO-178 1.6 mm

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Package Designator	Tape Section	Cavity Number	Cavity Status	Cover Type Status
	Leader (Start End)	125 (Typical)	Empty	Sealed
M5X	Carrier	3000	Filled	Sealed
	Trailer (Hub End)	75 (Typical)	Empty	Sealed

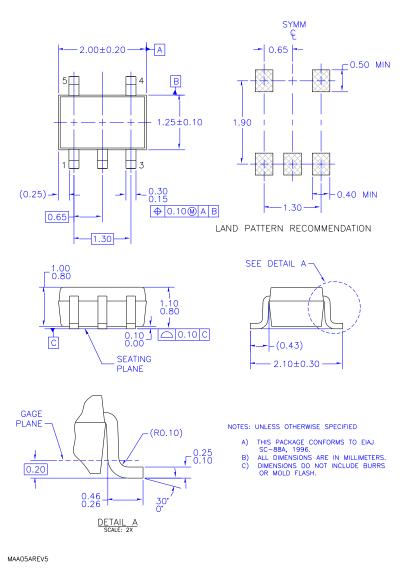
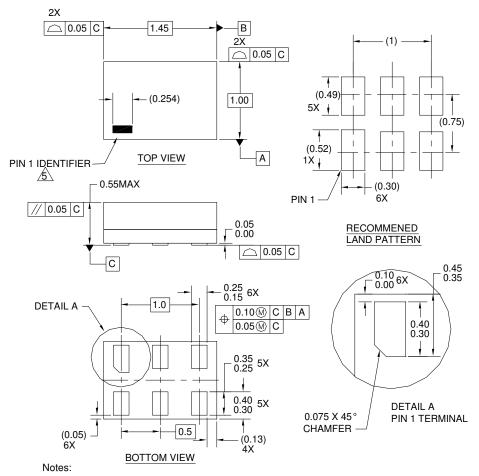


Figure 8. 5-Lead, SC70, EAJ SC-88a, 1.25 mm Wide

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Package Designator	Tape Section	pe Section		Cover Type Status
	Leader (Start End)	125 (Typical)	Empty	Sealed
P5X	Carrier	3000	Filled	Sealed
	Trailer (Hub End)	75 (Typical)	Empty	Sealed



- 1. CONFORMS TO JEDEC STANDARD M0-252 VARIATION UAAD
- 2. DIMENSIONS ARE IN MILLIMETERS
- 3. DRAWING CONFORMS TO ASME Y14.5M-1994
- 4. FILENAME AND REVISION: MAC06AREV4
- 5 PIN ONE IDENTIFIER IS 2X LENGTH OF ANY
 - OTHER LINE IN THE MARK CODE LAYOUT.

Figure 9. 6-Lead, MicroPak™, 1.0 mm Wide

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L6X	Carrier	5000	Filled	Sealed
	Trailer (Hub End)	75 (Typical)	Empty	Sealed

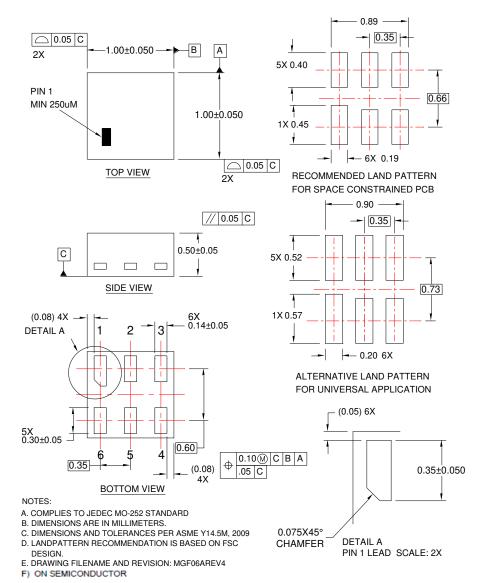


Figure 10. 6-Lead, MicroPak2, 1x1 mm Body, .35 mm Pitch

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Package Designator	Tape Section	Cavity Number	Cavity Status	Cover Type Status
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FHX	Carrier	5000	Filled	Sealed
	Trailer (Hub End)	75 (Typical)	Empty	Sealed

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