

# R1560x Series

**AEC-Q100** Compliant

# 60 V 100 mA Ultra-low Power Voltage Regulator for Automotive Applications

No. EC-395-220114

#### OVERVIEW

The R1560x is a CMOS-based ultra-low power voltage regulator for automotive applications featuring 60 V input voltage and 100 mA output current. The device includes a short current limit circuit, an overcurrent protection circuit and a thermal shutdown. These features make the R1560x an ideal constant voltage power source for on-vehicle accessories and control units.

### **KEY BENEFITS**

•

- Supply current is as low as Typ. 3.0 µA, which can reduce battery consumption at an engine stop.
- The input voltage range is as wide as 5.5 V to 60 V, and the output voltage accuracy is as high as ±0.8%.
- High heat dissipation and space-saving HSOP-6J and TO-252-5-P2 packages.

#### KEY SPECIFICATIONS

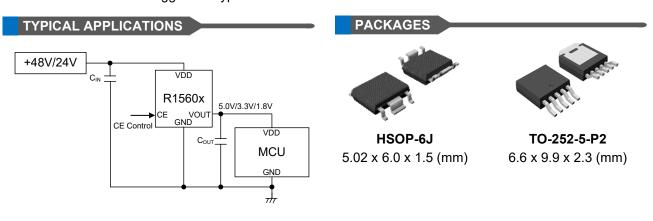
- Input Voltage Range (Max. Rating): 5.5 V to 60 V (80 V)
- Operating Temperature Range: -40°C to 125°C
- Supply Current: Typ. 3.0 μA
- Dropout Voltage: Typ. 1.5 V
  - (Iout = 100 mA, Vout = 5.0 V)
  - Output Voltage Accuracy: ±0.8% (Ta = 25°C)
- Temp. Coefficient of Output Voltage: Typ. ±100 ppm/°C
- Line Regulation: Typ. 0.01%/V (6 V  $\leq$  V<sub>IN</sub>  $\leq$  60 V)
- Short-circuit Current Limiting: limits to Typ. 50 mA
- Overcurrent Protection: triggers at Typ. 150 mA
- Thermal Shutdown: triggers at Typ.165°C

#### SELECTION GUIDE

Product Name	Package
R1560Sxx1B-E2-#E	HSOP-6J
R1560Jxx1B-T1-#E	TO-252-5-P2

xx: Set Output Voltage (V<sub>SET</sub>)

1.8 V (18) / 2.5 V (25) / 2.8 V (28) / 3.0 V (30) / 3.3 V (33) / 3.4 V (34) / 5.0 V (50) / 7.0 V (70) / 8.0 V (80) / 9.0 V (90) / 10.0 V (A0) / 12.0 V (C0) / 14.0 V (E0)



#### APPLICATIONS

- EV Inverters and Charge Control Units
- 48-V Battery Systems (Mild Hybrid) and BMS

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## **SELECTION GUIDE**

The output voltage, the package type and the quality class are user-selectable options.

## Selection Guide

Product Name	Package	Package Quantity per Reel		Halogen Free
R1560Sxx1B-E2-#E	HSOP-6J	1,000 pcs	Yes	Yes
R1560Jxx1B-T1-#E	TO-252-5-P2	3,000 pcs	Yes	Yes

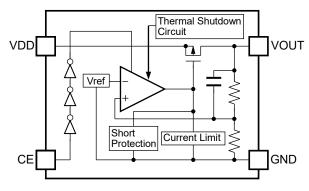
## xx: Set Output Voltage (VSET)

1.8 V (18) / 2.5 V (25) / 2.8 V (28) / 3.0 V (30) / 3.3 V (33) / 3.4 V (34) / 5.0 V (50) / 7.0 V (70) / 8.0 V (80) / 9.0 V (90) / 10.0 V (A0) / 12.0 V (C0) / 14.0 V (E0)

#: Quality Class

#	Operating Temp. Range	Test Temp.
А	−40°C to 125°C	25°C, High
К	−40°C to 125°C	Low, 25°C, High

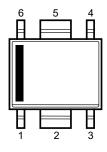
## **BLOCK DIAGRAM**



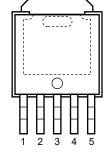
R1560x Block Diagram

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# **PIN DESCRIPTIONS**



## **HSOP-6J Pin Configuration**



## TO-252-5-P2 Pin Configuration

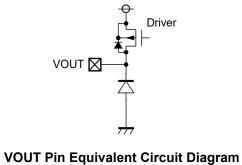
### HSOP-6J Pin Description

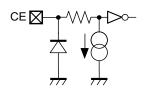
Pin No.	Pin Name	Description
1	VOUT	Output Pin
2	GND <sup>(1)</sup>	Ground Pin
3	CE	Chip Enable Pin, Active-high
4	GND <sup>(1)</sup>	Ground Pin
5	GND <sup>(1)</sup>	Ground Pin
6	VDD	Input Pin

## TO-252-5-P2 Pin Description

Pin No.	Pin Name	Description
1	VDD	Input Pin
2	NC	No Connection
3	GND	Ground Pin
4	VOUT	Output Pin
5	CE	Chip Enable Pin, Active-high

## Pin Equivalent Circuit Diagrams





## CE Pin Equivalent Circuit Diagram

<sup>(1)</sup> The GND pins are connected to each other on the board.

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# ABSOLUTE MAXIMUM RATINGS

#### **Absolute Maximum Ratings**

Symbol		Parameter	Rating	Unit	
VIN	Input Voltage			-0.3 to 80	V
VIN	Peak Inrush Voltage	.(1)		90	V
VCE	CE Pin Input Voltage	Э		-0.3 to 80	V
Vout	Output Voltage			-0.3 to V <sub>IN</sub> + 0.3 ≤ 80	V
Іоит	Output Current			150	mA
PD	Power	HSOP-6J	JEDEC STD.51-7	3400	mW
PD	Dissipation <sup>(2)</sup>	TO-252-5-P2	4800	mvv	
Tj	Junction Temperatur	re	-40 to 150	°C	
Tstg	Storage Temperatur	e Range	-55 to 150	°C	

#### **ABSOLUTE MAXIMUM RATINGS**

Electronic and mechanical stress momentarily exceeded absolute maximum ratings may cause permanent damage and may degrade the life time and safety for both device and system using the device in the field. The functional operation at or over these absolute maximum ratings is not assured.

# **RECOMMENDED OPERATING CONDITIONS**

#### **Recommended Operating Conditions**

Symbol	Parameter	Rating	Unit
VIN	Input Voltage	5.5 to 60	V
Та	Operating Temperature Range	−40 to 125	°C

#### RECOMMENDED OPERATING CONDITONS

All of electronic equipment should be designed that the mounted semiconductor devices operate within the recommended operating conditions. The semiconductor devices cannot operate normally over the recommended operating conditions, even if they are used over such conditions by momentary electronic noise or surge. And the semiconductor devices may receive serious damage when they continue to operate over the recommended operating conditions.

<sup>&</sup>lt;sup>(1)</sup> Duration: 200 ms or less

<sup>&</sup>lt;sup>(2)</sup> Refer to POWER DISSIPATION for detailed information.

# **ELECTRICAL CHARACTERISTICS**

 $C_{IN} = 0.1 \ \mu F / C_{OUT} = 0.1 \ \mu F$ , unless otherwise noted.

The specifications surrounded by are guaranteed by design engineering at  $-40^{\circ}C \le Ta \le 125^{\circ}C$ .

Symbol	Parameter	Test Con	ditions/C	omments	Min.	Тур.	Max.	Unit
-		V <sub>IN</sub> = 14 V V <sub>CE</sub> = 14 V I <sub>OUT</sub> = 0 mA		V <sub>SET</sub> ≤ 5.0 V		3.0	8.0	
lss	Supply Current	V <sub>IN</sub> = 18 V V <sub>CE</sub> = 18 V I <sub>OUT</sub> = 0 mA		V <sub>SET</sub> > 5.0 V		3.5	12	- μA
Istandby	Standby Current	V <sub>IN</sub> = 60 V, V <sub>CE</sub>	= 0 V			0.1	2.0	μA
		$V_{SET} \le 5.0 V$	Ta = 25°	С	×0.992		×1.008	
		V <sub>IN</sub> = 14 V I <sub>OUT</sub> = 1 mA	-40°C ≤	Ta ≤ 125°C	×0.985		×1.015	
V <sub>OUT</sub>	Output Voltage	V <sub>SET</sub> > 5.0 V	Ta = 25°	С	×0.988		×1.012	V
		V <sub>IN</sub> = 18 V I <sub>OUT</sub> = 1 mA	-40°C ≤	Ta ≤ 125°C	×0.980 ×1.02			0
ΔVουτ /ΔΙουτ	Load Regulation	$V_{IN} = 8 V (V_{SET} + 3)$ $V_{IN} = V_{SET} + 3$ $1 mA \le I_{OUT} \le 1$	Refer to Voltage-specific Electrical Characteristics					
$\Delta V_{OUT}$	Line Degulation			V <sub>SET</sub> ≤ 5.0 V	-0.02	0.01	0.02	-%/V
$/\Delta V_{IN}$	Line Regulation	V <sub>SET</sub> +1 V ≤ V <sub>IN</sub> I <sub>OUT</sub> = 1 mA	i ≤ 60 V	V <sub>SET</sub> > 5.0 V	-0.06	0.03	0.06	70/ V
VDIF	Dropout Voltage	І <sub>оит</sub> = 100 mA			Refer to Electrica			
ILIM	Output Current Limit	V <sub>IN</sub> = 8.0 V (Vs V <sub>IN</sub> = V <sub>SET</sub> + 3 V			100	150	250	mA
lsc	Short-circuit Current	V <sub>IN</sub> = 8.0 V (V <sub>S</sub> V <sub>IN</sub> = V <sub>SET</sub> + 3 V V <sub>OUT</sub> = 0 V	$V_{IN} = 8.0 V (V_{SET} \le 5.0 V)$ $V_{IN} = V_{SET} + 3 V (V_{SET} > 5.0 V)$		20	50	75	mA
VCEH	CE Input Voltage "H"	V <sub>IN</sub> = 60 V			3.0		60	V
VCEL	CE Input Voltage "L"	V <sub>IN</sub> = 60 V		0		0.3	V	
IPD	CE Pull-down Current	V <sub>IN</sub> = 60 V, V <sub>CE</sub>	= 3 V			0.4	0.8	μA
T <sub>TSD</sub>	Thermal Shutdown Temperature	Junction Temperature		150	165		°C	
T <sub>TSR</sub>	Thermal Shutdown Release Temperature <sup>(1)</sup>	Junction Tempe	erature		125	135		°C

All parameters are tested under the pulse load condition (Tj  $\approx$  Ta = 25°C).

<sup>(1)</sup> If the VDD and CE pins are turned on at the same time when Ta > 125°C, the thermal shutdown can be activated.

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<u>R1560x (-AE) Pr</u>	oduct-sp	oecific E	Electrica	I Charact	eristic	s				(Ta = 25°C)		
Product		V <sub>оит</sub> (V) (Ta = 25°С)			V <sub>о∪т</sub> (V) (−40°С ≤ Та ≤ 125°С)			ΔV <sub>OUT</sub> /ΔI <sub>OUT</sub> (mV)			V <sub>DIF</sub> (V)	
Name	Min.	Тур.	Max.	Min.	Тур.	Max.	Min.	Тур.	Max.	Тур.	Max.	
R1560x181B	1.7856	1.80	1.8144	1.7730	1.80	1.8270				3.7	4.0	
R1560x251B	2.4800	2.50	2.5200	2.4625	2.50	2.5375				3.0	3.6	
R1560x281B	2.7776	2.80	2.8224	2.7580	2.80	2.8420				2.7	3.6	
R1560x301B	2.9760	3.00	3.0240	2.9550	3.00	3.0450	-300	30	300	2.5	3.6	
R1560x331B	3.2736	3.30	3.3264	3.2505	3.30	3.3495				2.2	3.0	
R1560x341B	3.3728	3.40	3.4272	3.3490	3.40	3.4510				2.1	3.0	
R1560x501B	4.9600	5.00	5.0400	4.9250	5.00	5.0750				1.5	3.0	
R1560x701B	6.9160	7.00	7.0840	6.8600	7.00	7.1400				1.5	3.0	
R1560x801B	7.9040	8.00	8.0960	7.8400	8.00	8.1600				1.5	3.0	
R1560x901B	8.8920	9.00	9.1080	8.8200	9.00	9.1800	-600	60	600	1.5	3.0	
R1560xA01B	9.8800	10.00	10.120	9.8000	10.0	10.200	-000	00	000	1.5	3.0	
R1560xC01B	11.856	12.00	12.144	11.760	12.0	12.240					1.5	3.0
R1560xE01B	13.832	14.00	14.168	13.720	14.0	14.280				1.5	3.0	

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•	(E) Electrical Characteri						°C ≤ Ta ≤	
Symbol	Parameter	Test Conditions/Comments		Min.	Тур.	Max.	Unit	
	Quere la Quere et	V <sub>IN</sub> = 14 V V <sub>CE</sub> = 14 V I <sub>OUT</sub> = 0 mA		V <sub>SET</sub> ≤ 5.0 V		3.0	8.0	
I <sub>SS</sub>	Supply Current	V <sub>IN</sub> = 18 V V <sub>CE</sub> = 18 V I <sub>OUT</sub> = 0 mA		V <sub>SET</sub> > 5.0 V		3.5	12	μA
Istandby	Standby Current	V <sub>IN</sub> = 60 V, Vo	<sub>CE</sub> = 0 V			0.1	2.0	μA
		V <sub>SET</sub> ≤ 5.0 V	Ta = 25°	С	×0.992		×1.008	
M		V <sub>IN</sub> = 14 V I <sub>OUT</sub> = 1 mA	-40°C ≤	Ta ≤ 125°C	×0.985		×1.015	v
Vout Output Voltage	Oulput voltage	V <sub>SET</sub> > 5.0 V	Ta = 25°	С	×0.988		×1.012	V
		V <sub>IN</sub> = 18 V I <sub>OUT</sub> = 1 mA	-40°C ≤	Ta ≤ 125°C	×0.980		×1.020	
ΔVουτ /ΔΙουτ	Load Regulation	$V_{IN} = 8 V (V_{SE})$ $V_{IN} = V_{SET} + 3$ $1 \text{ mA} \le I_{OUT} \le 1$	Refer to Voltage-specific Electrical Characteristics					
$\Delta V_{OUT}$	Line Regulation	$6 V \leq V_{\rm W} \leq 60 V$		$V_{SET} \le 5.0 V$	-0.02	0.01	0.02	%/V
$/\Delta V_{\rm IN}$	Line Regulation	$V_{SET}$ +1 V $\leq$ VI I <sub>OUT</sub> = 1 mA	<sub>N</sub> ≤ 60 V	V <sub>SET</sub> > 5.0 V	-0.06	0.03	0.06	70/ V
VDIF	Dropout Voltage	Ι <sub>ουτ</sub> = 100 mA	A		Refer to Voltage-specific Electrical Characteristics			
ILIM	Output Current Limit	V <sub>IN</sub> = 8.0 V (V V <sub>IN</sub> = V <sub>SET</sub> + 3			100	150	250	mA
I <sub>SC</sub>	Short-circuit Current	V <sub>IN</sub> = 8.0 V (V V <sub>IN</sub> = V <sub>SET</sub> + 3 V <sub>OUT</sub> = 0 V	′ <sub>SET</sub> ≤ 5.0 S V (V <sub>SET</sub> >	V) > 5.0 V)	20	50	75	mA
VCEH	CE Input Voltage "H"	V <sub>IN</sub> = 60 V			3.0		60	V
VCEL	CE Input Voltage "L"	V <sub>IN</sub> = 60 V		0		0.3	V	
IPD	CE Pull-down Current	V <sub>IN</sub> = 60 V, V <sub>CE</sub> = 3 V			0.4	0.8	μA	
T <sub>TSD</sub>	Thermal Shutdown Temperature	Junction Temperature		150	165		°C	
T <sub>TSR</sub>	Thermal Shutdown Release Temperature <sup>(1)</sup>	Junction Temp	perature		125	135		°C

 $C_{IN} = 0.1 \ \mu\text{F} / C_{OUT} = 0.1 \ \mu\text{F}$ , unless otherwise noted. **B1560x** (-KE) Electrical Characteristics

All parameters are tested under the pulse load condition (Tj  $\approx$  Ta = 25°C).

<sup>(1)</sup> If the VDD and CE pins are turned on at the same time when Ta >  $125^{\circ}$ C, the thermal shutdown can be activated.

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<u>R1560x (-KE) Pr</u>	R1560x (-KE) Product-specific Electrical Characteristics								(-4	0°C ≤ Ta	≤ 125°C)	
Product		V <sub>оит</sub> (V) (Ta = 25°С)			V <sub>ОUT</sub> (V) (−40°C ≤ Ta ≤ 125°C)			ΔV <sub>OUT</sub> /ΔI <sub>OUT</sub> (mV)			V <sub>DIF</sub> (V)	
Name	Min.	Тур.	Max.	Min.	Тур.	Max.	Min.	Тур.	Max.	Тур.	Max.	
R1560x181B	1.7856	1.80	1.8144	1.7730	1.80	1.8270				3.7	4.0	
R1560x251B	2.4800	2.50	2.5200	2.4625	2.50	2.5375		30		3.0	3.6	
R1560x281B	2.7776	2.80	2.8224	2.7580	2.80	2.8420				2.7	3.6	
R1560x301B	2.9760	3.00	3.0240	2.9550	3.00	3.0450	-300		300	2.5	3.6	
R1560x331B	3.2736	3.30	3.3264	3.2505	3.30	3.3495				2.2	3.0	
R1560x341B	3.3728	3.40	3.4272	3.3490	3.40	3.451						2.1
R1560x501B	4.9600	5.00	5.0400	4.9250	5.00	5.0750				1.5	3.0	
R1560x701B	6.9160	7.00	7.0840	6.8600	7.00	7.1400				1.5	3.0	
R1560x801B	7.9040	8.00	8.0960	7.8400	8.00	8.1600			60 600	1.5	3.0	
R1560x901B	8.8920	9.00	9.1080	8.8200	9.00	9.1800	-600	60		1.5	3.0	
R1560xA01B	9.8800	10.00	10.120	9.8000	10.0	10.200		-600 60		1.5	3.0	
R1560xC01B	11.856	12.00	12.144	11.760	12.0	12.240				1.5	3.0	
R1560xE01B	13.832	14.00	14.168	13.720	14.0	14.280				1.5	3.0	

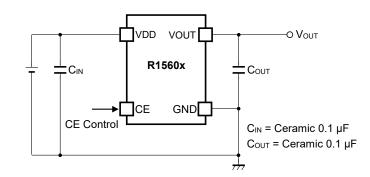
R1560x (-KE) Product-specific Electrical Characteristics

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# THEORY OF OPERATION

## **Thermal Shutdown**

If the junction temperature increases above 165°C (Typ.), the operation of the regulator would stop. And if the junction temperature decreases below 135°C (Typ.), the operation of the regulator would restart. Unless the causes of temperature rising are removed, the regulator repeats turning on and off and the output waveform becomes a pulse shape.

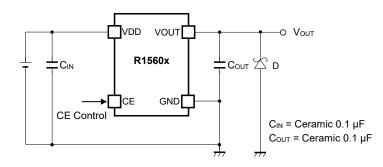


# **APPLICATION INFORMATION**

**R1560x Typical Applications** 

## Typical Application for IC Chip Breakdown Prevention

When a sudden surge of electrical current travels along the VOUT pin and GND due to a short-circuit, electrical resonance of a circuit involving an output capacitor ( $C_{OUT}$ ) and a short circuit inductor generates a negative voltage and may damage the device or the load devices. Connecting a schottky diode (D) between the VOUT pin and GND has the effect of preventing damage to them.

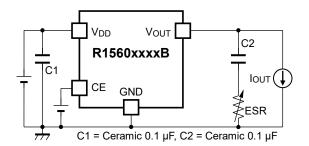


R1560x Typical Application for IC Chip Breakdown Prevention

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## Equivalent Series Resistance vs. Output Current

It is recommended that a ceramic type capacitor be used for this device. However, other types of capacitors having lower ESR can also be used. The relation between the output current ( $I_{OUT}$ ) and the ESR of output capacitor is shown below.



## **Measurement Conditions**

Frequency Band: 10 Hz to 2 MHz Measurement Temperature:  $-40^{\circ}$ C to 125°C Capacitor: C1 = Ceramic 0.1 µF, C2 = Ceramic 0.1 µF ESR: 0 to 100  $\Omega$ V<sub>OUT</sub>: 1.8 V, 5.0 V It is confirmed that the output noise level is less than the specified value (40 µVrms) under the measurement conditions above.

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# **TECHNICAL NOTES**

The performance of a power source circuit using this device is highly dependent on a peripheral circuit. A peripheral component or the device mounted on PCB should not exceed its rated voltage, rated current or rated power. When designing a peripheral circuit, please be fully aware of the following points.

## Phase Compensation

A phase compensation is provided to secure stable operation even when the load current is varied. For this purpose, use a  $0.1-\mu$ F or more output capacitor (C<sub>OUT</sub>) with good frequency characteristics and proper ESR (Equivalent Series Resistance). In case of using a tantalum type capacitor with a large ESR, the output might become unstable. Evaluate your circuit including consideration of frequency characteristics. Connect a  $0.1-\mu$ F or more input capacitor (C<sub>IN</sub>) between the VDD and GND pins with shortest-distance wiring.

## PCB Layout

As for the HSOP-6J package, ensure that the GND pins (Pin No. 2, 4 and 5) are connected to each other and the ground plane.

## Operating the Device below the Minimum Operating Voltage

Operating the device below the recommended operating voltage range can make the output voltage unstable and make the output voltage higher than the set output voltage ( $V_{SET}$ ) of the device.

In the case of turning on the VIN and CE pins at the same time, both pins must be turned on using a 100-V/ms or more slew rate in order to prevent the unstable operation upon start-up. In the case of turning on the VIN pin using a 100-V/ms or less slew rate, the CE pin must be turned on after the supply voltage becomes 5.5 V or more.

In the case of turning off the VIN and CE pins at the same time, both pins must be turned off using a steep slew rate, -100 V/ms or higher in order to prevent the unstable operation. In the case of turning off the VIN pin using a slow rate, lower than -100 V/ms, the CE pin must be turned off before the supply voltage decreases to 5.5 V.

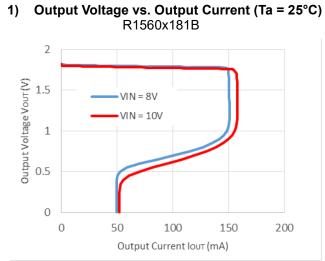
## **Transient Response**

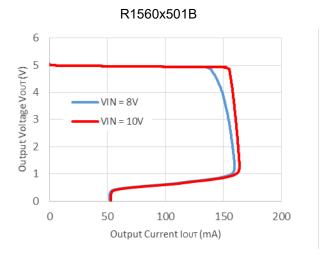
An output ceramic capacitor of  $C_{OUT} = 0.1 \ \mu\text{F}$  prevents R1560x series from phase oscillation to ensure the IC's stable operation. However, variation in input voltage and load current would lead to an unstable output voltage which fails to meet the requirements of the system. Especially, in a high output version:  $V_{SET} > 5 \ V$ , this results in slow response of the IC and a great variation in output. To avoid this problem, use a ceramic capacitor of  $C_{OUT} = 10 \ \mu\text{F}$  or more to minimize variation in output. Place the capacitor as close as possible to and outside of the IC when the electrolytic capacitor is used as an output line element.

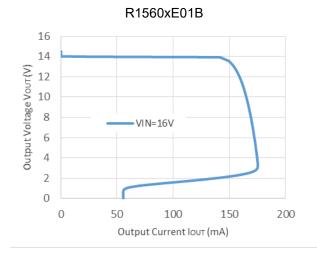
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# **TYPICAL CHARACTERISTICS**

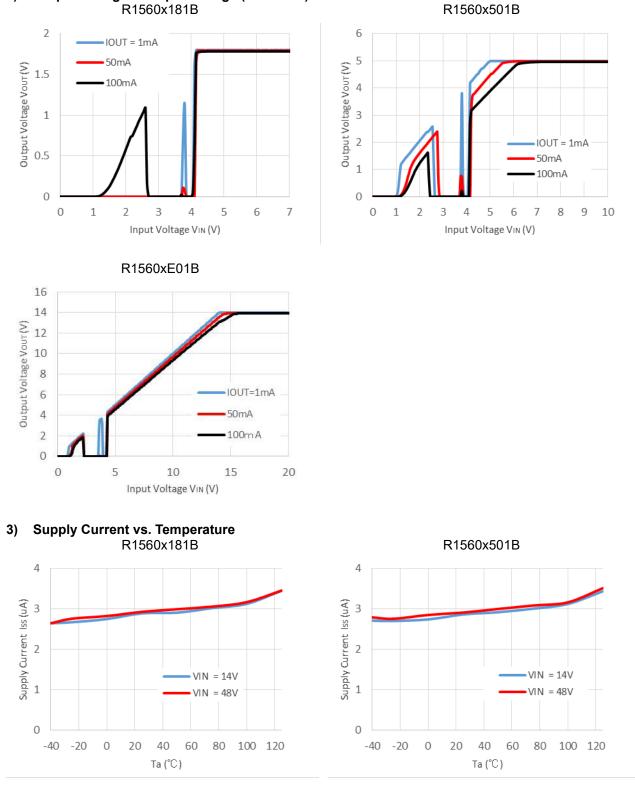
Note: Typical Characteristics are intended to be used as reference data; they are not guaranteed.





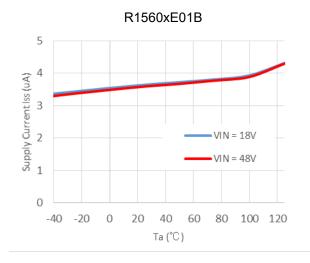


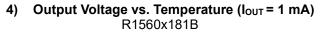
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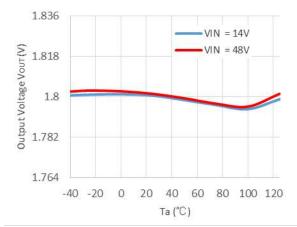


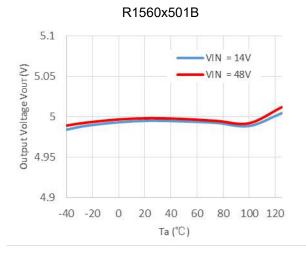
#### 2) Output Voltage vs. Input Voltage (Ta = 25°C) R1560x181B

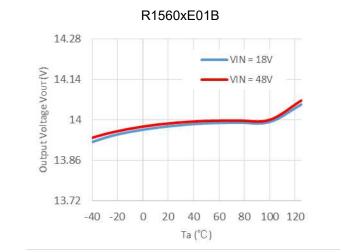
No. EC-395-220114





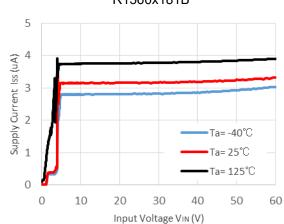




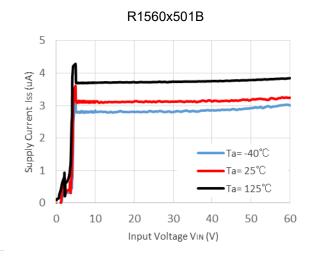


## <u>R1560x</u>

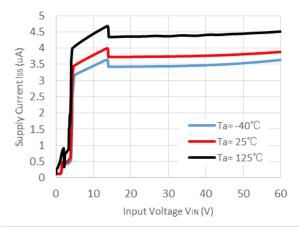
No. EC-395-220114

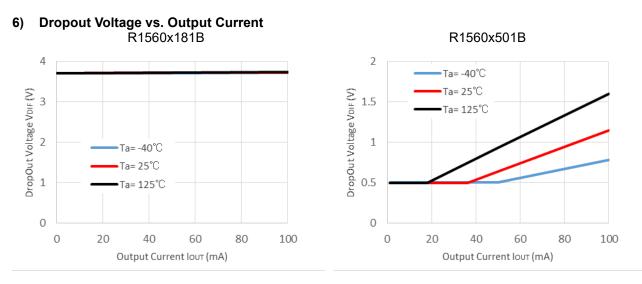


## 5) Supply Current vs. Input Voltage R1560x181B

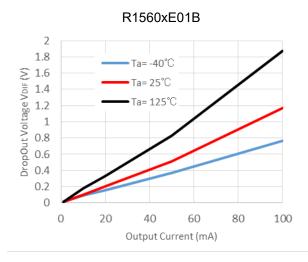


#### R1560xE01B

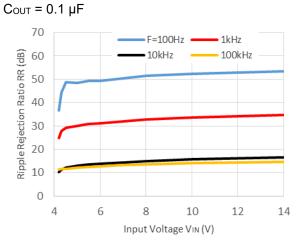


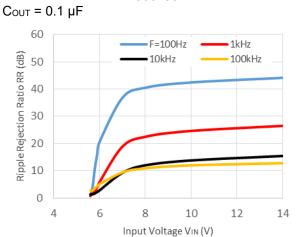


No. EC-395-220114

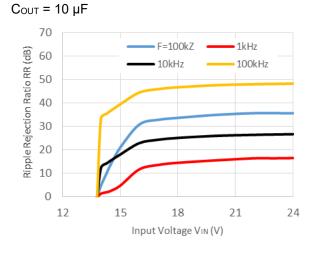




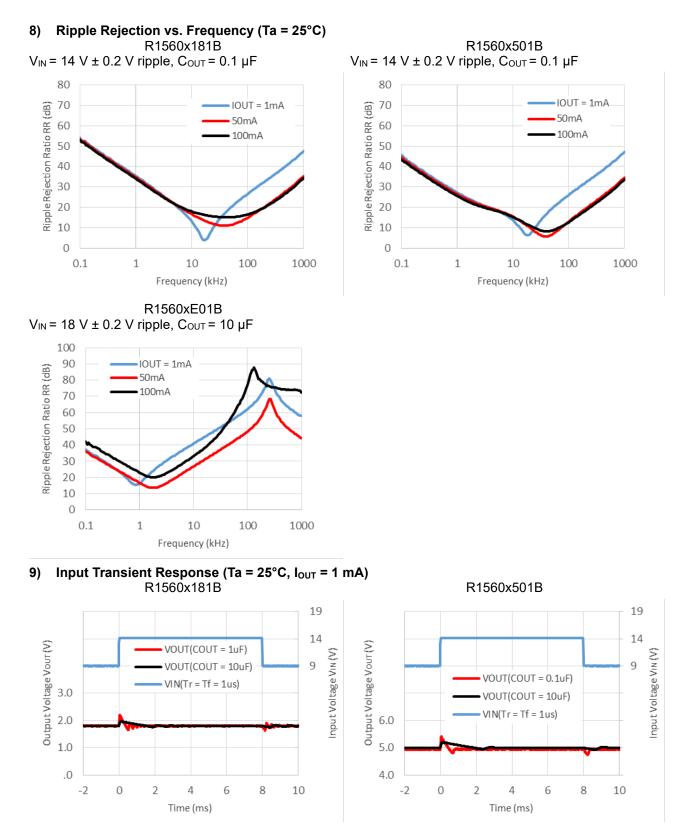






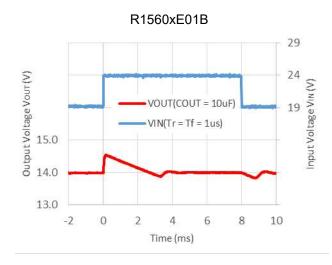


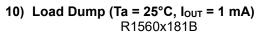
No. EC-395-220114

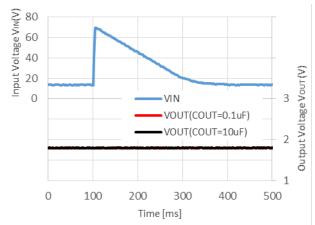


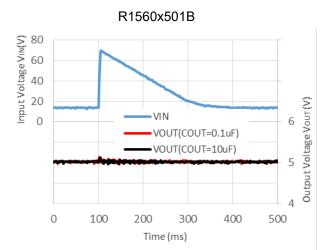
## <u>R1560x</u>

No. EC-395-220114



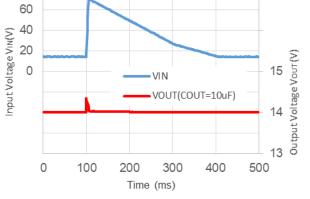




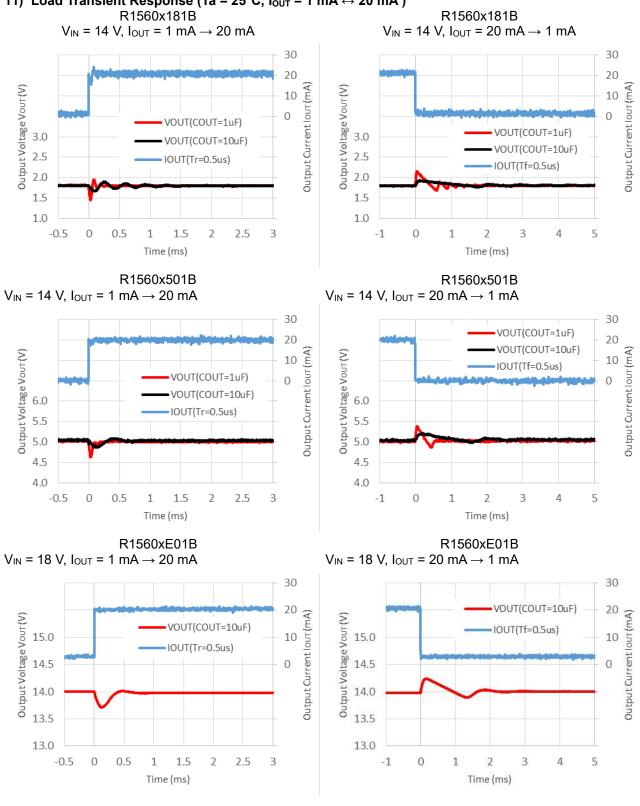




80

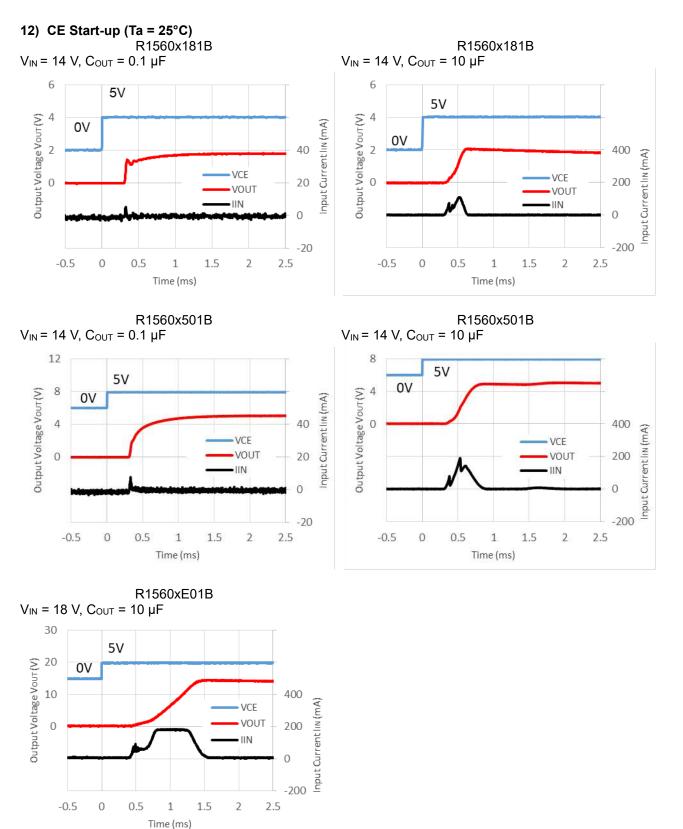


No. EC-395-220114



# 11) Load Transient Response (Ta = 25°C, $I_{OUT}$ = 1 mA $\leftrightarrow$ 20 mA )

No. EC-395-220114



# **POWER DISSIPATION**

## **HSOP-6J**

PD-HSOP-6J-(125150)-JE-A

The power dissipation of the package is dependent on PCB material, layout, and environmental conditions. The following measurement conditions are based on JEDEC STD. 51-7.

#### **Measurement Conditions**

ltem	Measurement Conditions		
Environment Mounting on Board (Wind Velocity = 0 m/s)			
Board Material	Glass Cloth Epoxy Plastic (Four-Layer Board)		
Board Dimensions	76.2 mm × 114.3 mm × 0.8 mm		
Copper Ratio	Outer Layer (First Layer): Less than 95% of 50 mm Square Inner Layers (Second and Third Layers): Approx. 100% of 50 mm Square Outer Layer (Fourth Layer): Approx. 100% of 50 mm Square		
Through-holes	φ 0.3 mm × 28 pcs		

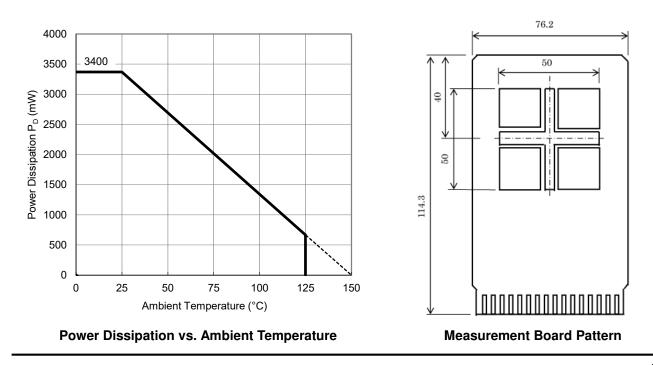
#### **Measurement Result**

(Ta = 25°C, Tjmax = 150°C)

Item	Measurement Result
Power Dissipation	3400 mW
Thermal Resistance (θja)	θja = 37°C/W
Thermal Characterization Parameter (ψjt)	ψjt = 7°C/W

 $\boldsymbol{\theta} ja:$  Junction-to-Ambient Thermal Resistance

ψjt: Junction-to-Top Thermal Characterization Parameter

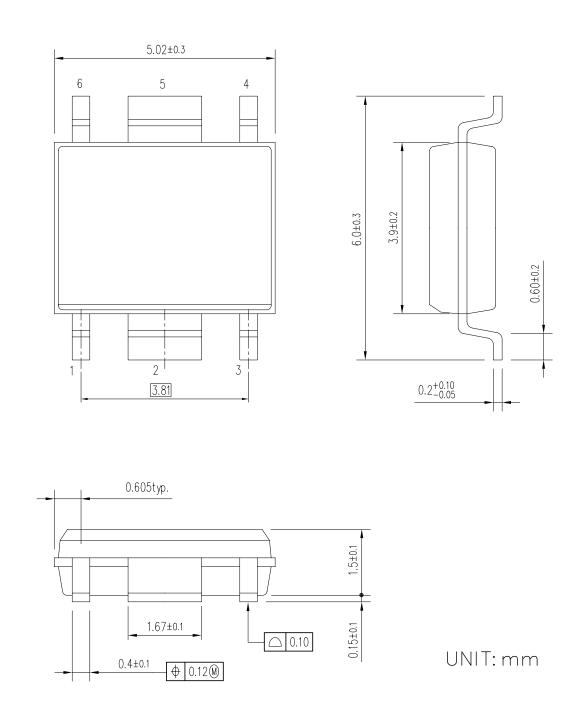


# PACKAGE DIMENSIONS

# HSOP-6J

i

DM-HSOP-6J-JE-A



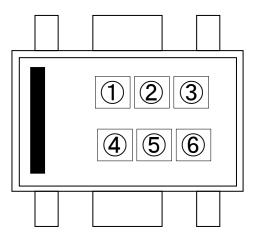
**HSOP-6J Package Dimensions** 

# PART MARKINGS

## R1560S

MK-R1560S-JCEC-B

①②③④: Product Code … Refer to *Part Marking List*⑤⑥: Lot Number … Alphanumeric Serial Number





## NOTICE

There can be variation in the marking when different AOI (Automated Optical Inspection) equipment is used. In the case of recognizing the marking characteristic with AOI, please contact our sales or our distributor before attempting to use AOI.

# PART MARKINGS

## R1560S

MK-R1560S-JCEC-B

#### R1560SxxxB Part Marking List **Product Name** 0234 R1560S181B D 1 8 B D 2 5 B R1560S251B R1560S281B D 2 8 B D 3 0 B R1560S301B D 3 3 B R1560S331B D 3 4 B R1560S341B R1560S501B D 5 0 B D 7 0 B R1560S701B R1560S801B D 8 0 B R1560S901B D 9 0 B DA0B R1560SA01B R1560SC01B DC0B R1560SE01B DE0B

# POWER DISSIPATION

## TO-252-5-P2

PD-TO-252-5-P2-(125150)-JE-A

The power dissipation of the package is dependent on PCB material, layout, and environmental conditions. The following measurement conditions are based on JEDEC STD. 51-7.

#### **Measurement Conditions**

ltem	Measurement Conditions
Environment	Mounting on Board (Wind Velocity = 0 m/s)
Board Material	Glass Cloth Epoxy Plastic (Four-Layer Board)
Board Dimensions	76.2 mm × 114.3 mm × 0.8 mm
Copper Ratio	Outer Layer (First Layer): Less than 95% of 50 mm Square Inner Layers (Second and Third Layers): Approx. 100% of 50 mm Square Outer Layer (Fourth Layer): Approx. 100% of 50 mm Square
Through-holes	φ 0.3 mm × 21 pcs

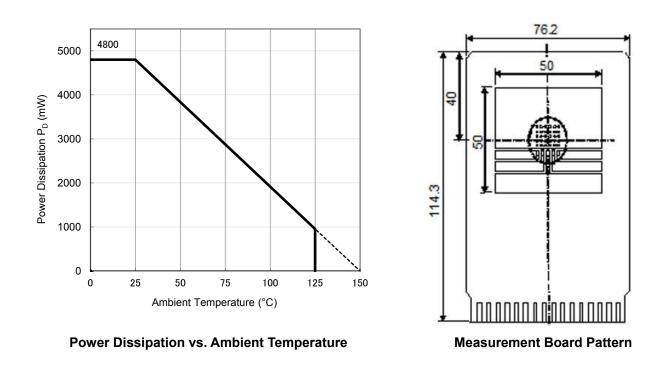
#### **Measurement Result**

(Ta = 25°C, Tjmax = 150°C)

Item	Measurement Result
Power Dissipation	4800 mW
Thermal Resistance (θja)	θja = 26°C/W
Thermal Characterization Parameter (ψjt)	ψjt = 7°C/W

θja: Junction-to-Ambient Thermal Resistance

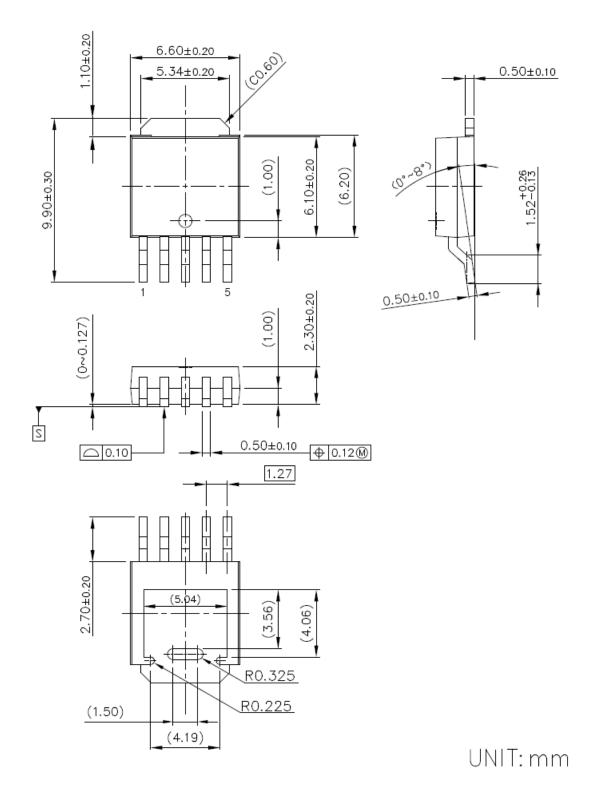
wjt: Junction-to-Top Thermal Characterization Parameter



# PACKAGE DIMENSIONS

# TO-252-5-P2

DM-TO-252-5-P2-JE-C



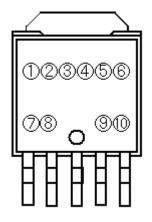
# PART MARKINGS

# R1560J

i

MK-R1560J-JCEC-C

①②③④⑤⑥⑦⑧: Product Code … Refer to *Part Marking List*⑨⑩: Lot Number … Alphanumeric Serial Number



## TO-252-5-P2 Part Markings

## NOTICE

There can be variation in the marking when different AOI (Automated Optical Inspection) equipment is used. In the case of recognizing the marking characteristic with AOI, please contact our sales or our distributor before attempting to use AOI.

# PART MARKINGS

# R1560J

MK-R1560J-JCEC-C

## R1560JxxxB Part Marking List

Product Name	12345678	
R1560J181B	P1J181B_	
R1560J251B	P1J251B_	
R1560J281B	P1J281B_	
R1560J301B	P1J301B_	
R1560J331B	P1J331B_	
R1560J341B	P1J341B_	
R1560J501B	P1J501B_	
R1560J701B	P1J701B_	
R1560J801B	P1J801B_	
R1560J901B	P1J901B_	
R1560JA01B	P1JA01B_	
R1560JC01B	P1JC01B_	
R1560JE01B	P1JE01B_	

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  - Various Safety Devices
  - Traffic control system
  - Combustion equipment

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- 7. The products have been designed and tested to function within controlled environmental conditions. Do not use products under conditions that deviate from methods or applications specified in this datasheet. Failure to employ the products in the proper applications can lead to deterioration, destruction or failure of the products. We shall not be responsible for any bodily injury, fires or accident, property damage or any consequential damages resulting from misuse or misapplication of the products.
- 8. Quality Warranty
  - 8-1. Quality Warranty Period

In the case of a product purchased through an authorized distributor or directly from us, the warranty period for this product shall be one (1) year after delivery to your company. For defective products that occurred during this period, we will take the quality warranty measures described in section 8-2. However, if there is an agreement on the warranty period in the basic transaction agreement, quality assurance agreement, delivery specifications, etc., it shall be followed.

8-2. Quality Warranty Remedies

When it has been proved defective due to manufacturing factors as a result of defect analysis by us, we will either deliver a substitute for the defective product or refund the purchase price of the defective product.

Note that such delivery or refund is sole and exclusive remedies to your company for the defective product.

- 8-3. Remedies after Quality Warranty Period With respect to any defect of this product found after the quality warranty period, the defect will be analyzed by us. On the basis of the defect analysis results, the scope and amounts of damage shall be determined by mutual agreement of both parties. Then we will deal with upper limit in Section 8-2. This provision is not intended to limit any legal rights of your company.
- 9. Anti-radiation design is not implemented in the products described in this document.
- 10. The X-ray exposure can influence functions and characteristics of the products. Confirm the product functions and characteristics in the evaluation stage.
- 11. WLCSP products should be used in light shielded environments. The light exposure can influence functions and characteristics of the products under operation or storage.
- 12. Warning for handling Gallium and Arsenic (GaAs) products (Applying to GaAs MMIC, Photo Reflector). These products use Gallium (Ga) and Arsenic (As) which are specified as poisonous chemicals by law. For the prevention of a hazard, do not burn, destroy, or process chemically to make them as gas or power. When the product is disposed of, please follow the related regulation and do not mix this with general industrial waste or household waste.
- 13. Please contact our sales representatives should you have any questions or comments concerning the products or the technical information.



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