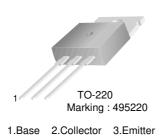


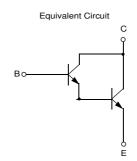
April 2008

495220

NPN Epitaxial Silicon Darlington Transistor

High Voltage & Medium Power Linear Application





Absolute Maximum Ratings * $T_C=25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Value	Units
BV _{CBO}	Collector-Base Voltage	550	V
BV _{CEO}	Collector-Emitter Voltage	325	V
BV _{EBO}	Emitter-Base Voltage 10		V
I _C	Collector Current (DC)	4	Α
I _{CP}	Collector Current (Pulse)**	6	Α
I _B	Base Current (DC)	0.5	Α
P _C	Collector Dissipation(T _C =25°C)	40	W
T _J	Junction Temperature	150	°C
T _{STG}	Storage Junction Temperature Range	- 55 ~ 150	°C

^{*} These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.
** Pulse Test : Pulse Width ≤ 5ms, Duty Cycle ≤ 10%

Electrical Characteristics * T_C=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV _{CEO(SUS)}	Collector-Emitter Sustaining Voltage	I _C =1.5A, I _B = 0.05A, L = 25mH	250			٧
I _{CBO}	Collector Cut-off Current	$V_{CB} = 550V, I_{E} = 0$			5	mA
I _{EBO}	Emitter Cut-off Current	V _{EB} =10V, I _C =0			1	mA
h _{FE}	DC Current Gain	V _{CE} =5V, I _C =05A V _{CE} =5V, I _C =3.0A	5000 1000			
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = 0.75A, I _B = 0.17A I _C = 2A, I _B = 5mA			1.7 1.5	V V
V _{BE} (sat)	Base-Emitter Saturation Voltage	$I_C = 2A$, $I_B = 5mA$			2	V

^{*} Pulse Test : Pulse Width ≤ 5ms, Duty Cycle ≤ 10%





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