



# PJA3404-AU

## 30V N-Channel Enhancement Mode MOSFET

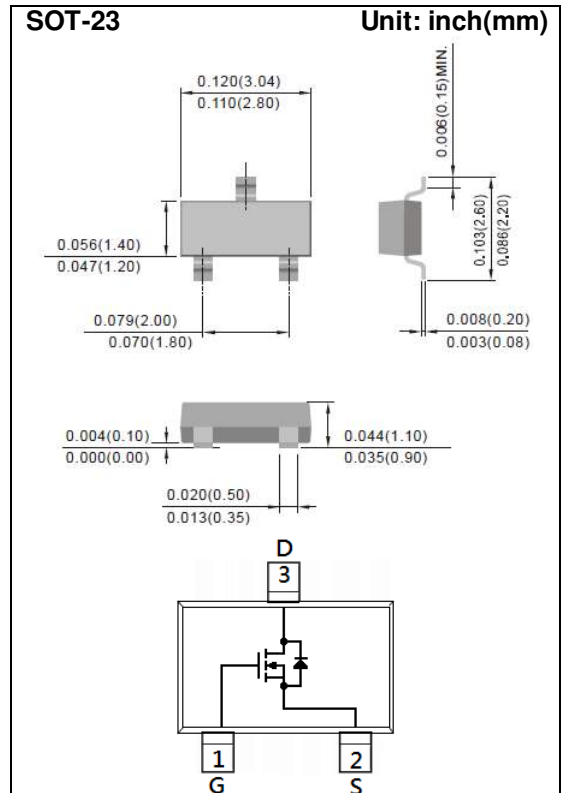
<b>Voltage</b>	<b>30 V</b>	<b>Current</b>	<b>5.6A</b>
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### Features

- RDS(ON) , VGS@10V, ID@5.6A<30mΩ
- RDS(ON) , VGS@4.5V, ID@3.5A<45mΩ
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc.
- Acquire quality system certificate : TS16949
- AEC-Q101 qualified
- Lead free in compliance with EU RoHS 2011/65/EU directive.
- Green molding compound as per IEC61249 Std.  
(Halogen Free)

### Mechanical Data

- Case: SOT-23 Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0003 ounces, 0.0084 grams
- Marking: A04



### Maximum Ratings and Thermal Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V <sub>DS</sub>	30	V
Gate-Source Voltage		V <sub>GS</sub>	±20	V
Continuous Drain Current		I <sub>D</sub>	5.6	A
Pulsed Drain Current		I <sub>DM</sub>	22	A
Power Dissipation	T <sub>a</sub> =25°C	P <sub>D</sub>	1.25	W
	Derate above 25°C		10	mW/°C
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55~150	°C
Typical Thermal Resistance		R <sub>θJA</sub>	100	°C/W
- Junction to Ambient <sup>(Note 3)</sup>				



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## Electrical Characteristics ( $T_A=25^{\circ}\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
<b>Static</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=250\mu A$	30	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	1.0	1.33	2.1	V
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=5.6A$	-	27	30	m $\Omega$
		$V_{GS}=4.5V, I_D=3.5A$	-	39	45	
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=30V, V_{GS}=0V$	-	0.01	1	$\mu A$
Gate-Source Leakage Current	$I_{GSS}$	$V_{GS}=\pm 20V, V_{DS}=0V$	-	$\pm 10$	$\pm 100$	nA
<b>Dynamic</b>						
Total Gate Charge	$Q_g$	$V_{DS}=15V, I_D=5.6A,$ $V_{GS}=10V$ (Note 1,2)	-	7.8	-	nC
Gate-Source Charge	$Q_{gs}$		-	1.2	-	
Gate-Drain Charge	$Q_{gd}$		-	1.5	-	
Input Capacitance	$C_{iss}$	$V_{DS}=15V, V_{GS}=0V,$ $f=1.0\text{MHz}$	-	343	-	pF
Output Capacitance	$C_{oss}$		-	48	-	
Reverse Transfer Capacitance	$C_{rss}$		-	34	-	
<b>Switching</b>						
Turn-On Delay Time	$t_{d(on)}$	$V_{DD}=15V, I_D=5.6A,$ $V_{GS}=10V,$ $R_G=3\Omega$ (Note 1,2)	-	3	-	ns
Turn-On Rise Time	$t_r$		-	40	-	
Turn-Off Delay Time	$t_{d(off)}$		-	38	-	
Turn-Off Fall Time	$t_f$		-	39	-	
<b>Drain-Source Diode</b>						
Maximum Continuous Drain-Source Diode Forward Current	$I_S$	---	-	-	1.5	A
Diode Forward Voltage	$V_{SD}$	$I_S=1.0A, V_{GS}=0V$	-	0.77	1.2	V

**NOTES :**

1. Pulse width  $\leq 300\mu s$ , Duty cycle  $\leq 2\%$
2. Essentially independent of operating temperature typical characteristics.
3.  $R_{\theta JA}$  is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
4. The maximum current rating is package limited



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## TYPICAL CHARACTERISTIC CURVES

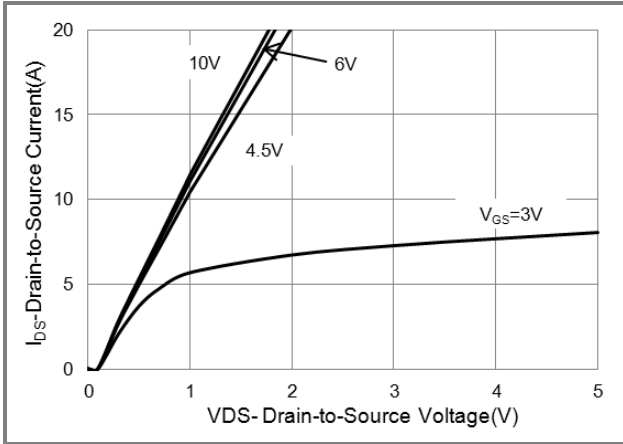


Fig.1 On-Region Characteristics

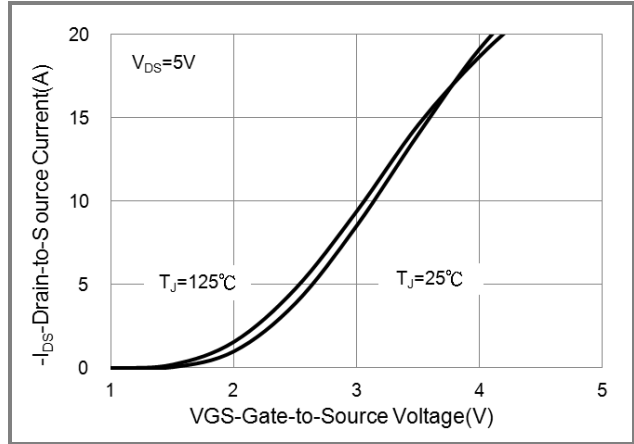


Fig.2 Transfer Characteristics

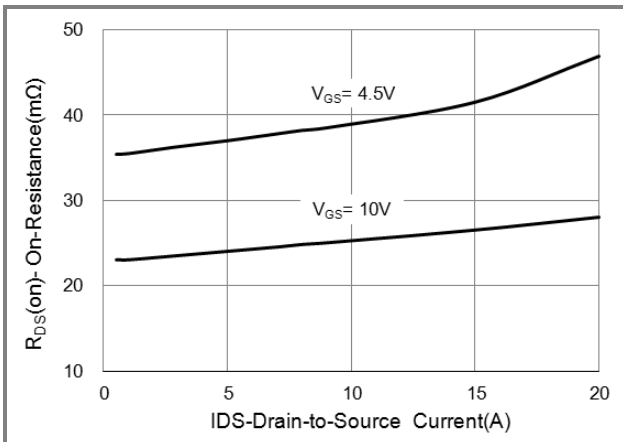


Fig.3 On-Resistance vs. Drain Current

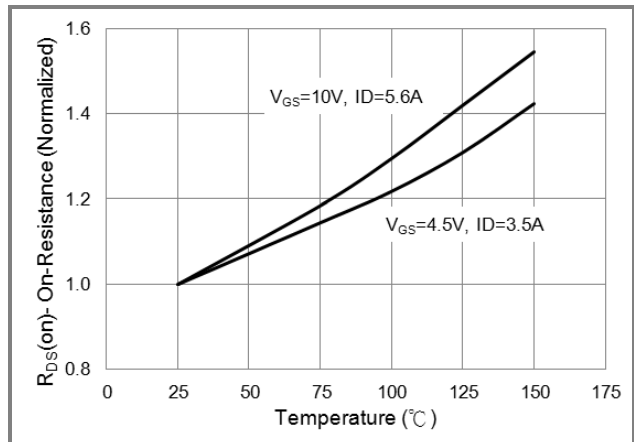


Fig.4 On-Resistance vs. Junction temperature

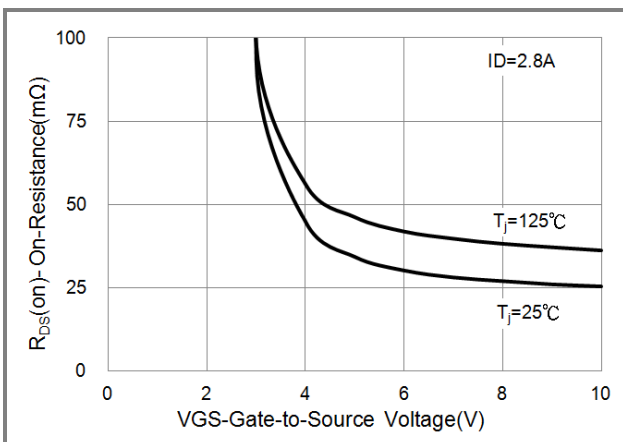


Fig.5 On-Resistance Variation with VGS.

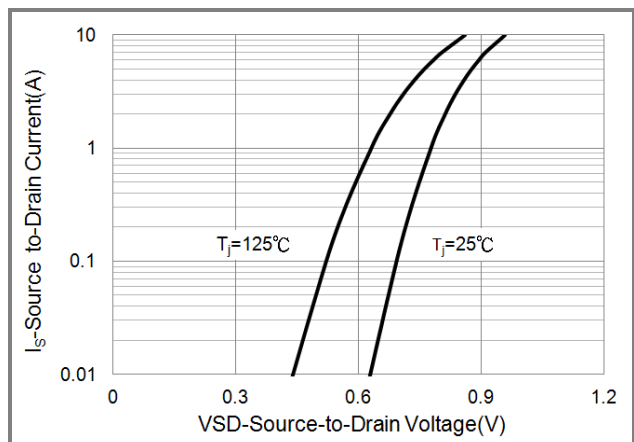


Fig.6 Body Diode Characteristics



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## TYPICAL CHARACTERISTIC CURVES

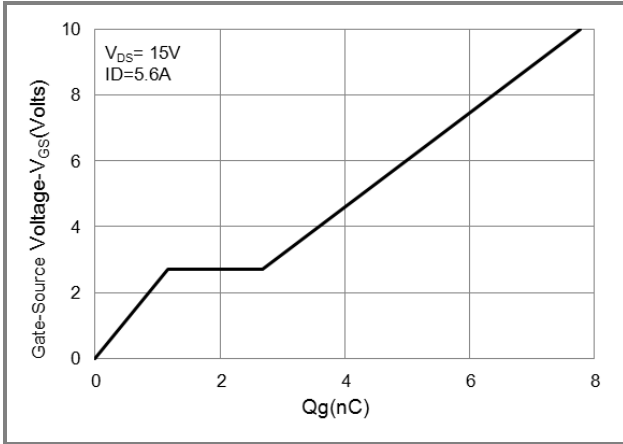


Fig.7 Gate-Charge Characteristics

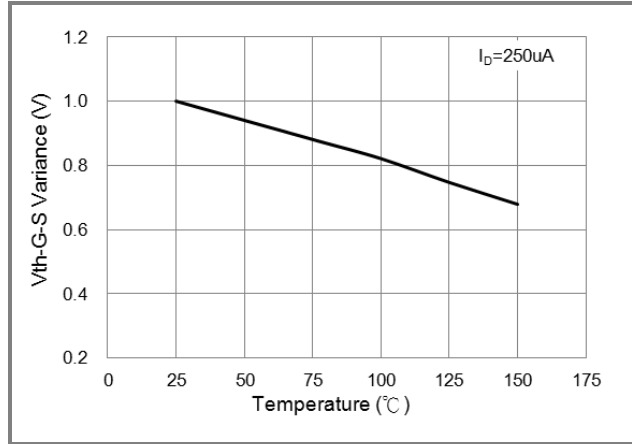


Fig.8 Threshold Voltage Variation with Temperature

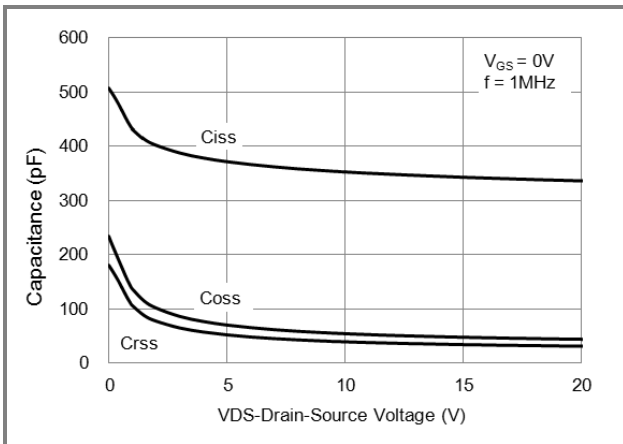


Fig.9 Capacitance vs. Drain-Source Voltage.

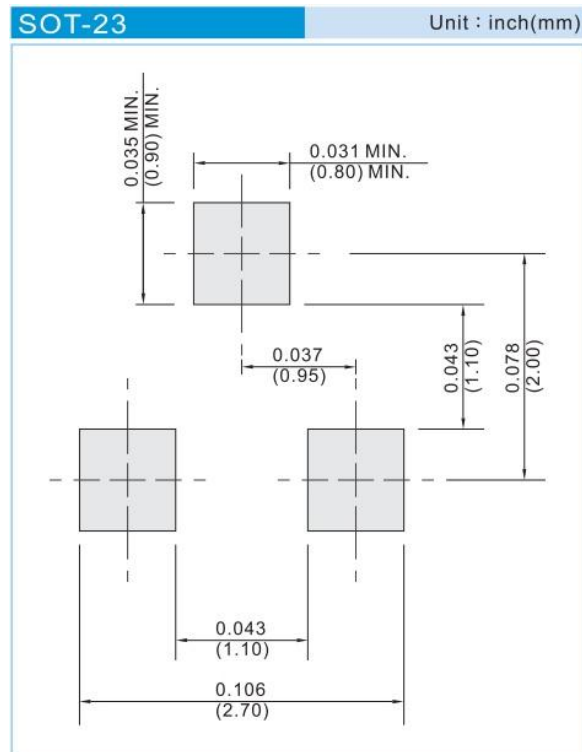


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## PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJA3404-AU_R1_000A1	SOT-23	3K pcs / 7" reel	A04	Halogen free
PJA3404-AU_R2_000A1	SOT-23	12K pcs / 13" reel	A04	Halogen free

## MOUNTING PAD LAYOUT





## PJA3404-AU

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