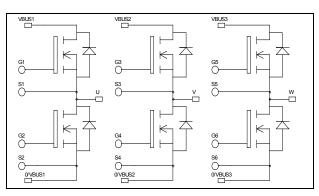
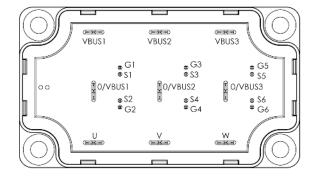


## Triple phase leg MOSFET Power Module





### Absolute maximum ratings

# APTM50TAM65FPG

### $V_{DSS} = 500V$ $R_{DSon} = 65m\Omega \text{ typ } @ \text{ Tj} = 25^{\circ}C$ $I_{D} = 51A @ \text{ Tc} = 25^{\circ}C$

### Application

- Welding converters
- Switched Mode Power Supplies
- Uninterruptible Power Supplies
- Motor control

#### Features

#### • Power MOS 7<sup>®</sup> FREDFETs

- Low R<sub>DSon</sub>
  - Low input and Miller capacitance
- Low gate charge
- Fast intrinsic reverse diode
- Avalanche energy rated
- Very rugged
- Kelvin source for easy drive
- Very low stray inductance
  - Symmetrical design
    - Lead frames for power connections
- High level of integration

### Benefits

- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Solderable terminals both for power and signal for easy PCB mounting
- Very low (12mm) profile
- Each leg can be easily paralleled to achieve a phase leg of three times the current capability
- Module can be configured as a three phase bridge
- Module can be configured as a boost followed by a full bridge
- RoHS Compliant

Symbol	Parameter		Max ratings	Unit
V <sub>DSS</sub>	Drain - Source Breakdown Voltage		500	V
$I_D$	Continuous Drain Current	$T_c = 25^{\circ}C$	51	
		$T_c = 80^{\circ}C$	38	А
I <sub>DM</sub>	Pulsed Drain current	204		
V <sub>GS</sub>	Gate - Source Voltage	±30	V	
R <sub>DSon</sub>	Drain - Source ON Resistance		78	mΩ
P <sub>D</sub>	Maximum Power Dissipation	$T_c = 25^{\circ}C$	390	W
I <sub>AR</sub>	Avalanche current (repetitive and non repetitive)		51	А
E <sub>AR</sub>	Repetitive Avalanche Energy		50	rea I
E <sub>AS</sub>	Single Pulse Avalanche Energy		3000	mJ

**CAUTION:** These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com

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### All ratings (a) $T_j = 25^{\circ}C$ unless otherwise specified

### **Electrical Characteristics**

Symbol	Characteristic	Test Conditions		Min	Тур	Max	Unit
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{GS} = 0V, V_{DS} = 500V$	$T_j = 25^{\circ}C$			100	μA
		$V_{GS} = 0V, V_{DS} = 400V$	$T_{j} = 125^{\circ}C$			500	
R <sub>DS(on)</sub>	Drain – Source on Resistance	$V_{GS} = 10V, I_D = 25.5A$			65	78	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 2.5 \text{mA}$		3		5	V
I <sub>GSS</sub>	Gate – Source Leakage Current	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$				±100	nA

### **Dynamic Characteristics**

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
C <sub>iss</sub>	Input Capacitance	$V_{GS} = 0V$		7000		
C <sub>oss</sub>	Output Capacitance	$V_{\rm DS} = 25 V$		1400		pF
C <sub>rss</sub>	Reverse Transfer Capacitance	f=1MHz		90		
Qg	Total gate Charge	$V_{GS} = 10V$		140		nC
Q <sub>gs</sub>	Gate – Source Charge	$V_{Bus} = 250V$		40		
$Q_{gd}$	Gate – Drain Charge	$I_D = 51A$		70		
T <sub>d(on)</sub>	Turn-on Delay Time	Inductive switching @ 125°C		21		ns
Tr	Rise Time	$V_{GS} = 15V$		38		
T <sub>d(off)</sub>	Turn-off Delay Time	$V_{Bus} = 333V$ $I_D = 51A$		75		
$T_{\mathrm{f}}$	Fall Time	$R_{G} = 3\Omega$		93		
Eon	Turn-on Switching Energy	Inductive switching (a) $25^{\circ}C$ $V_{GS} = 15V$ , $V_{Bus} = 333V$ $I_D = 51A$ , $R_G = 3\Omega$		1035		T
E <sub>off</sub>	Turn-off Switching Energy			845		μJ
Eon	Turn-on Switching Energy	Inductive switching @ 125°C		1556		T
E <sub>off</sub>	Turn-off Switching Energy	$V_{GS} = 15V, V_{Bus} = 333V$ $I_D = 51A, R_G = 3\Omega$		1013		μJ

### Source - Drain diode ratings and characteristics

Symbol	Characteristic	<b>Test Conditions</b>		Min	Тур	Max	Unit
Is	Continuous Source current		$Tc = 25^{\circ}C$			51	٨
	(Body diode)		$Tc = 80^{\circ}C$			38	А
V <sub>SD</sub>	Diode Forward Voltage	$V_{GS} = 0V, I_S = -51A$				1.3	V
dv/dt	Peak Diode Recovery <b>1</b>					15	V/ns
t <sub>rr</sub>	Reverse Recovery Time		$T_j = 25^{\circ}C$			270	20
	Reverse Recovery Time	$I_{S} = -51A$ $V_{R} = 333V$	$T_j = 125^{\circ}C$			540	ns
Qrr	Payaraa Paaayary Charga	$di_{\rm S}/dt = 100 {\rm A}/{\rm \mu s}$	$T_j = 25^{\circ}C$		2.6		чС
	Reverse Recovery Charge		$T_{j} = 125^{\circ}C$		9.6		μC

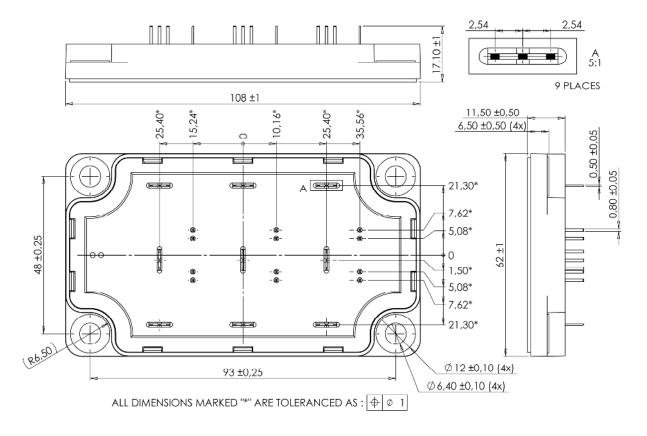
• dv/dt numbers reflect the limitations of the circuit rather than the device itself.  $I_S \le -51A$  di/dt  $\le 700A/\mu s$   $V_R \le V_{DSS}$   $T_j \le 150^{\circ}C$ 



### Thermal and package characteristics

Symbol	Characteristic			Min	Тур	Max	Unit
R <sub>thJC</sub>	Junction to Case Thermal Resistance IGBT				0.32	°C/W	
V <sub>ISOL</sub>	RMS Isolation Voltage, any terminal to case t =1 min, 50/60Hz			4000			V
T <sub>J</sub>	Operating junction temperature range			-40		150	
T <sub>STG</sub>	Storage Temperature Range			-40		125	°C
T <sub>C</sub>	Operating Case Temperature			-40		100	
Torque	Mounting torque	To heatsink	M6	3		5	N.m
Wt	Package Weight					250	g

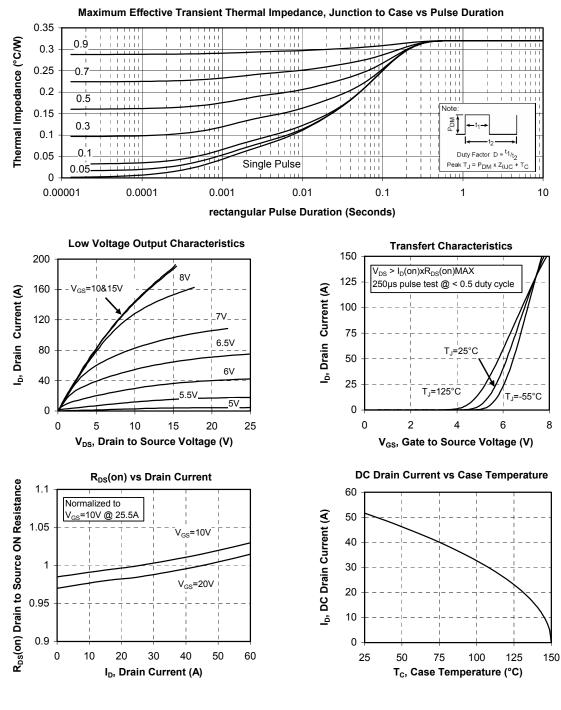
### SP6-P Package outline (dimensions in mm)



See application note 1902 - Mounting Instructions for SP6-P (12mm) Power Modules on www.microsemi.com

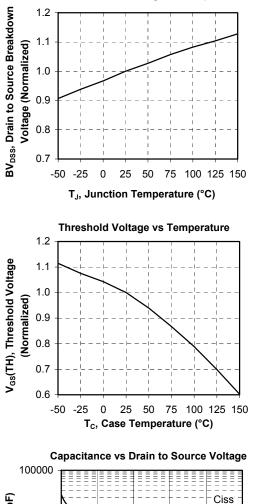


### **Typical Performance Curve**



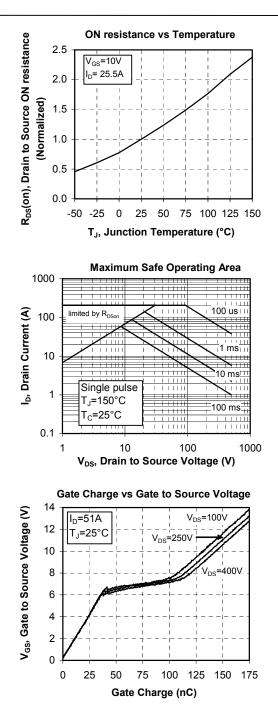


Breakdown Voltage vs Temperature



#### C, Capacitance (pF) Ciss 10000 Coss 1000 === Crss 100 10 0 10 20 30 40 50 V<sub>DS</sub>, Drain to Source Voltage (V)

## APTM50TAM65FPG





70 80

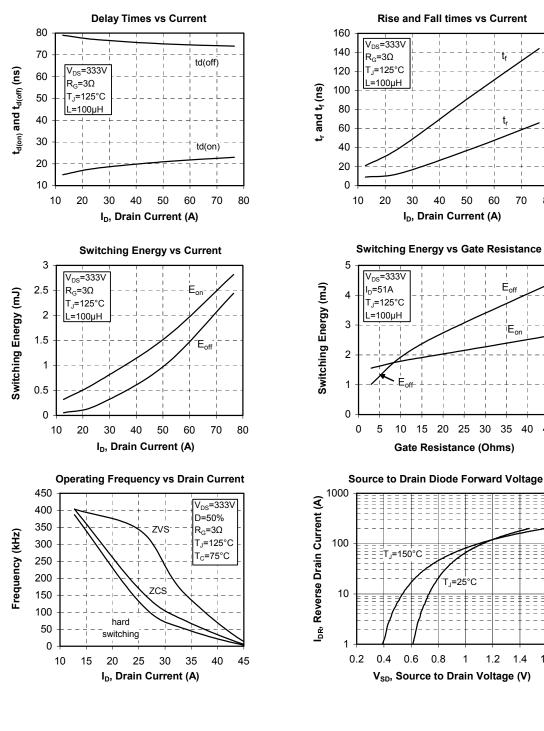
E<sub>off</sub>

Eon

40 45

1.4

1.6





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