TPS54200EVM-818 28-V, 1.5-A Buck LED Driver Evaluation Module

This user's guide describes the characteristics and use of the high-current buck light-emitting diode (LED) driver evaluation module.

Contents

List of Figures

List of Tables

1 Introduction

The TPS54200EVM-818 evaluation module (EVM) helps designers evaluate the operation and performance of the TPS54200 synchronous buck switching regulator designed for high-current LED driver applications. The TPS54200 is a 1.5-A synchronous buck LED driver and features a wide input voltage range (4.5 V to 28 V), deep analog mode dimming (1% to 100%) implemented by PWM input, and PWM mode dimming capability. It also has full protection, including LED open protection and short protection, sense resistor open protection and short protection, and thermal protection.

2 Warnings and Cautions

Observe the following precautions when using the TPS54200EVM-818.

WARNING

When choosing an LED component (not included with this EVM) the end-user must consult the LED data sheet supplied by the LED manufacturer to identify the EN62471 Risk Group Rating and review any potential eye hazards associated with the LED chosen. Always consider and implement the use of effective light filtering and darkening protective eyewear and be fully aware of surrounding laboratory-type set-ups when viewing intense light sources that may be required to minimize or eliminate such risks in order to avoid accidents related to temporary blindness.

3 Description

The TPS54200EVM-818 provides an LED driver based on the TPS54200 buck regulator. It is designed to operate with an input voltage in the range of 4.5 V to 28 V. The EVM is set up for a default output current of 1.5 A at 3.3 V / 100% duty cycle PWM input, which makes the driver work in analog dimming mode. If PWM dimming mode is chosen, the output current will be halved to 750 mA. See the TPS54200 datasheet (SLUSCO8) for more information about choosing dimming mode and components selection. The forward voltage of the LED load is between approximately 1.5 V and 23 V (depending on the input voltage). The TPS54200 helps provide high efficiency, wide dimming range, good line regulation, and low output ripple LED driver.

3.1 Typical Applications

This converter design describes an application of the TPS54200 as an LED driver using the following specifications. For applications with a different input voltage range or different output voltage and current, see the TPS54200 datasheet (SLUSCO8).

Table 1 lists the electrical performance specifications.

Table 1. TPS54200EVM-818 Electrical Performance Specifications

Parameter	Test Conditions	MIN	TYP	MAX	Units
Input voltage range, V_{IN}		4.5		28	V
Output voltage range, V_{OUT}	LED+ to LED–, depends on V_{IN}	1.5		23	V
Output current	3.3V/100% duty PWM input	1.44	1.5	1.56	A
Output current ripple	V_{IN} = 28 V, 6 White LEDs, 1.5-A output current		20		mApp
Analog dimming range	3.3-V PWM amplitude, 50 kHz	1		100	%
PWM dimming range	1.5-V PWM amplitude, 200 Hz, V_{IN} = 24 V, 3 White LEDs, 1.5-A output current	2		100	%
Efficiency	V_{IN} = 21 V, 5 White LEDs, 1-A output current, PWM dimming mode		96		%
Switching frequency			600		kHz

3.2 Test Setup

This section describes the connectors and test points on the EVM and how to properly connect, setup, and use the TPS54200EVM-818.

3.2.1 Connector Description

Table 2. EVM Connectors and Test Points

Reference Designator	Function
J1	V_{IN} (see Table 1 for V_{IN} range)
J2	LED load, make sure the LED has a maximum 1.5-A current rating
J3/J4	2-pin header to enable driver when no dimming required
TP1	BOOT test point
TP2	SW test point
TP3	V_{IN} test point
TP4	VOUT test point, also the anode of the LED load
TP5	FB test point
TP6	LOOP test point between FB filter and VSENSE. Used for loop response measurements.
TP7	VSENSE test point, also the cathode of the LED load
TP8	PWM input here
TP9	PWM test point
TP10	GND terminal for PWM input
TP11	GND test point at VIN
TP12/TP13/TP14	General purpose GND test point

3.2.2 Input/Output Connection

A power supply capable of supplying 2 A must be connected to J1 through a pair of 20-AWG wires. The LED load must be connected to J2 through a pair of 20-AWG wires. The positive terminal of the LED load should be connected to the J2 terminal beside TP4 (VOUT), and the negative terminal of the LED load should be connected to the J2 terminal beside TP7 (VSENSE). Wires should be twisted and kept as short as possible to minimize voltage drop, inductance, and EMI transmission.

TP8/TP10 are the input terminals for the PWM dimming signal. If analog dimming mode is used, apply a square wave with a low level of GND and a high level higher than 2 V, typically 3.3 V. The PWM frequency range is 10 kHz to 100 kHz, typically 50 kHz. If PWM dimming mode is used, apply a square wave with a low level of GND and a high level voltage between 1 V and 2 V, typically 1.5 V. The dimming frequency range is 100 Hz to 1 kHz.

Once the connection is ready, first apply the input voltage, then apply the PWM signal.

3.2.3 No Dimming Application

In a case where no dimming function is needed, J3 and J4 can be shorted to feed the input voltage to the PWM pin through resistor divider R5 and R8, thus no external PWM signal is needed. The value of the resistor divider is sized to make a converter work in PWM dimming mode with 100% duty under 24-V nominal input voltage. See the datasheet to change the resistor divider if a different input voltage is needed.

4 Performance Data and Typical Characteristics Curves

The figures in this section present the typical performance of the TPS54200EVM-818. The ambient temperature is 25°C, unless otherwise noted.

4.1 Efficiency

Figure 1 shows the efficiency versus PWM duty in analog dimming mode. The maximum LED current is 1.5 A when the PWM duty is 100%. V_{IN} = 12 V, and an infrared (IR) LED load is used. The typical forward voltage of an IR LED is 1.8 V at 1.5 A. The LED number in series is 1, 3, and 5, respectively.

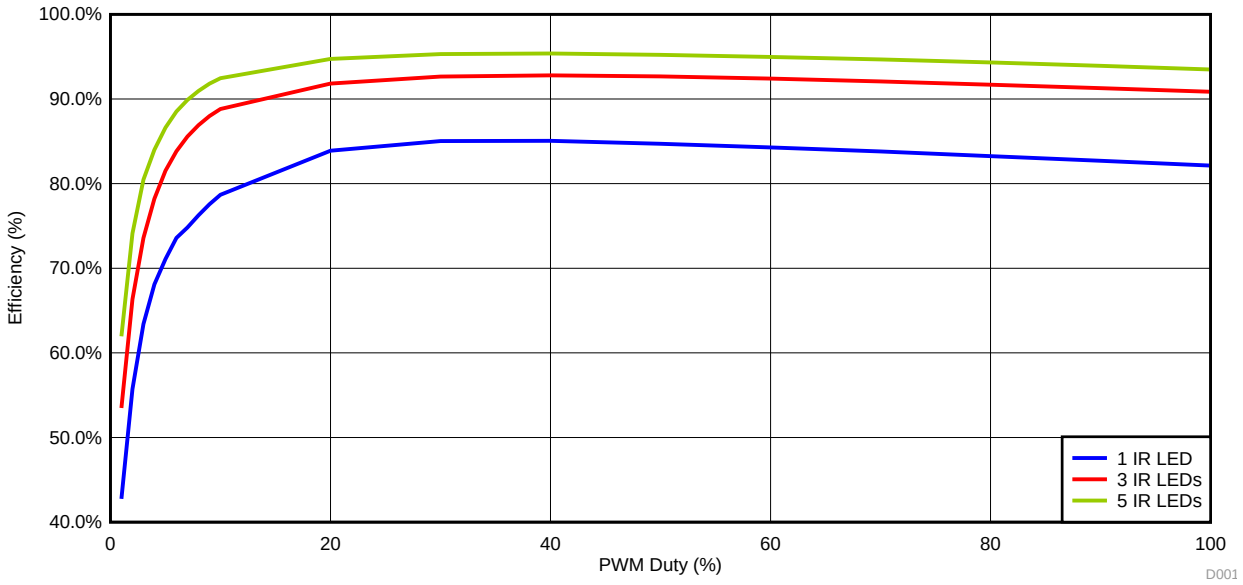

Figure 1. Efficiency vs. PWM Duty in Analog Dimming Mode, 1.5 A at 100% Duty, V_{IN} = 12 V

Figure 2 shows the efficiency versus input voltage in PWM dimming mode. PWM duty is 100%, LED current is set at 1 A. A White LED load is used. The typical forward voltage of a White LED is 3 V at 1 A. The LED number in series is 1, 3, and 5, respectively.

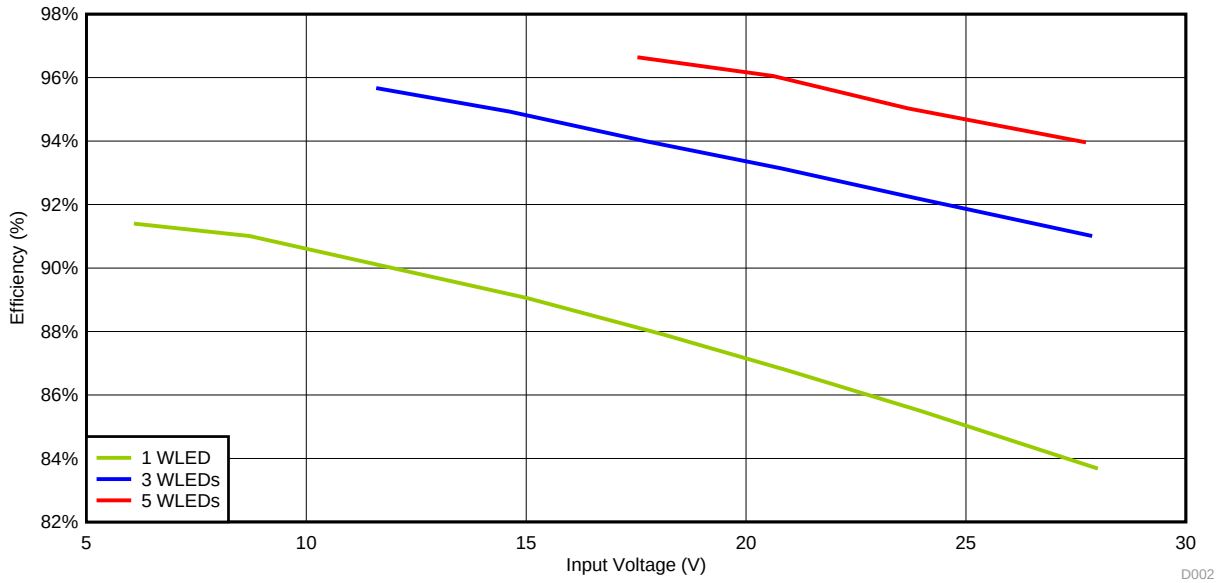

Figure 2. Efficiency vs. Input Voltage in PWM Dimming Mode, 1 A at 100% Duty

4.2 Line Regulation

Figure 3 shows the output current deviation ratio vs. input voltage in analog dimming mode. PWM duty is 100%. 1 White LED is used as load. The LED current is set at 1.5 A and 350 mA, respectively. The typical forward voltage of the White LED is 3.1 V at 1.5 A, and 2.8 V at 0.35 A.

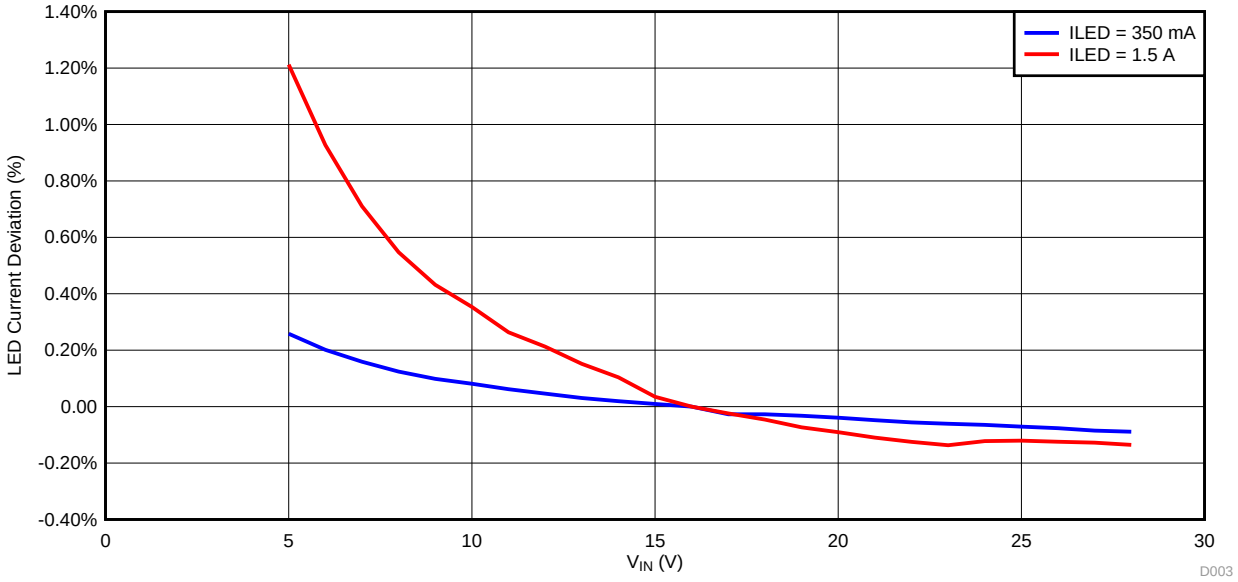

Figure 3. LED Current Deviation vs. Input Voltage in Analog Dimming Mode, 1 WLED

4.3 Load Regulation

Figure 4 shows the output current deviation ratio vs. output voltage in analog dimming mode. PWM duty is 100%. White LEDs are used as load, LED number in series is 1, 2, 3, 4, 5, and 6, respectively. LED current is set at 1.5 A and 350 mA, respectively. The typical forward voltage of the White LED is 3.1 V at 1.5 A, and 2.8 V at 0.35 A. Changing the LED number in series from 1 piece to 6 pieces will change the output voltage from approximately 3 V to approximately 18 V. Input voltage is fixed at 24 V.

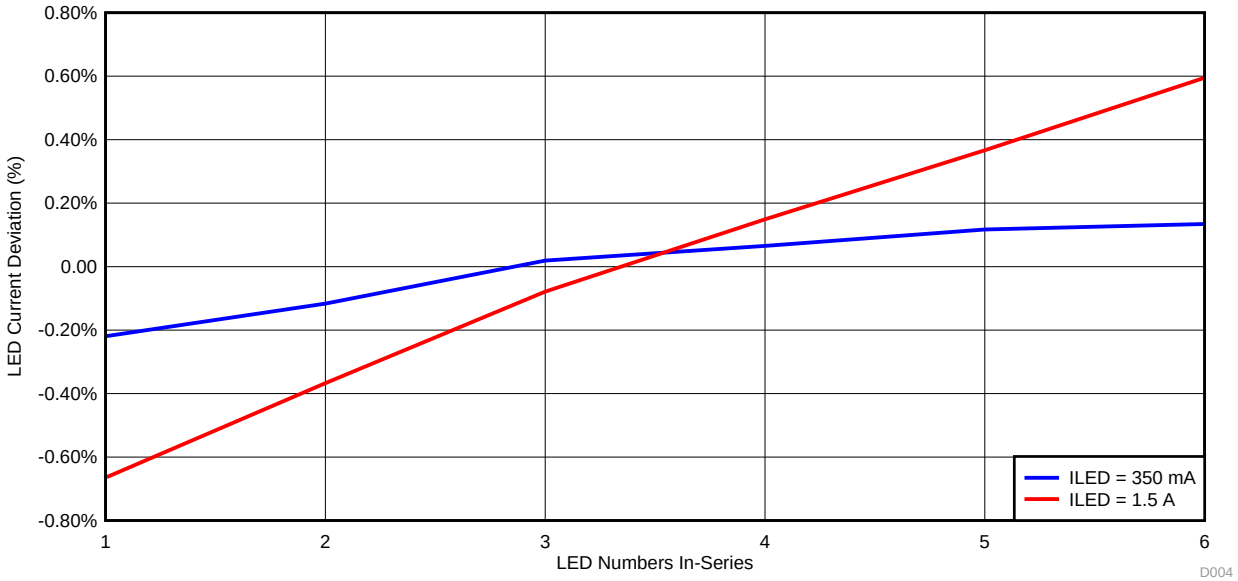

Figure 4. LED Current Deviation vs. LED Numbers in Series in Analog Dimming Mode, V_{IN} = 24 V

4.4 Analog Dimming

Figure 5 shows the output current ratio to the full-scale output current versus PWM duty cycle in analog dimming mode. V_{IN} = 12 V, 3 IR LEDs in series used as load. The LED current is set at 1.5 A with 100% PWM duty. PWM frequency is 50 kHz.

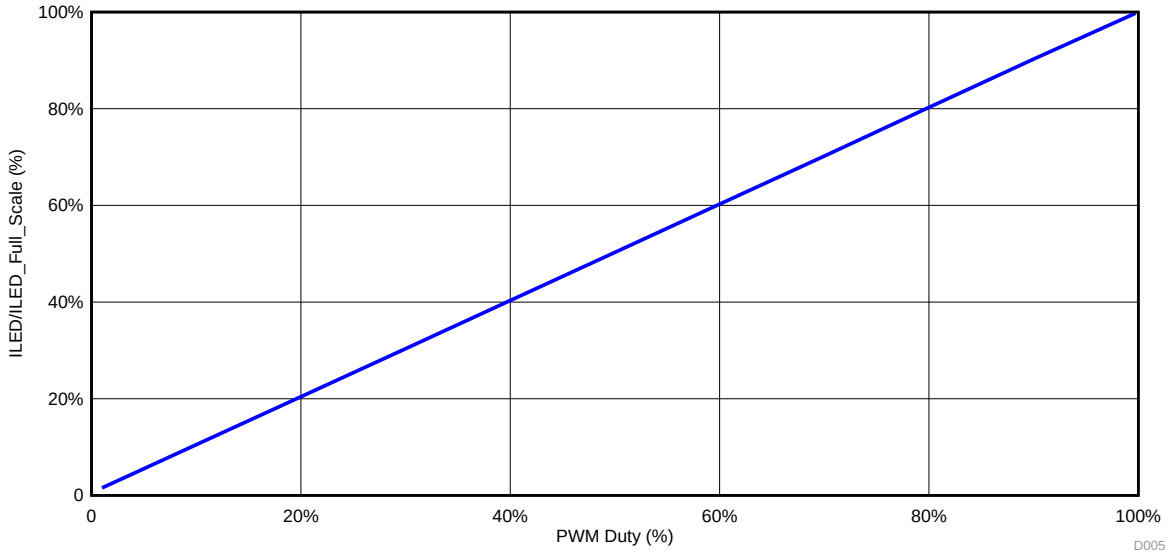

Figure 5. Output Current Ratio vs. PWM duty cycle in Analog Dimming Mode

4.5 PWM Dimming Waveforms

Figure 6, Figure 7, and Figure 8 illustrate the PWM dimming waveforms at 2%, 50%, and 99% duty cycles, respectively, in PWM dimming mode. Input voltage is 24 V, with 3 White LEDs in series used as load. The LED current is set at 1.5 A, PWM frequency is 200 Hz. The resistor, R3, of the RC filter at the FB pin is changed to 200 Ω for better loop response.

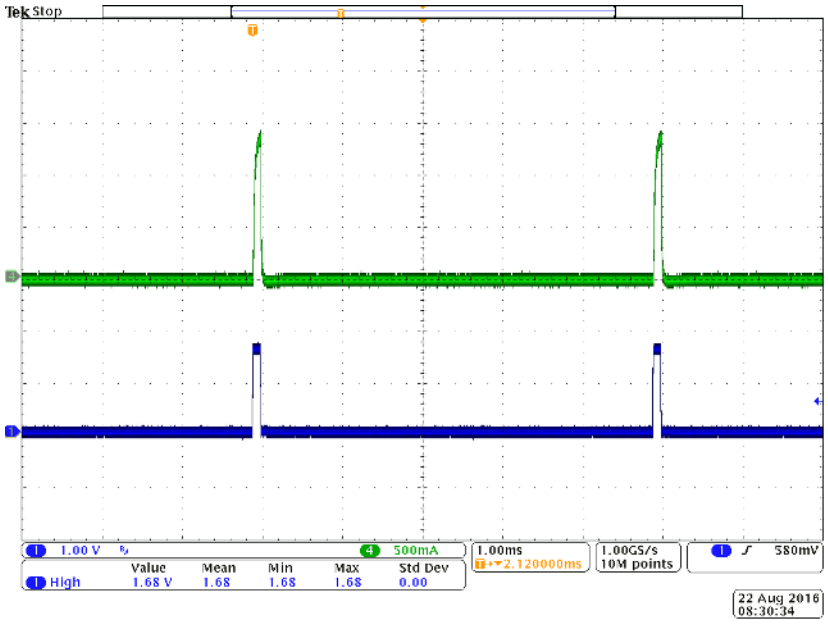

Figure 6. 2% Duty Cycle 200-Hz PWM Dimming, Top = LED Current, Bottom = PWM

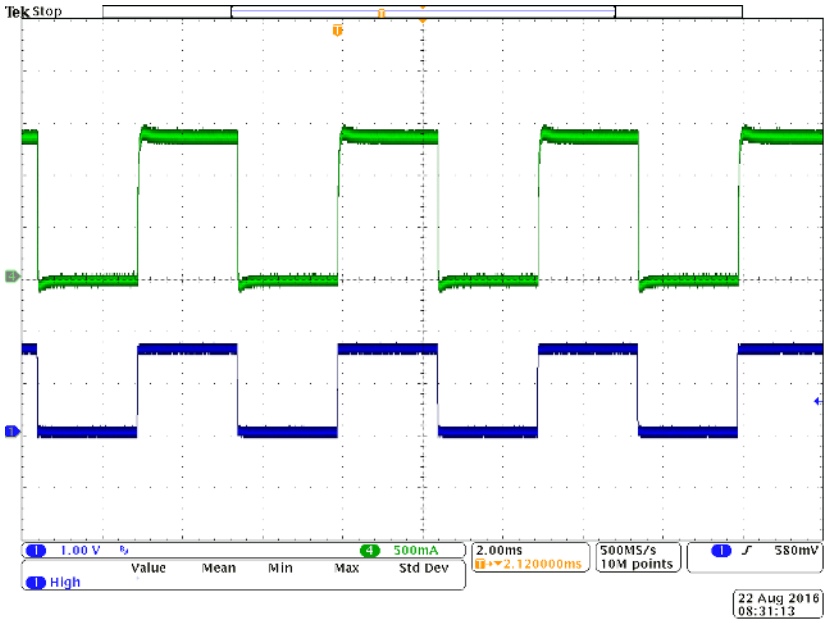

Figure 7. 50% Duty Cycle 200-Hz PWM Dimming, Top = LED Current, Bottom = PWM

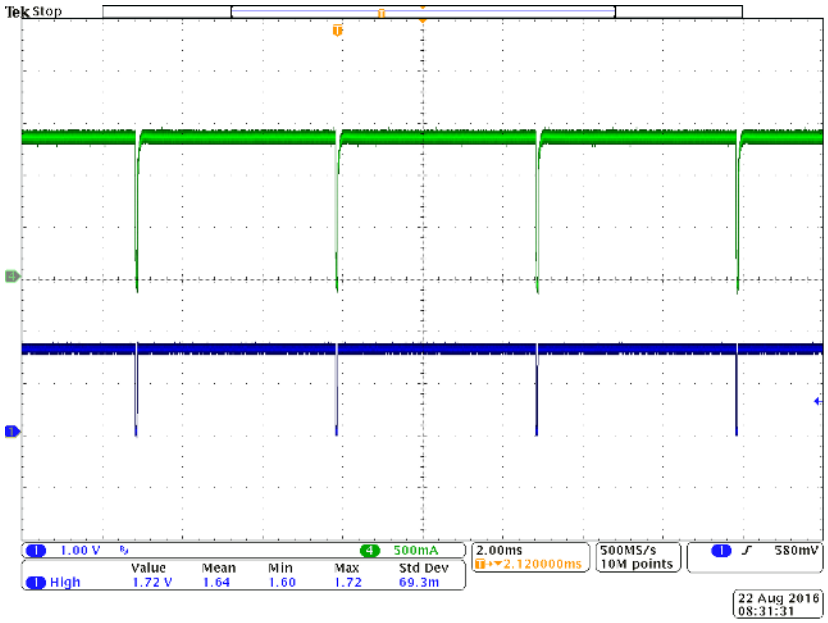

Figure 8. 99% Duty Cycle 200-Hz PWM Dimming, Top = LED Current, Bottom = PWM

4.6 LED Open and Short Protection

Figure 9 shows the LED open protection waveform in PWM dimming mode. The LED load is open at first, then apply V_{IN} and PWM. V_{IN} = 12 V, PWM is 1.6 V DC. The LED current is set at 1.5 A. The curves in this waveform are defined as: CH1: PWM; CH2: SW; CH3: VOUT; CH4: Inductor Current.

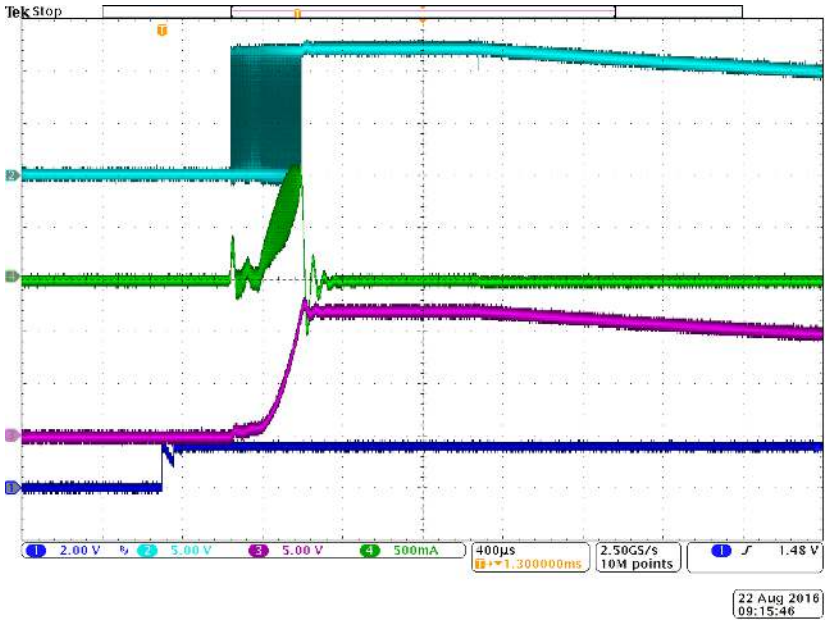

Figure 9. LED Open Failure Protection

Figure 10 shows the LED short protection waveform in PWM dimming mode. The LED load is shorted at first, then apply V_{IN} and PWM. V_{IN} = 12 V, PWM is 1.6 V DC. The LED current is set at 1.5 A. The curves in this waveform are defined as: CH1: PWM; CH2: SW; CH3: FB; CH4: Inductor Current.

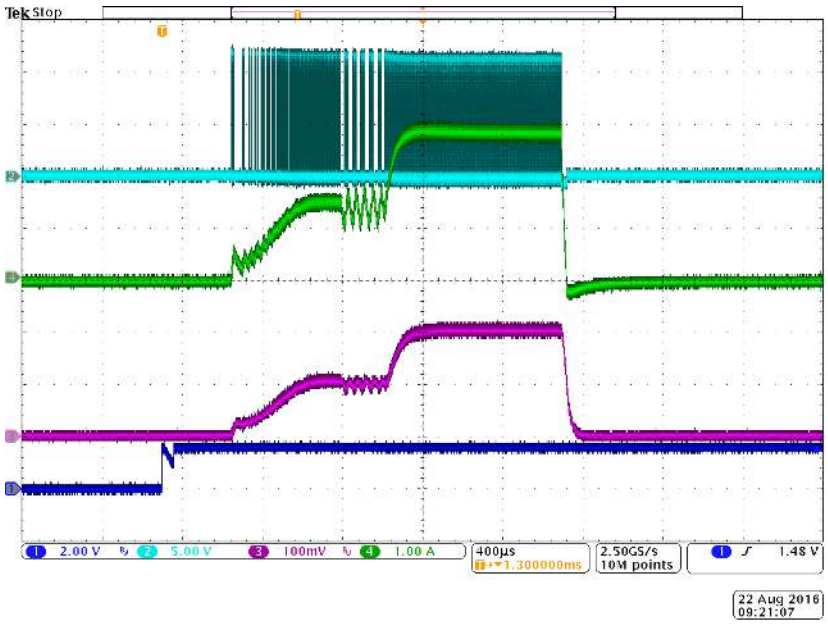

Figure 10. LED Short Failure Protection

5 Schematic

Figure 11 displays the EVM schematic.

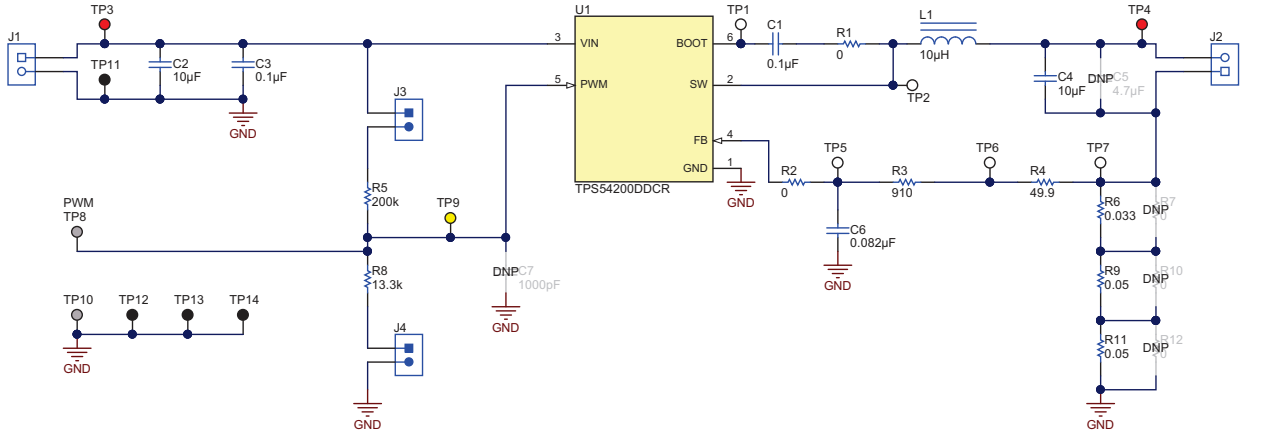

Figure 11. TPS54200EVM-818 Schematic

6 TPS54200EVM-818 PCB Layout

Figure 12 and Figure 13 show the design of the TPS54200EVM-818 printed-circuit board.

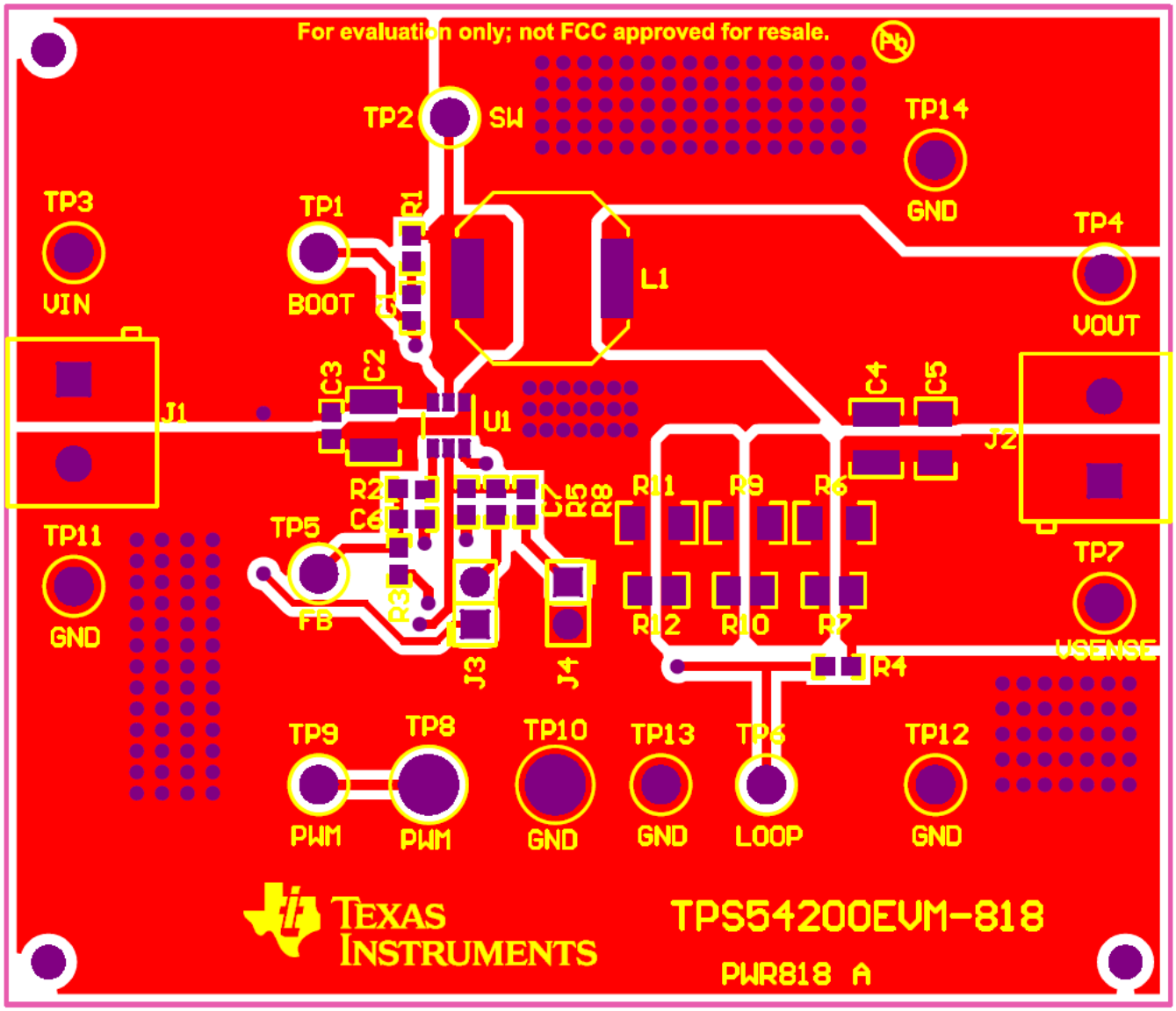

Figure 12. Top Layer and Top Overlay (Top View)

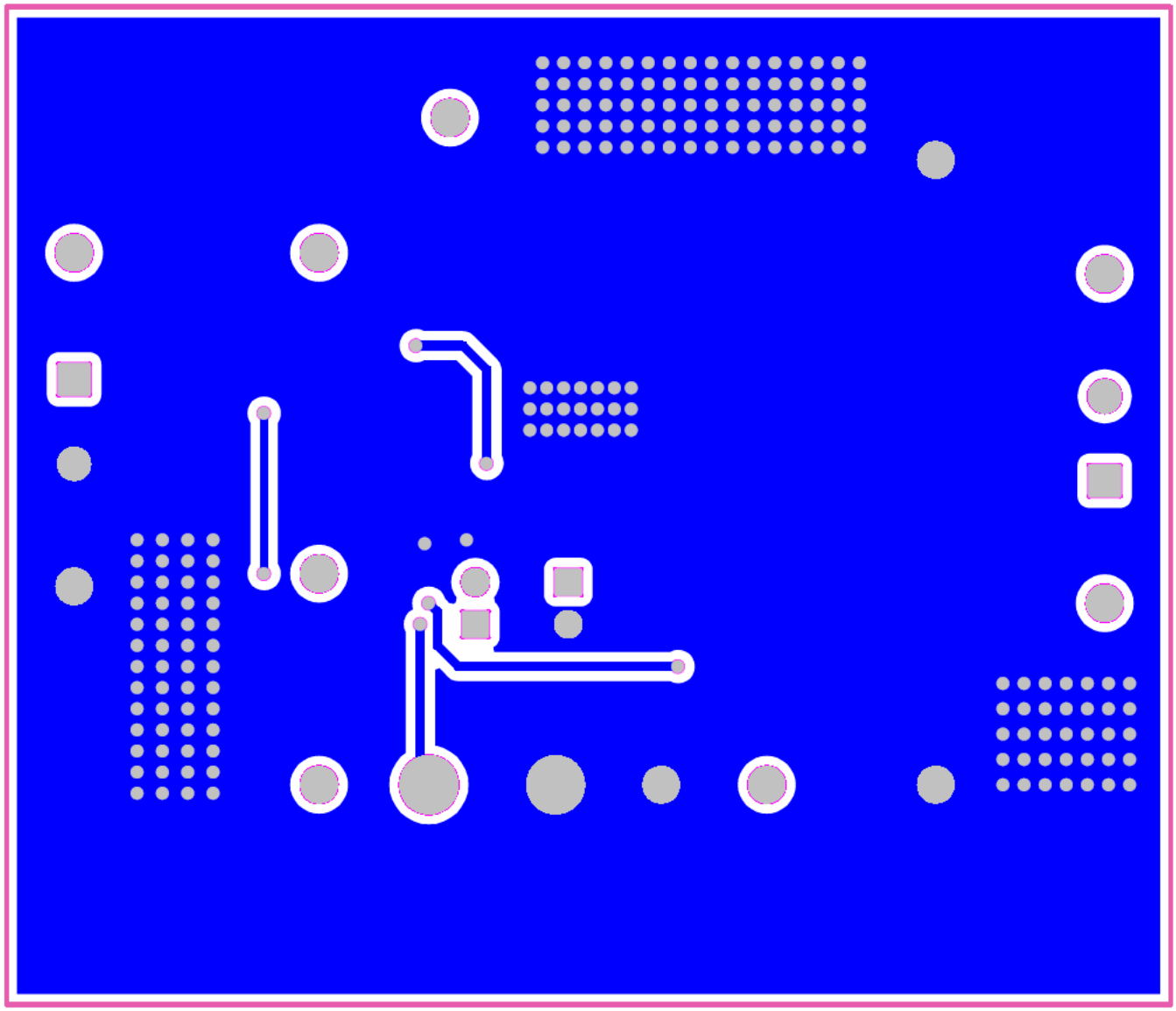

Figure 13. Bottom Layer and Bottom Overlay (Bottom View)

7 Bill of Materials

Table 3 displays the TPS54200EVM-818 components list according to the schematic in Figure 11.

Table 3. TPS54200EVM-818 Components List

Designator	Qty	Value	Description	Package	Part Number	Manufacturer
C1, C3	2	0.1uF	CAP, CERM, 0.1 µF, 50 V, +/- 10%, X7R, 0603	0603	GRM188R71H104KA93D	Murata
C2, C4	2	10uF	CAP, CERM, 10 µF, 35 V, +/- 10%, X7R, 1210	1210	GRM32ER7YA106KA12L	Murata
C6	1	0.082uF	CAP, CERM, 0.082 µF, 50 V, +/- 10%, X7R, 0603	0603	GRM188R71H823KA93D	Murata
J1, J2	2		Terminal Block, 5.08 mm, 2x1, Brass, TH	2x1 5.08 mm Terminal Block	ED120/2DS	On-Shore Technology
J3, J4	2		Header, 100mil, 2x1, Gold, TH	2x1 Header	TSW-102-07-G-S	Samtec
L1	1	10uH	Inductor, Shielded Drum Core, Ferrite, 10 µH, 3.6 A, 0.028 ohm, SMD	WE-TPC-XLH1	744066100	Wurth Elektronik
R1, R2	2	0	RES, 0, 5%, 0.1 W, 0603	0603	CRCW06030000Z0EA	Vishay-Dale
R3	1	910	RES, 910, 5%, 0.1 W, 0603	0603	CRCW0603910RJNEA	Vishay-Dale
R4	1	49.9	RES, 49.9, 1%, 0.1 W, 0603	0603	CRCW060349R9FKEA	Vishay-Dale
R5	1	200k	RES, 200 k, 1%, 0.1 W, 0603	0603	CRCW0603200KFKEA	Vishay-Dale
R6	1	0.033	RES, 0.033, 1%, 1 W, AEC-Q200 Grade 0, 1206	1206	ERJ-8CWFR033V	Panasonic
R8	1	13.3k	RES, 13.3 k, 1%, 0.1 W, 0603	0603	CRCW060313K3FKEA	Vishay-Dale
R9, R11	2	0.05	RES, 0.05, 1%, 0.5 W, 1206	1206	CSR1206FK50L0	Stackpole Electronics Inc
TP1, TP2, TP5, TP6, TP7	5		Test Point, Multipurpose, White, TH	White Multipurpose Testpoint	5012	Keystone
TP3, TP4	2		Test Point, Multipurpose, Red, TH	Red Multipurpose Testpoint	5010	Keystone
TP8, TP10	2		Terminal, Turret, TH, Double	Keystone1502-2	1502-2	Keystone
TP9	1		Test Point, Multipurpose, Yellow, TH	Yellow Multipurpose Testpoint	5014	Keystone
TP11, TP12, TP13, TP14	4		Test Point, Multipurpose, Black, TH	Black Multipurpose Testpoint	5011	Keystone
U1	1		4.5V TO 28V INPUT VOLTAGE, 1.5 A OUTPUT CURRENT, SYNCHRONOUS BUCK WLED Driver, DDC0006A	DDC0006A	TPS54200DDCR	Texas Instruments

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1. *Delivery:* TI delivers TI evaluation boards, kits, or modules, including demonstration software, components, and/or documentation which may be provided together or separately (collectively, an “EVM” or “EVMs”) to the User (“User”) in accordance with the terms and conditions set forth herein. Acceptance of the EVM is expressly subject to the following terms and conditions.

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   2.2 TI warrants that the TI EVM will conform to TI's published specifications for ninety (90) days after the date TI delivers such EVM to User. Notwithstanding the foregoing, TI shall not be liable for any defects that are caused by neglect, misuse or mistreatment by an entity other than TI, including improper installation or testing, or for any EVMs that have been altered or modified in any way by an entity other than TI. Moreover, TI shall not be liable for any defects that result from User's design, specifications or instructions for such EVMs. Testing and other quality control techniques are used to the extent TI deems necessary or as mandated by government requirements. TI does not test all parameters of each EVM.

   2.3 If any EVM fails to conform to the warranty set forth above, TI's sole liability shall be at its option to repair or replace such EVM, or credit User's account for such EVM. TI's liability under this warranty shall be limited to EVMs that are returned during the warranty period to the address designated by TI and that are determined by TI not to conform to such warranty. If TI elects to repair or replace such EVM, TI shall have a reasonable time to repair such EVM or provide replacements. Repaired EVMs shall be warranted for the remainder of the original warranty period. Replaced EVMs shall be warranted for a new full ninety (90) day warranty period.

3 *Regulatory Notices:*

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      3.1.1 *Notice applicable to EVMs not FCC-Approved:*

      This kit is designed to allow product developers to evaluate electronic components, circuitry, or software associated with the kit to determine whether to incorporate such items in a finished product and software developers to write software applications for use with the end product. This kit is not a finished product and when assembled may not be resold or otherwise marketed unless all required FCC equipment authorizations are first obtained. Operation is subject to the condition that this product not cause harmful interference to licensed radio stations and that this product accept harmful interference. Unless the assembled kit is designed to operate under part 15, part 18 or part 95 of this chapter, the operator of the kit must operate under the authority of an FCC license holder or must secure an experimental authorization under part 5 of this chapter.

      3.1.2 *For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant:*

      CAUTION

      This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

      Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

      FCC Interference Statement for Class A EVM devices

      NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

FCC Interference Statement for Class B EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- *Reorient or relocate the receiving antenna.*
- *Increase the separation between the equipment and receiver.*
- *Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.*
- *Consult the dealer or an experienced radio/TV technician for help.*

3.2 *Canada*

3.2.1 *For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210*

Concerning EVMs Including Radio Transmitters:

This device complies with Industry Canada license-exempt RSS standard(s). Operation is subject to the following two conditions: (1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

Concernant les EVMs avec appareils radio:

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

Concerning EVMs Including Detachable Antennas:

Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur

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3.3.1 *Notice for EVMs delivered in Japan:* Please see http://www.tij.co.jp/lsds/ti_ja/general/eStore/notice_01.page 日本国内に輸入される評価用キット、ボードについては、次のところをご覧ください。
http://www.tij.co.jp/lsds/ti_ja/general/eStore/notice_01.page

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If User uses EVMs in Japan, not certified to Technical Regulations of Radio Law of Japan, User is required by Radio Law of Japan to follow the instructions below with respect to EVMs:

1. Use EVMs in a shielded room or any other test facility as defined in the notification #173 issued by Ministry of Internal Affairs and Communications on March 28, 2006, based on Sub-section 1.1 of Article 6 of the Ministry's Rule for Enforcement of Radio Law of Japan,
2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
3. Use of EVMs only after User obtains the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to EVMs. Also, do not transfer EVMs, unless User gives the same notice above to the transferee. Please note that if User does not follow the instructions above, User will be subject to penalties of Radio Law of Japan.

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西新宿三井ビル

3.3.3 *Notice for EVMs for Power Line Communication:* Please see http://www.tij.co.jp/lsds/ti_ja/general/eStore/notice_02.page

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4 *EVM Use Restrictions and Warnings:*

4.1 EVMS ARE NOT FOR USE IN FUNCTIONAL SAFETY AND/OR SAFETY CRITICAL EVALUATIONS, INCLUDING BUT NOT LIMITED TO EVALUATIONS OF LIFE SUPPORT APPLICATIONS.

4.2 User must read and apply the user guide and other available documentation provided by TI regarding the EVM prior to handling or using the EVM, including without limitation any warning or restriction notices. The notices contain important safety information related to, for example, temperatures and voltages.

4.3 *Safety-Related Warnings and Restrictions:*

4.3.1 User shall operate the EVM within TI's recommended specifications and environmental considerations stated in the user guide, other available documentation provided by TI, and any other applicable requirements and employ reasonable and customary safeguards. Exceeding the specified performance ratings and specifications (including but not limited to input and output voltage, current, power, and environmental ranges) for the EVM may cause personal injury or death, or property damage. If there are questions concerning performance ratings and specifications, User should contact a TI field representative prior to connecting interface electronics including input power and intended loads. Any loads applied outside of the specified output range may also result in unintended and/or inaccurate operation and/or possible permanent damage to the EVM and/or interface electronics. Please consult the EVM user guide prior to connecting any load to the EVM output. If there is uncertainty as to the load specification, please contact a TI field representative. During normal operation, even with the inputs and outputs kept within the specified allowable ranges, some circuit components may have elevated case temperatures. These components include but are not limited to linear regulators, switching transistors, pass transistors, current sense resistors, and heat sinks, which can be identified using the information in the associated documentation. When working with the EVM, please be aware that the EVM may become very warm.

4.3.2 EVMs are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems. User assumes all responsibility and liability for proper and safe handling and use of the EVM by User or its employees, affiliates, contractors or designees. User assumes all responsibility and liability to ensure that any interfaces (electronic and/or mechanical) between the EVM and any human body are designed with suitable isolation and means to safely limit accessible leakage currents to minimize the risk of electrical shock hazard. User assumes all responsibility and liability for any improper or unsafe handling or use of the EVM by User or its employees, affiliates, contractors or designees.

4.4 User assumes all responsibility and liability to determine whether the EVM is subject to any applicable international, federal, state, or local laws and regulations related to User's handling and use of the EVM and, if applicable, User assumes all responsibility and liability for compliance in all respects with such laws and regulations. User assumes all responsibility and liability for proper disposal and recycling of the EVM consistent with all applicable international, federal, state, and local requirements.

5. *Accuracy of Information:* To the extent TI provides information on the availability and function of EVMs, TI attempts to be as accurate as possible. However, TI does not warrant the accuracy of EVM descriptions, EVM availability or other information on its websites as accurate, complete, reliable, current, or error-free.

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