

10V Drive Nch MOS FET

RDN120N25

●Structure

Silicon N-channel
MOS FET

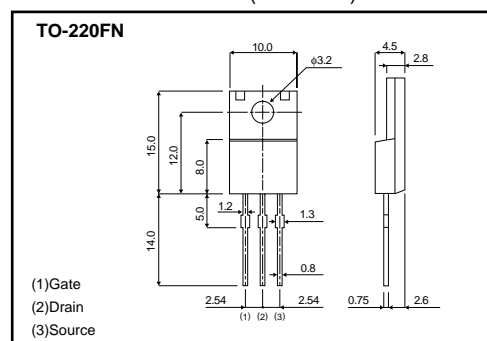
●Features

- 1) Low on-resistance.
- 2) Low input capacitance.
- 3) Excellent resistance to damage from static electricity.

●Application

Switching

●External dimensions (Unit : mm)



●Packaging specifications

Type	Package	Bulk
	Code	-
	Basic ordering unit (pieces)	500
RDN120N25		○

●Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	V _{DSS}	250	V
Gate-Source Voltage	V _{GSS}	±30	V
Drain Current	Continuous	I _D	12 A
	Pulsed	I _{DP} *1	48 A
Reverse Drain Current	Continuous	I _{DR}	12 A
	Pulsed	I _{DRP} *1	48 A
Source Current (Body diode)	Continuous	I _S	12 A
	Pulsed	I _{SP} *1	48 A
Avalanche Current	I _{AS} *2	12	A
Avalanche Energy	E _{AS} *2	216	mJ
Total Power Dissipation (T _C =25°C)	P _D	40	W
Channel Temperature	T _{ch}	150	°C
Storage Temperature	T _{stg}	-55 to +150	°C

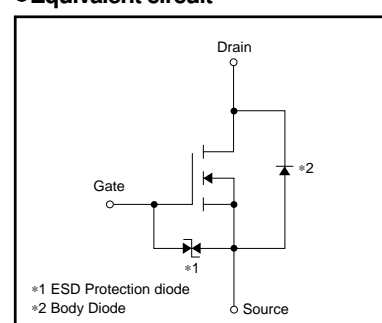
*1 P_w ≤ 10μs, Duty cycle ≤ 1%

*2 L ≈ 2.4mH, V_{DD}=50V, R_G=25Ω, 1Pulse, T_{ch}=25°C

●Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to case	R _{th(ch-c)}	3.13	°C/W
Channel to ambient	R _{th(ch-a)}	62.5	°C/W

●Equivalent circuit



*A protection diode is included between the gate and the source terminals to protect the diode against static electricity when the product is in use. Use the protection circuit when the fixed voltages are exceeded.

Transistors

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Gate-Source Leakage	I _{gss}	—	—	±10	μA	V _{GS} =±30V, V _{DS} =0V
Drain-Source Breakdown Voltage	V _{(BR) DSS}	250	—	—	V	I _D =250μA, V _{GS} =0V
Zero Gate Voltage Drain Current	I _{DSS}	—	—	25	μA	V _{DS} =250V, V _{GS} =0V
Gate Threshold Voltage	V _{GS(th)}	2.0	—	4.0	V	V _{DS} =10V, I _D =1mA
Static Drain-Source On-State Resistance	R _{DS(on)} *	—	0.16	0.21	Ω	I _D =6A, V _{GS} =10V
Forward Transfer Admittance	Y _{fs} *	3.7	6.1	—	S	V _{DS} =10V, I _D =6.0A
Input Capacitance	C _{iss}	—	1224	—	pF	V _{DS} =10V
Output Capacitance	C _{oss}	—	443	—	pF	V _{GS} =0V
Reverse Transfer Capacitance	C _{rss}	—	154	—	pF	f=1MHz
Turn-On Delay Time	t _{d(on)} *	—	17	—	ns	I _D =6.0A, V _{DD} ≐100V
Rise Time	t _r *	—	32	—	ns	V _{GS} =10V
Turn-Off Delay Time	t _{d(off)} *	—	58	—	ns	R _L =16.7Ω
Fall Time	t _f *	—	28	—	ns	R _G =10Ω
Total Gate Charge	Q _g *	—	31.0	62.0	nC	V _{DD} =125V
Gate-Source Charge	Q _{gs} *	—	7.0	—	nC	V _{GS} =10V
Gate-Drain Charge	Q _{gd} *	—	11.0	—	nC	I _D =12A

* Pulsed

●Body diode characteristics (Source-drain) (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Forward voltage	V _{SD} *	—	—	2.0	V	I _S = 6A, V _{GS} =0V
Reverse recovery time	t _{rr} *	—	169	—	ns	I _{DR} = 12A, V _{GS} =0V
Reverse recovery charge	Q _{rr} *	—	0.95	—	μC	di/dt= 100A / μs

* Pulsed

Transistors

●Electrical characteristic curves

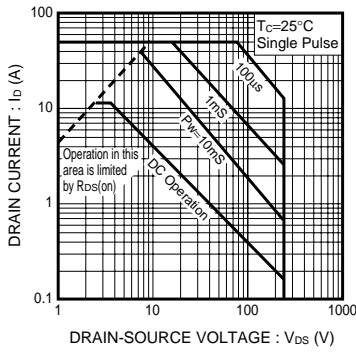


Fig.1 Maximum Safe Operating Area

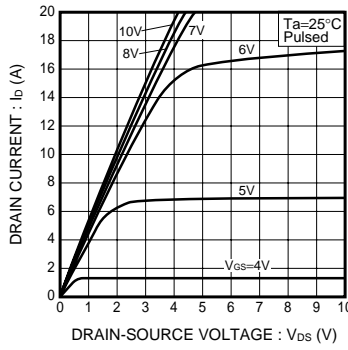


Fig.2 Typical Output Characteristics

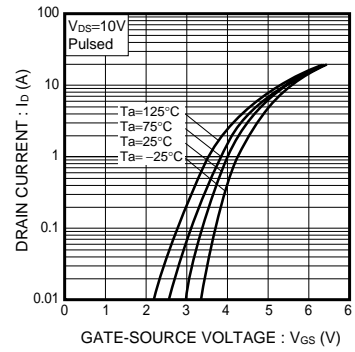


Fig.3 Typical Transfer Characteristics

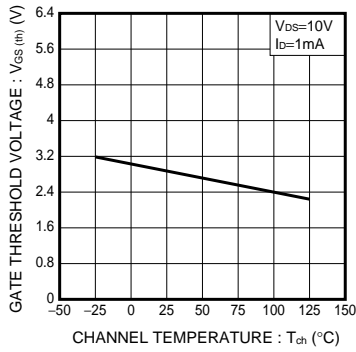


Fig.4 Gate Threshold Voltage vs. Channel Temperature

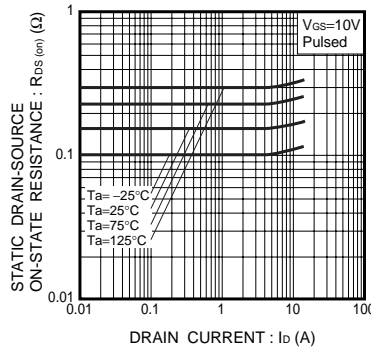


Fig.5 Static Drain-Source On-State Resistance vs. Drain Current

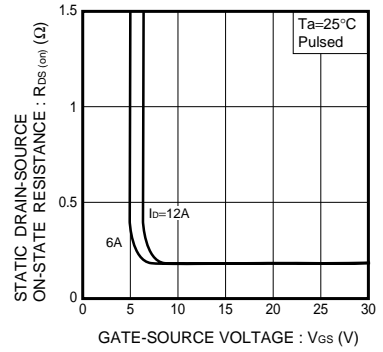


Fig.6 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

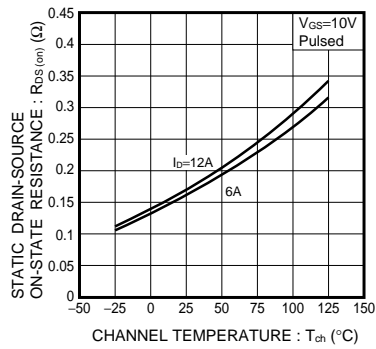


Fig.7 Static Drain-Source On-State Resistance vs. Channel Temperature

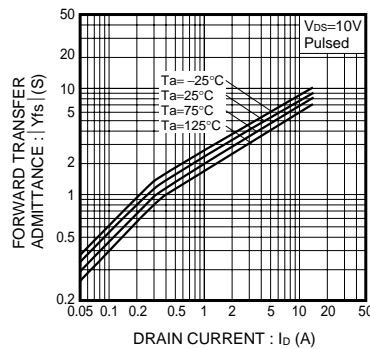


Fig.8 Forward Transfer Admittance vs. Drain Current

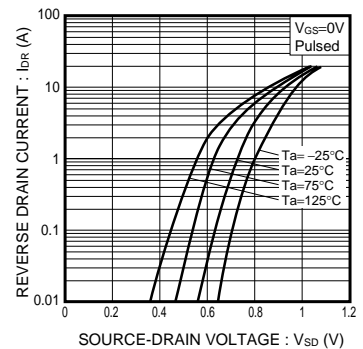


Fig.9 Reverse Drain Current vs. Source-Drain Voltage

Transistors

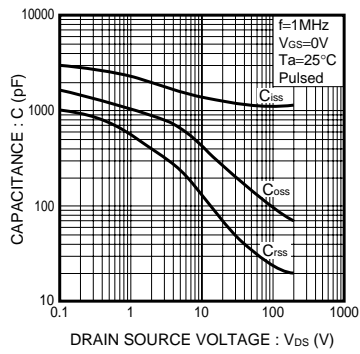


Fig.10 Typical Capacitance vs. Drain-Source Voltage

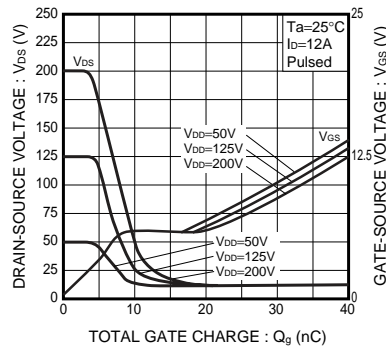


Fig.11 Dynamic Input Characteristics

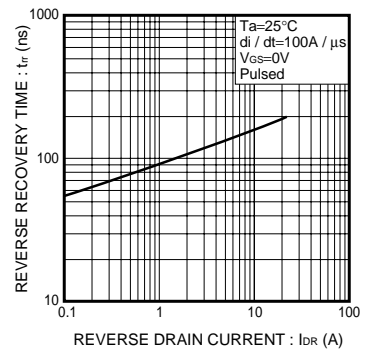


Fig.12 Reverse Recovery Time vs. Reverse Drain Current

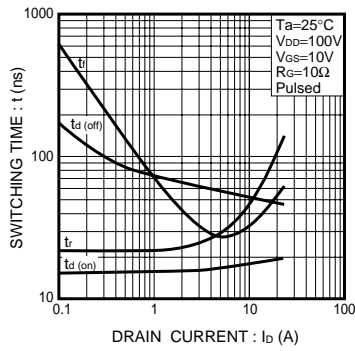


Fig.13 Switching Characteristics

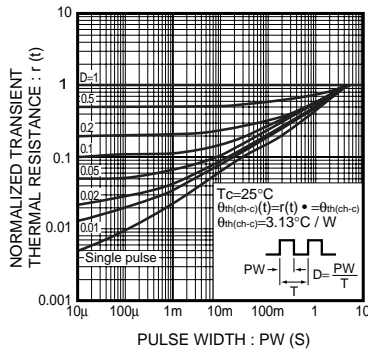


Fig.14 Normalized Transient Thermal Resistance vs. Pulse Width

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