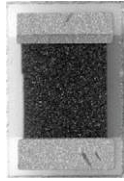


## Thin Film Microwave Resistor



Product may not be to scale

The MIF resistor chips on alumina are designed with low shunt capacitance. Resistor geometrics are compatible with strip lines, making them ideally suited for microwave circuits.

These chips are manufactured using Vishay Electro-Films (EFI) sophisticated thin film equipment and manufacturing technology. The MIFs are 100 % electrically tested and visually inspected to MIL-STD-883, method 2032 class H or K.

### FEATURES

- Wire bondable
- High frequency
- Small single chip size: 0.016" x 0.020"
- Case: 02016
- Microwave resistance range: 20  $\Omega$  to 100  $\Omega$
- Overall resistance range: 20  $\Omega$  to 2 k $\Omega$
- Alumina substrate
- Low stray capacitance: < 0.2 pF
- Resistor material: Tantalum nitride, self passivating
- Moisture resistant
- Power: 50 mW
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)



**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**  
**GREEN**  
(5-2008)

### APPLICATIONS

Vishay EFI MIF chip resistors provide excellent high frequency response and are ideally suited for prototyping. Typical application areas are:

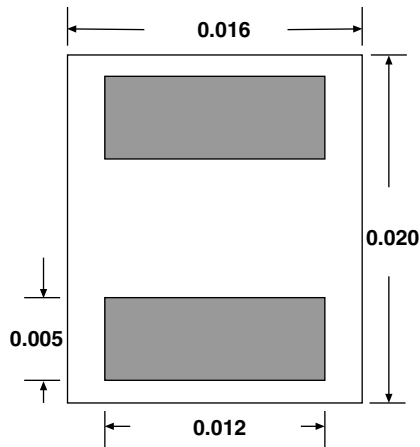
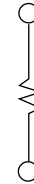
- Amplifiers
- Oscillators
- Attenuators
- Couplers
- Filters

### TEMPERATURE COEFFICIENT OF RESISTANCE, VALUES, AND TOLERANCES

| PARAMETER        | VALUE                                   | UNIT              |
|------------------|---|-------------------|
| Resistance Range | 20 to 100                               | $\Omega$          |
| Tolerances       | $\pm 1, \pm 5, \pm 10, \pm 20$ standard | %                 |
| TCR              | $\pm 100$                               | ppm/ $^{\circ}$ C |

### STANDARD ELECTRICAL SPECIFICATIONS

| PARAMETER  | VALUE                        | UNIT         |
|--|------------------------------|--------------|
| Noise, MIL-STD-202, Method 308   | - 20 typ.                    | dB           |
| Moisture Resistance, MIL-STD-202, Method 106                               | $\pm 0.5$ max. $\Delta R/R$  | %            |
| Stability, 1000 h, + 125 $^{\circ}$ C, 25 mW                               | $\pm 0.5$ max. $\Delta R/R$  | %            |
| Operating Temperature Range  | - 55 to + 125                | $^{\circ}$ C |
| Thermal Shock, MIL-STD-202, Method 107, Test Condition F                   | $\pm 0.25$ max. $\Delta R/R$ | %            |
| High Temperature Exposure + 150 $^{\circ}$ C, 1000 h                       | $\pm 0.5$ max. $\Delta R/R$  | %            |
| Dielectric Voltage Breakdown   | 400                          | V            |
| Insulation Resistance  | $10^{12}$ min.               | $\Omega$     |
| Operating Voltage  | 100 max.                     | V            |
| DC Power Rating at + 70 $^{\circ}$ C (Derated to Zero at 150 $^{\circ}$ C) | 0.050 max.                   | W            |
| 5 x Rated Power Short-Time Overload, + 25 $^{\circ}$ C, 5 s                | $\pm 0.25$ max. $\Delta R/R$ | %            |

**DIMENSIONS** in inches

**SCHEMATIC**


| MECHANICAL SPECIFICATIONS |  |
|---------------------------|--|
| PARAMETER                 |  |
| Chip Size                 | 0.016" x 0.020" ± 0.003" (0.40 mm x 0.5 mm ± 0.076 mm) |
| Chip Thickness            | 0.010" ± 0.001" (0.25 mm ± 0.025 mm)                   |
| Chip Substrate Material   | 99.6 % alumina, 2 μ" to 4 μ" finish                    |
| Resistor Material         | Tantalum nitride, self-passivating                     |
| Bonding Pad Size          | 0.005" x 0.012" (0.125 mm x 0.30 mm)                   |
| Number of Pads            | 2  |
| Pad Material              | 25 kÅ minimum gold standard                            |
| Backing                   | None   |

| GLOBAL PART NUMBER INFORMATION  |  |   |  |  |   |                    |                                  |  |   |          |          |          |          |          |          |
|---|--|---|--|--|---|--------------------|----------------------------------|--|---|----------|----------|----------|----------|----------|----------|
| Global Part Number: <b>MIF5000BKKMGNHWS</b>   |  |   |  |  |   |                    |                                  |  |   |          |          |          |          |          |          |
| Global Part Number Description: <b>MIF 50 10 %, 100 ppm/°C, Mic trim, Au term, no back metal, class H, WS</b> |  |   |  |  |   |                    |                                  |  |   |          |          |          |          |          |          |
| <b>M</b>  | <b>I</b>   | <b>F</b>  | <b>5</b>   | <b>0</b>   | <b>0</b>                                    | <b>0</b>           | <b>B</b>                         | <b>K</b>                                 | <b>K</b>                                | <b>M</b> | <b>G</b> | <b>N</b> | <b>H</b> | <b>W</b> | <b>S</b> |
| <b>MODEL</b>  | <b>RESISTANCE</b>                                    | <b>RESISTANCE MULTIPLIER CODE</b>                 | <b>TOL. CODE (%)</b>   | <b>TCR (ppm/°C)</b>  | <b>TRIM STYLE</b>                           | <b>TERMINATION</b> | <b>BACK METAL</b>                | <b>VISUAL CLASS</b>                      | <b>PACKAGING CODE</b>                   |          |          |          |          |          |          |
| <b>MIF</b><br>20 x 16 size microwave resistor TaN on alumina  | First 4 digits are significant figures of resistance | <b>B</b> = 0.01<br><b>A</b> = 0.1<br><b>0</b> = 1 | <b>F</b> = 1.0<br><b>G</b> = 2.0<br><b>H</b> = 2.5<br><b>J</b> = 5.0<br><b>K</b> = 10<br><b>M</b> = 20 | <b>K</b> = ± 100<br><b>L</b> = ± 200<br><b>R</b> = 0/- 250 | <b>M</b> = Microwave<br><b>S</b> = Standard | <b>G</b> = Au      | <b>G</b> = Au<br><b>N</b> = None | <b>H</b> = Class H<br><b>K</b> = Class K | <b>WS</b> = Waffle pack 100 min, 1 mult |          |          |          |          |          |          |



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