

- Sourced from process 47.
- See MPSH11 for characteristics.

1. Base 2. Emitter 3. Collector

MPSH34

# Absolute Maximum Ratings ${\rm T_a=25^{\circ}C}$ unless otherwise noted

Symbol	Parameter		Value	Units
V <sub>CEO</sub>	Collector-Emitter Voltage		40	V
V <sub>CBO</sub>	Collector-Base Voltage		40	V
V <sub>EBO</sub>	Emitter-Base Voltage		4.0	V
I <sub>C</sub>	Collector current	- Continuous	50	mA
T <sub>J</sub> , T <sub>stg</sub>	Junction and Storage Temperature		-55 ~ +150	°C

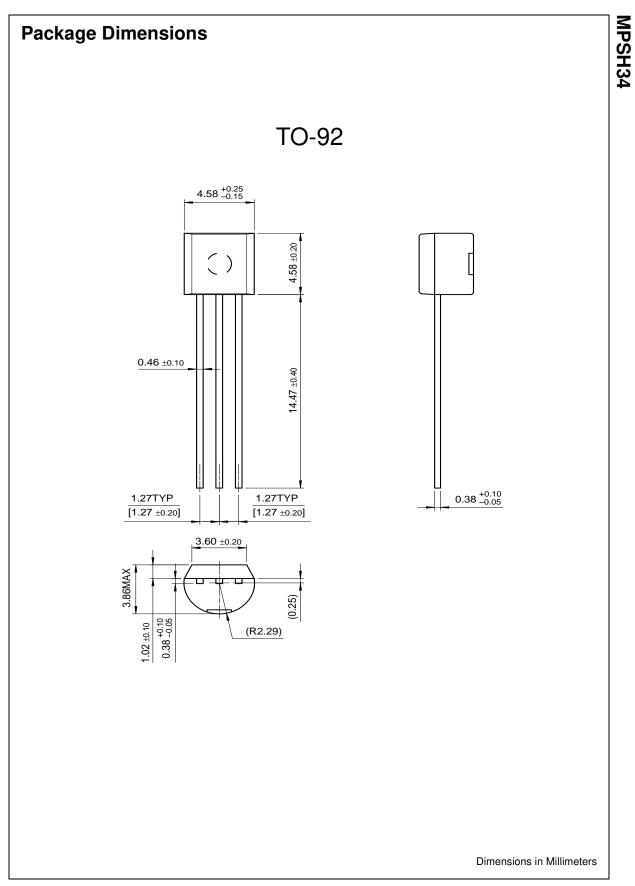
# Electrical Characteristics T<sub>a</sub>=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Max.	Units
Off Characte	eristics	·			
V <sub>(BR)CEO</sub>	Collector-Emitter Sustaining Voltage *	$I_{\rm C} = 1.0 {\rm mA}, I_{\rm B} = 0$	40		V
V <sub>(BR)CBO</sub>	Collector-Base Breakdown Voltage	$I_{\rm C} = 100 \mu {\rm A}, I_{\rm E} = 0$	40		
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown Voltage	$I_{\rm E} = 10 \mu A, I_{\rm C} = 0$	4.0		VV
I <sub>CBO</sub>	Collector Cutoff Current	$V_{CB} = 30V, I_E = 0$		50	nA
On Characte	eristics	-			
h <sub>FE</sub>	DC Current Gain	$V_{CE} = 2.0V, I_C = 20mA$ $V_{CE} = 15V, I_C = 7.0mA$	15 40		
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = 7.0mA, I <sub>B</sub> = 2.0mA		0.5	V
V <sub>BE(on)</sub>	Base-Emitter On Voltage	V <sub>CE</sub> = 15V, I <sub>C</sub> = 7.0mA		0.95	V
	I Characteristics	•	•		
f <sub>T</sub>	Current Gain Bandwidth Product	I <sub>C</sub> =15mA, V <sub>CE</sub> = 15V, f = 100MHz	500		MHz
C <sub>cb</sub>	Collector-Base Capacitance	$V_{CB} = 10V, I_{E} = 0, f = 1.0MHz$		0.32	pF

\* Pulse Test: Pulse Width  $\leq$  300µs, Duty Cycle  $\leq$  2.0%

# Thermal Characteristics TA=25°C unless otherwise noted

Symbol	Parameter	Max.	Units	
PD	Total Device Dissipation	625	mW	
	Derate above 25°C	5.0	mW/°C	
R <sub>θJC</sub>	Thermal Resistance, Junction to Case	83.3	°C/W	
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction to Ambient	200	°C/W	



## TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

ACEx <sup>™</sup> ActiveArray <sup>™</sup> Bottomless <sup>™</sup> CoolFET <sup>™</sup> <i>CROSSVOLT</i> <sup>™</sup> DOME <sup>™</sup> EcoSPARK <sup>™</sup> E <sup>2</sup> CMOS <sup>™</sup> EnSigna <sup>™</sup> FACT <sup>™</sup> Across the board. The Power Franch	FACT Quiet Series <sup>™</sup> FAST <sup>®</sup> FASTr <sup>™</sup> FRFET <sup>™</sup> GlobalOptoisolator <sup>™</sup> GTO <sup>™</sup> HiSeC <sup>™</sup> I <sup>2</sup> C <sup>™</sup> ImpliedDisconnect <sup>™</sup> ISOPLANAR <sup>™</sup> Around the world. <sup>™</sup> nise <sup>™</sup>	LittleFET <sup>™</sup> MICROCOUPLER <sup>™</sup> MicroFET <sup>™</sup> MICROWIRE <sup>™</sup> MSX <sup>™</sup> MSXPro <sup>™</sup> OCX <sup>™</sup> OCX <sup>™</sup> OCXPro <sup>™</sup> OPTOLOGIC <sup>®</sup> OPTOPLANAR <sup>™</sup> PACMAN <sup>™</sup>	Power247 <sup>™</sup> PowerTrench <sup>®</sup> QFET <sup>®</sup> QS <sup>™</sup> QT Optoelectronics <sup>™</sup> Quiet Series <sup>™</sup> RapidConfigure <sup>™</sup> RapidConnect <sup>™</sup> SILENT SWITCHER <sup>®</sup> SMART START <sup>™</sup> SPM <sup>™</sup> Stealth <sup>™</sup>	SuperSOT <sup>™</sup> -6 SuperSOT <sup>™</sup> -8 SyncFET <sup>™</sup> TinyLogic <sup>®</sup> TINYOPTO <sup>™</sup> TruTranslation <sup>™</sup> UHC <sup>™</sup> UltraFET <sup>®</sup> VCX <sup>™</sup>
	nise™	PACMAN™ POP™	Stealth™ SuperSOT™-3	

#### DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

### LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

## As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to

result in significant injury to the user.

2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

# **PRODUCT STATUS DEFINITIONS**

#### **Definition of Terms**

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.