



P-Channel 20-V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)	Q _g (Typ.)		
	0.027 at V _{GS} = - 4.5 V	- 7			
- 20	0.035 at $V_{GS} = -2.5 \text{ V}$	- 6.2	21		
	0.048 at V _{GS} = - 1.8 V	- 5.2			

TSOP-6 Top View 1 6 1 3 mm 2 5 1 3 4 1

Ordering Information: Si3493DV-T1-E3 (Lead (Pb)-free)

Si3493DV-T1-GE3 (Lead (Pb)-free and Halogen-free)

Marking Code: 93xxx

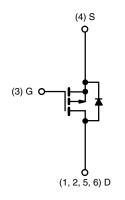
FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFET: 1.8 V Rated
- Ultra-Low On-Resistance
- Compliant to RoHS Directive 2002/95/EC



APPLICATIONS

- Load Switch
- PA Switch
- Battery Switch



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	$T_A = 25 ^{\circ}C$, unles	ss otherwise n	oted		
Parameter		Symbol	5 s	Steady State	Unit
Drain-Source Voltage		V _{DS}	- 20		V
Gate-Source Voltage		V_{GS}	± 8		
0 D . 0 (T 150.00)3	T _A = 25 °C	I _D	- 7	- 5.3	
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 85 °C		- 3.6	- 3.9	
Pulsed Drain Current		I _{DM}	- 20		Α
Continuous Source Current (Diode Conduction) ^a		I _S	- 1.7	- 0.9	
Maximum Power Dissipation ^a	T _A = 25 °C	P _D	2.0	1.1	W
	T _A = 85 °C		1.0	0.6	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Mariana la Antique	t ≤ 5 s	R _{thJA}	45	62.5	°C/W	
Maximum Junction-to-Ambient ^a	Steady State		90	110		
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	25	30		

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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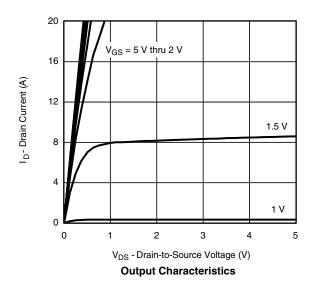
Parameter	Symbol	Test Conditions Mi		Тур.	Max.	Unit	
Static			•	•			
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 0.40		- 1	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 20 V, V _{GS} = 0 V			- 1	μΑ	
		V_{DS} = - 20 V, V_{GS} = 0 V, T_{J} = 85 °C			- 5		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	- 20			Α	
Drain-Source On-State Resistance ^a		V _{GS} = - 4.5 V, I _D = - 7 A		0.022	0.027	Ω	
	R _{DS(on)}	$V_{GS} = -2.5 \text{ V}, I_D = -6.2 \text{ A}$		0.029	0.035		
		V _{GS} = - 1.8 V, I _D = - 3 A		0.039	0.048		
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 5 V, I _D = - 7 A		25		S	
Diode Forward Voltage ^a	V_{SD}	I _S = - 1.7 A, V _{GS} = 0 V		- 0.7	- 1.2	V	
Dynamic ^b	'		•	•			
Total Gate Charge	Q_g			21	32		
Gate-Source Charge	Q _{gs}	$V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -7 \text{ A}$		2.6		nC	
Gate-Drain Charge	Q_{gd}			6		1	
Turn-On Delay Time	t _{d(on)}			20	30		
Rise Time	t _r	V_{DD} = - 10 V, R_L = 10 Ω		40	60		
Turn-Off Delay Time	t _{d(off)}	$I_D\cong$ - 1 A, V_{GEN} = - 4.5 V, R_g = 6 Ω		125	190	ns	
Fall Time	t _f			85	130		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 1.7 A, dl/dt = 100 A/μs		64	90		

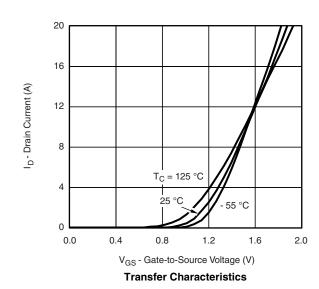
Notes:

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



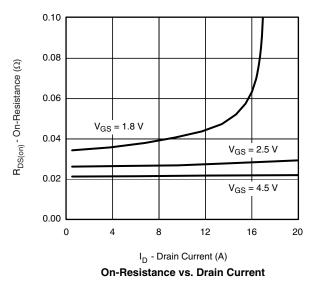


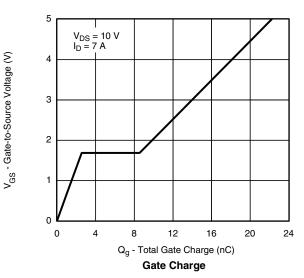


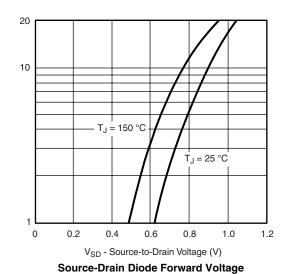




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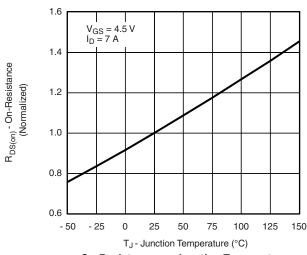




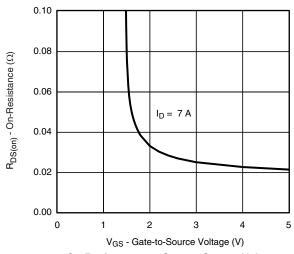


2500
2500
C_{iss}
1500
C_{oss}
C_{rss}
0
0
4
8
12
16
20

V_{DS} - Drain-to-Source Voltage (V) **Capacitance**



On-Resistance vs. Junction Temperature



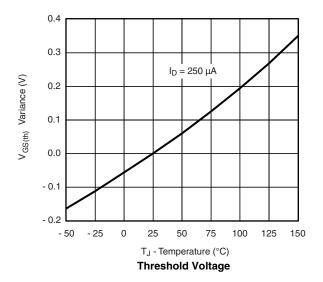
On-Resistance vs. Gate-to-Source Voltage

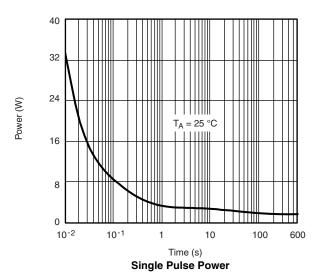
Is - Source Current (A)

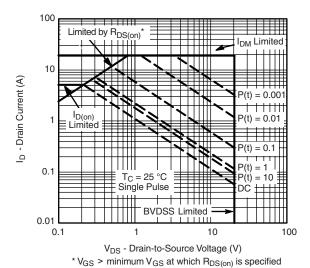
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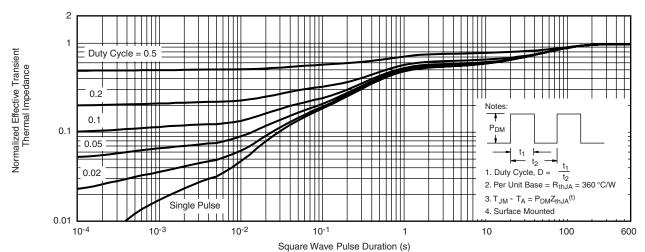
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted







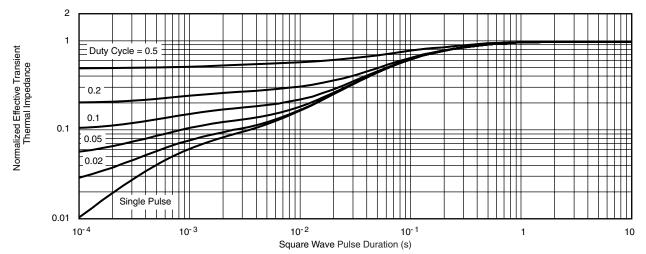




Normalized Thermal Transient Impedance, Junction-to-Ambient



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

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