

NPN 100mA 50V Digital Transistor (Bias Resistor Built-in Transistor)

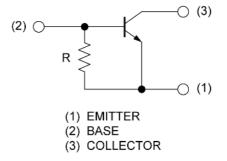
Parameter	Value	
V _{CEO}	50V	
I _C	100mA	
R	22kΩ	

● Outline SOT-323 SC-70 (2) (1) (UMT3)

Features

1) Built-In Biasing Resistors, R = $22k\Omega$

•Inner circuit



Application

INVERTER, INTERFACE, DRIVER

Packaging specifications

Part No.	Package	Package size	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit.(pcs)	Marking
DTC124GUA	SOT-323 (UMT3)	2021	T106	180	8	3000	K25

● Absolute maximum ratings (T_a = 25°C)

Parameter	Symbol	Values	Unit
Collector-base voltage	V_{CBO}	50	V
Collector-emitter voltage	V _{CEO}	50	V
Emitter-base voltage	V _{EBO}	5	V
Collector current	I _C	100	mA
Power dissipation	P _D *1	200	mW
Junction temperature	T _j	150	°C
Range of storage temperature	T _{stg}	-55 to +150	°C

● Electrical characteristics (T_a = 25°C)

Davameter	Cumbal	Conditions	Values			l limit	
Parameter	Symbol Conditions		Min.	Тур.	Max.	Unit	
Collector-base breakdown voltage	BV _{CBO}	I _C = 50μA	50	-	-	V	
Collector-emitter breakdown voltage	BV _{CEO}	BV _{CEO} I _C = 1mA		-	-	V	
Emitter-base breakdown voltage	BV _{EBO}	I _E = 330μA	5	-	-	V	
Collector cut-off current	I _{CBO}	V _{CB} = 50V	-	1	500	nA	
Emitter cut-off current	I _{EBO}	V _{EB} = 4V	140	-	260	μA	
Collector-emitter saturation voltage	V _{CE(sat)}	I _C = 10mA, I _B = 0.5mA	-	-	300	mV	
DC current gain	h _{FE}	$V_{CE} = 5V$, $I_{C} = 5mA$	56	1	-	-	
Emitter-base resistance	R	-	15.4	22	28.6	kΩ	
Transition frequency	f _T *2	$V_{CE} = 10V, I_{E} = -5mA,$ f = 100MHz	-	250	-	MHz	

^{*1} Each terminal mounted on a reference land.

^{*2} Characteristics of built-in transistor

● Electrical characteristic curves (T_a =25°C)

Fig.1 Grounded emitter propagation characteristics

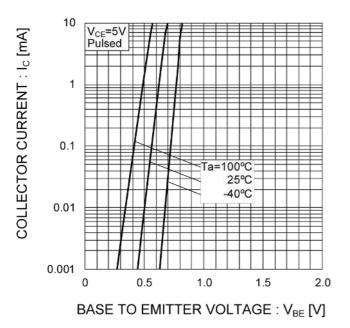
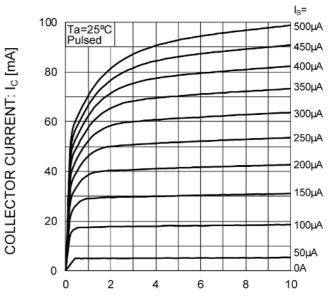


Fig.2 Grounded emitter output characteristics



COLLECTOR TO EMITTER VOLTAGE: V_{CE} [V]

Fig.3 DC Current gain vs. Collector Current

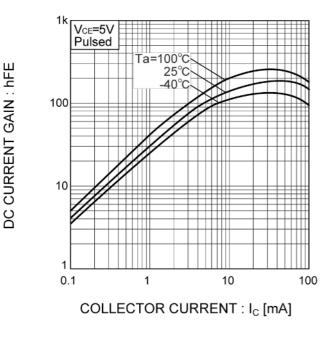
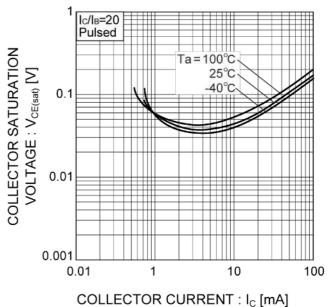
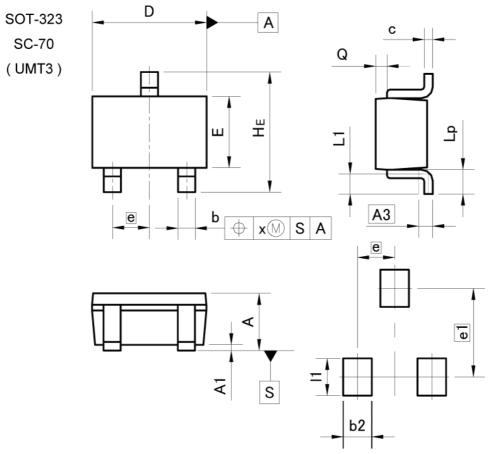


Fig.4 Collector-emitter saturation voltage vs. Collector Current



Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM MILII		ETERS	INC	HES
DIM [MIN	MAX	MIN	MAX
Α	0.80	1.00	0.031	0.039
A1	0.00	0.10	0.000	0.004
A3	0.2	25	0.0	10
b	0.25	0.40	0.010	0.016
С	0.10	0.20	0.004	0.008
D	1.90	2.10	0.075	0.083
E	1.15	1.35	0.045	0.053
е	0.0	65	0.0	26
HE	2.00	2.20	0.079	0.087
L1	0.10	0.40	0.004	0.016
Lp	0.25	0.55	0.010	0.022
Q	0.10	0.30	0.004	0.012
х		0.10	=	0.004

DIM	MILIME	TERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
b2	22	0.50	(<u>3022)</u>	0.020	
e1	1.55		0.0	61	
11	-	0.65	-	0.026	

Dimension in mm/inches

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JAPAN	USA	EU	CHINA
CLASSⅢ	CL ACCIII	CLASSIIb	СГАССШ
CLASSIV	CLASSII	CLASSⅢ	CLASSIII

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 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
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 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
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