## SUP50010E Vishay Siliconix

www.vishay.com

N-Channel 60 V (D-S) MOSFET



PRODUCT SUMMARY					
V <sub>DS</sub> (V)	60				
$R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS}$ = 10 V	0.00200				
$R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS}$ = 7.5 V	0.00250				
Q <sub>g</sub> typ. (nC)	141				
I <sub>D</sub> (A)	150 <sup>d</sup>				
Configuration	Single				

### FEATURES

- TrenchFET<sup>®</sup> power MOSFET
- Maximum 175 °C junction temperature
- Very low  $Q_{gd}$  reduces power loss from passing through  $V_{plateau}$
- 100 %  $R_g$  and UIS tested
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### APPLICATIONS

- Power supply
  Secondary synchronous rectification
- DC/DC converter
- Power tools
- Motor drive switch
- DC/AC inverter
- Battery management
- OR-ing / e-fuse

N-Channel MOSFET

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ORDERING INFORMATION	
Package	TO-220
Lead (Pb)-free and halogen-free	SUP50010E-GE3

<b>ABSOLUTE MAXIMUM RATINGS</b> ( $T_C = 25 \degree C$ , unless otherwise noted)					
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-source voltage		V <sub>DS</sub>	60	- V	
Gate-source voltage		V <sub>GS</sub>	± 20		
Continuous drain current (T <sub>J</sub> = 150 °C)	T <sub>C</sub> = 25 °C		150 <sup>d</sup>		
	T <sub>C</sub> = 70 °C	I <sub>D</sub>	150 <sup>d</sup>	^	
Pulsed drain current (t = 100 µs)		I <sub>DM</sub>	500	- A	
Avalanche current		I <sub>AS</sub>	60		
Single avalanche energy <sup>a</sup> L = 0.1 mH		E <sub>AS</sub>	180	mJ	
Maximum power dissipation <sup>a</sup>	T <sub>C</sub> = 25 °C	D-	375 <sup>b</sup>	- w	
	T <sub>C</sub> = 125 °C	P <sub>D</sub>	125 <sup>b</sup>		
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>stg</sub>	-55 to +175	°C	

THERMAL RESISTANCE RATINGS			
PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-ambient (PCB mount) <sup>c</sup>	R <sub>thJA</sub>	40	°C/W
Junction-to-case (drain)	R <sub>thJC</sub>	0.4	0/10

#### Notes

a. Duty cycle ≤ 1 %

b. See SOA curve for voltage derating

c. When mounted on 1" square PCB (FR4 material)

d. Package limited

S18-1019-Rev. A, 08-Oct-2018

1



**RoHS** COMPLIANT

HALOGEN

FREE

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PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static			•	•		
Drain-source breakdown voltage	V <sub>DS</sub>	$V_{GS}$ = 0 V, $I_D$ = 250 $\mu$ A	60	-	-	
Gate threshold voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	2	-	4	V
Gate-body leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$	-	-	± 250	nA
		$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}$	-	-	1	
Zero gate voltage drain current	I <sub>DSS</sub>	$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 125 ^{\circ}\text{C}$	-	-	150	μA
		$V_{DS} = 60 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 175 ^{\circ}\text{C}$	-	-	5	mA
On-state drain current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \! \geq \! 10$ V, $V_{GS} \! = \! 10$ V	120	-	-	А
		$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 30 \text{ A}$	-	0.00166	0.00200	
Drain-source on-state resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 7.5 \text{ V}, \text{ I}_{D} = 20 \text{ A}$	-	0.00208	0.00250	Ω
Forward transconductance a	9 <sub>fs</sub>	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 30 \text{ A}$	-	120	-	S
Dynamic <sup>b</sup>			•	•	1 1	
Input capacitance	C <sub>iss</sub>		-	10 895	-	
Output capacitance	C <sub>oss</sub>	$V_{GS}$ = 0 V, $V_{DS}$ = 30 V, f = 1 MHz	-	2420	-	pF
Reverse transfer capacitance	C <sub>rss</sub>		-	85	-	
Total gate charge <sup>c</sup>	Qg		-	141	212	
Gate-source charge <sup>c</sup>	Q <sub>gs</sub>	$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 20 \text{ A}$	-	43.6	-	_
Gate-drain charge <sup>c</sup>	Q <sub>gd</sub>		-	19.1	-	nC
Output charge	Q <sub>oss</sub>	$V_{DS} = 50 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	-	143	215	
Gate resistance	R <sub>g</sub>	f = 1 MHz	0.24	1.2	2.4	Ω
Turn-on delay time <sup>c</sup>	t <sub>d(on)</sub>		-	28	56	
Rise time <sup>c</sup>	tr	$V_{DD}$ = 30 V, $R_L$ = 3 $\Omega$	-	12	24	
Turn-off delay time <sup>c</sup>	t <sub>d(off)</sub>	$I_D \cong 10 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$	-	50	100	ns
Fall time <sup>c</sup>	t <sub>f</sub>		-	13	26	
Drain-Source Body Diode Ratings	and Characte	ristics <sup>b</sup> (T <sub>C</sub> = 25 °C)				
Pulsed current (t = 100 µs)	I <sub>SM</sub>		-	-	250	А
Forward voltage <sup>a</sup>	V <sub>SD</sub>	$I_{F} = 10 \text{ A}, V_{GS} = 0 \text{ V}$	-	0.8	1.5	V
Reverse recovery time	t <sub>rr</sub>	I <sub>F</sub> = 34 A, di/dt = 100 A/μs	-	75	150	ns
Peak reverse recovery charge	I <sub>RM(REC)</sub>		-	2.8	5.6	А
Reverse recovery charge	Q <sub>rr</sub>		-	0.12	0.24	μC
Reverse recovery fall time	t <sub>a</sub>		-	38	-	-
Reverse recovery rise time	t <sub>b</sub>		-	37	-	ns

Notes

a. Pulse test; pulse width  $\leq 300~\mu s,~duty~cycle \leq 2~\%$ 

b. Guaranteed by design, not subject to production testing

c. Independent of operating temperature

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

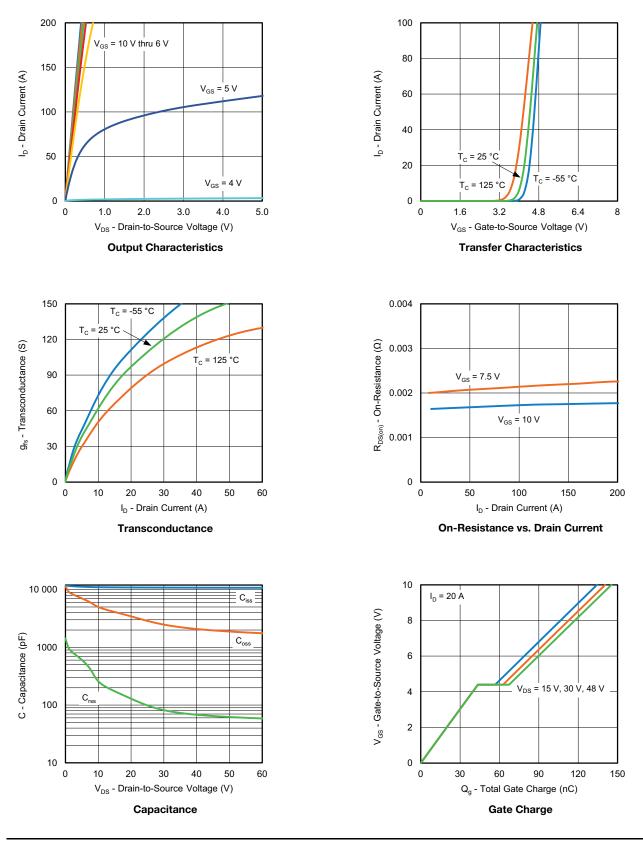
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# SUP50010E

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## **TYPICAL CHARACTERISTICS** (T<sub>A</sub> = 25 °C, unless otherwise noted)



S18-1019-Rev. A, 08-Oct-2018

3

Document Number: 79578

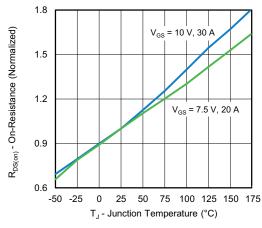
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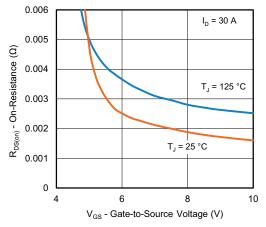
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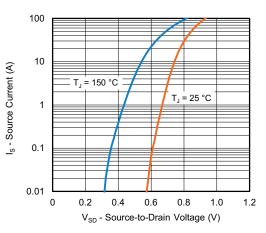
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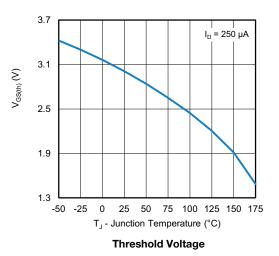
On-Resistance vs. Junction Temperature

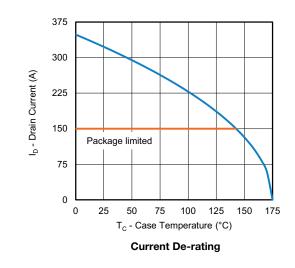


On-Resistance vs. Gate-to-Source Voltage



Source Drain Diode Forward Voltage





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Drain Source Breakdown vs. Junction Temperature

S18-1019-Rev. A, 08-Oct-2018

70

 $I_{D} = 250 \ \mu A$ 

4

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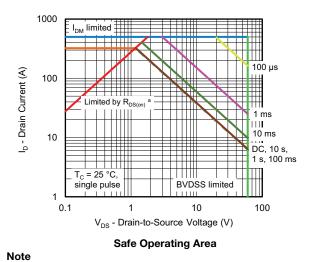
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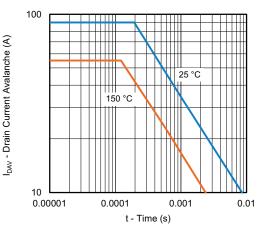


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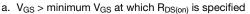
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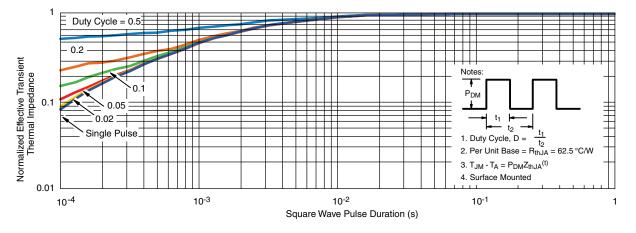
### **THERMAL RATINGS** ( $T_A = 25 \text{ °C}$ , unless otherwise noted)





Single Pulse Avalanche Current Capability vs. Time





Normalized Thermal Transient Impedance, Junction-to-Case

#### Note

• The characteristics shown in the two graphs

- Normalized Transient Thermal Impedance Junction to Ambient (25 °C)

- Normalized Transient Thermal Impedance Junction to Case (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual pplication parameters and operating conditions

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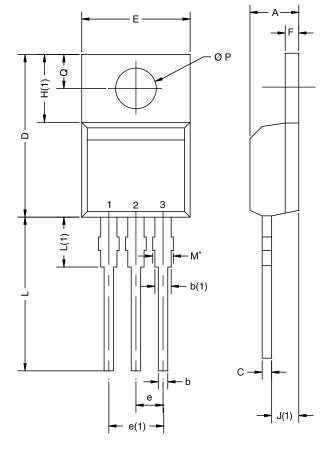
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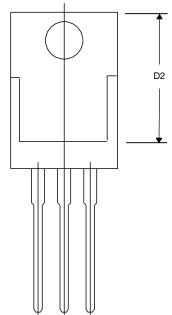
# **TO-220AB**



	MILLIMETERS		INC	HES
DIM.	MIN.	MAX.	MIN.	MAX.
А	4.25	4.65	0.167	0.183
b	0.69	1.01	0.027	0.040
b(1)	1.20	1.73	0.047	0.068
С	0.36	0.61	0.014	0.024
D	14.85	15.49	0.585	0.610
D2	12.19	12.70	0.480	0.500
E	10.04	10.51	0.395	0.414
е	2.41	2.67	0.095	0.105
e(1)	4.88	5.28	0.192	0.208
F	1.14	1.40	0.045	0.055
H(1)	6.09	6.48	0.240	0.255
J(1)	2.41	2.92	0.095	0.115
L	13.35	14.02	0.526	0.552
L(1)	3.32	3.82	0.131	0.150
ØР	3.54	3.94	0.139	0.155
Q	2.60	3.00	0.102	0.118
	0413-Rev. P,		0.102	0.118

Note

 $^{\star}$  M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM



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