

# 40V Nch+Pch Power MOSFET

Symbol	Tr1:Nch	Tr2:Pch
$V_{DSS}$	40V	-40V
R <sub>DS(on)</sub> (Max.)	44mΩ	41mΩ
I <sub>D</sub>	±4.5A	±5.0A
$P_D$	1.5	5W

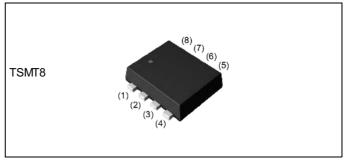
#### Features

- 1) Low on resistance
- 2) Small Surface Mount Package (TSMT8)
- 3) Pb-free plating; RoHS compliant
- 4) Halogen Free

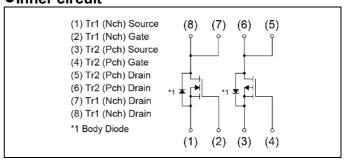
# Application

Switching

#### Outline



## •Inner circuit



Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	180
Туре	Tape width (mm)	8
	Quantity (pcs)	3000
	Taping code	TCR
	Marking	MB5

# ● **Absolute maximum ratings** (T<sub>a</sub> = 25°C ,unless otherwise specified)

Doramata	•	Cymbol	Va	lue	Linit
Parameter		Symbol	Tr1:Nch	Tr2:Pch	Unit
Drain - Source voltage		$V_{DSS}$	40	-40	V
Continuous drain current		I <sub>D</sub>	±4.5	±5.0	Α
Pulsed drain current		I <sub>DP</sub> *1	±18	±20	Α
Gate - Source voltage		$V_{GSS}$	±20	±20	V
Avalanche current, single pulse		I <sub>AS</sub> *2	4.5	-5.0	Α
Avalanche energy, single pulse		E <sub>AS</sub> *2	1.6	2.0	mJ
Down discinction	total	P <sub>D</sub> *3	1	.5	10/
Power dissipation	total	P <sub>D</sub> *4	1	.1	W
Junction temperature		T <sub>j</sub>	15	50	°C
Operating junction and storage t	emperature range	T <sub>stg</sub>	-55 to	+150	°C

#### ●Thermal resistance

Doromotor		Cumphol		Values		Lloit
Parameter		Symbol	Min.	Тур.	Max.	Unit
Thermal registance junction, embient	total	R <sub>thJA</sub> *3	-	-	83.3	°C/W
Thermal resistance, junction - ambient	total	R <sub>thJA</sub> *4	-	-	113	C/VV

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

Davarantas	C: make al	T	Conditions		Values		I lait	
Parameter	Symbol	Type Conditions		Min.	Тур.	Max.	Unit	
Drain - Source breakdown	V	Tr1	$V_{GS} = 0V$ , $I_D = 1mA$	40	-	-	V	
voltage	V <sub>(BR)DSS</sub>	Tr2	$V_{GS} = 0V$ , $I_D = -1mA$	-40	-	-	V	
Breakdown voltage	ΔV <sub>(BR)DSS</sub>	Tr1	I <sub>D</sub> = 1mA, referenced to 25°C	1	28.9	-	mV/°C	
temperature coefficient	$\Delta T_{j}$	Tr2	I <sub>D</sub> = -1mA, referenced to 25°C	1	-22	-	IIIV/ C	
Zero gate voltage		Tr1	$V_{DS} = 40V, V_{GS} = 0V$	ı	-	1		
drain current	I <sub>DSS</sub>	Tr2	$V_{DS} = -40V, V_{GS} = 0V$	ı	-	-1	μA	
Gate - Source		Tr1	$V_{GS} = \pm 20V, V_{DS} = 0V$	ı	-	±100	nA	
leakage current	I <sub>GSS</sub>	Tr2	$V_{GS}$ = ±20V, $V_{DS}$ = 0V	1	-	±100	nA	
Gate threshold	\/	Tr1	$V_{DS} = V_{GS}$ , $I_D = 1mA$	1.0	-	2.5	V	
voltage	V <sub>GS(th)</sub>	Tr2	$V_{DS} = V_{GS}$ , $I_D = -1mA$	-1.0	-	-2.5	V	
Gate threshold voltage	$\Delta V_{GS(th)}$	Tr1	I <sub>D</sub> = 1mA, referenced to 25°C	-	-4.6	-	mV/°C	
temperature coefficient	$\Delta T_j$	Tr2	I <sub>D</sub> = -1mA, referenced to 25°C	1	3.7	-	mv/ C	
		Tr1	V <sub>GS</sub> = 10V, I <sub>D</sub> = 4.5A	-	34	44		
Static drain - source	D *5	111	$V_{GS} = 4.5V, I_D = 4.5A$	-	44	74	m0	
on - state resistance	R <sub>DS(on)</sub> *5	Tr2	$V_{GS} = -10V, I_D = -5.0A$	-	33	41	mΩ	
		112	$V_{GS}$ = -4.5V, $I_{D}$ = -5.0A	-	41	51		
Gate resistance	$R_{G}$	Tr1		-	2.7	-	Ω	
	r\ <sub>G</sub>	Tr2	-	-	16	-	12	
Forward Transfer	IV. 1*5	Tr1	$V_{DS} = 5V, I_{D} = 4.5A$	1.7	-	-	S	
Admittance	Y <sub>fs</sub>  *5	Tr2	$V_{DS} = -5V, I_{D} = -5A$	5.0	-	-	) 	

<sup>\*1</sup> Pw  $\leq$  10 $\mu$ s, Duty cycle  $\leq$  1%

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<sup>\*2</sup> Tr1: L  $\simeq$  0.1mH, V<sub>DD</sub> = 20V, R<sub>G</sub> = 25 $\Omega$ , STARTING T<sub>j</sub> = 25 $^{\circ}$ C Fig.3-1,3-2

Tr2: L  $\simeq$  0.1mH, V<sub>DD</sub> = -20V, R<sub>G</sub> = 25 $\Omega$ , STARTING T<sub>j</sub> = 25 $^{\circ}$ C Fig.6-1,6-2

<sup>\*3</sup> Mounted on a ceramic board (30×30×0.8mm)

<sup>\*4</sup> Mounted on a FR4 (25×25×0.8mm)

<sup>\*5</sup> Pulsed

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

# <Tr1>

Daramatar	Cumphal	Conditions	,	Values		Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	150	-	
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 20V	-	85	-	pF
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	10	-	
Turn - on delay time	t <sub>d(on)</sub> *5	$V_{DD} \simeq 20V$ , $V_{GS} = 10V$	-	5.0	-	
Rise time	t <sub>r</sub> *5	I <sub>D</sub> = 2.25A	-	4.4	-	
Turn - off delay time	t <sub>d(off)</sub> *5	$R_L = 8.9\Omega$	-	11.0	-	ns
Fall time	<b>t</b> <sub>f</sub> *5	$R_G = 10\Omega$	-	2.7	-	

## <Tr2>

Darameter	Symbol Conditions -		,	Unit		
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	920	-	
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = -20V	-	95	-	pF
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	70	-	
Turn - on delay time	t <sub>d(on)</sub> *5	$V_{DD} \simeq$ -20V, $V_{GS}$ = -10V	-	9.2	-	
Rise time	t <sub>r</sub> *5	I <sub>D</sub> = -2.5A	-	18.0	-	
Turn - off delay time	t <sub>d(off)</sub> *5	$R_L = 8\Omega$	-	82.0	-	ns
Fall time	<b>t</b> <sub>f</sub> *5	$R_G = 10\Omega$	-	43.0	-	

# ●Gate charge characteristics (T<sub>a</sub> = 25°C)

## <Tr1>

Doromotor	Cumbal	mbol Conditions		Values			Unit
Parameter	Symbol	Condi	uoris	Min.	Тур.	Max.	Offic
Total gate charge	<b>○</b> *5		V <sub>GS</sub> = 10V	1	3.5	1	
Total gate charge	$Q_g^{*5}$	$V_{DD} \simeq 20V$		-	1.8	-	~C
Gate - Source charge	Q <sub>gs</sub> *5	I <sub>D</sub> = 4.5A	V <sub>GS</sub> = 4.5V	-	1.1	-	nC
Gate - Drain charge	$Q_{gd}^{*5}$			-	0.3	1	

## <Tr2>

Doromotor	Cumabal	ool Conditions		Values			l loit
Parameter	Symbol	Condi	uons	Min.	Тур.	Max.	Unit
Total gate aborgo	$Q_g^{*5}$		V <sub>GS</sub> = -10V	1	17.2	1	
Total gate charge	₹g	$V_{DD} \simeq -20V$		ı	9.0	1	nC
Gate - Source charge	Q <sub>gs</sub> *5	$I_D = -5.0A$	$V_{GS} = -4.5V$	ı	2.7	1	IIC
Gate - Drain charge	$Q_{gd}^{*5}$			-	3.4	-	

# ● Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

## <Tr1>

Parameter	Symbol	Conditions	,	Values	Unit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Continuous forward current	I <sub>S</sub>	T <sub>a</sub> = 25°C	-	-	1.25	^
Pulse forward current	I <sub>SP</sub> *1	1 <sub>a</sub> - 23 C	-	-	18	Α
Forward voltage	$V_{SD}^{*5}$	$V_{GS} = 0V, I_{S} = 1.25A$	-	-	1.2	V
Reverse recovery time	t <sub>rr</sub> *5	I <sub>S</sub> = 4.5A, V <sub>GS</sub> = 0V	-	18	1	ns
Reverse recovery charge	Q <sub>rr</sub> *5	di/dt = 100A/μs	-	11	-	nC

#### <Tr2>

Parameter	Symbol	Conditions	,	Values		
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Continuous forward current	I <sub>S</sub>	T <sub>a</sub> = 25°C	-	-	-1.25	^
Pulse forward current	I <sub>SP</sub> *1	1 <sub>a</sub> - 25 C	-	-	-20	Α
Forward voltage	$V_{SD}^{*5}$	$V_{GS} = 0V, I_{S} = -1.25A$	-	-	-1.2	V
Reverse recovery time	t <sub>rr</sub> *5	I <sub>S</sub> = -5A, V <sub>GS</sub> = 0V	-	25.0	1	ns
Reverse recovery charge	Q <sub>rr</sub> *5	di/dt = 100A/μs	-	22.9	-	nC



Fig.1 Power Dissipation Derating Curve

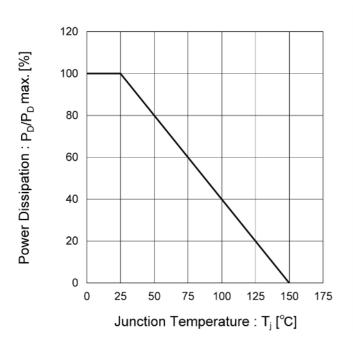


Fig.2 Maximum Safe Operating Area

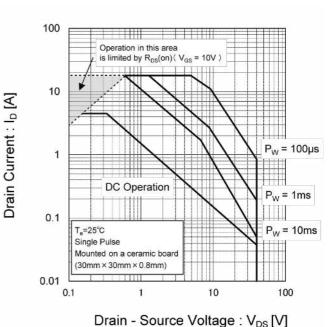


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

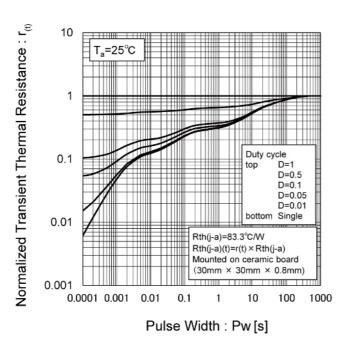


Fig.4 Single Pulse Maximum Power Dissipation

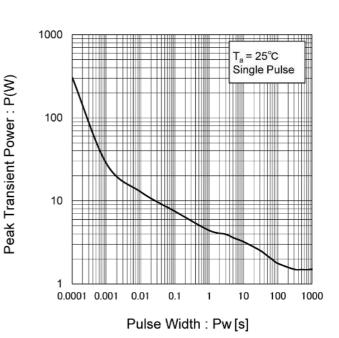


Fig.5 Typical Output Characteristics(I)

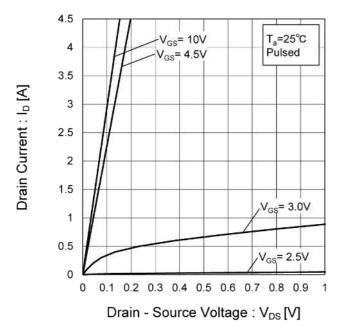
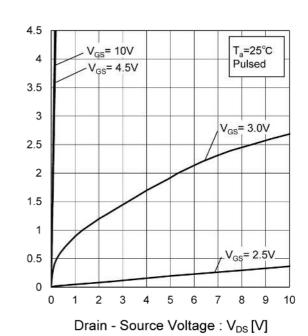


Fig.6 Typical Output Characteristics(II)



Drain Current: Ip [A]

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Fig.7 Breakdown Voltage vs. **Junction Temperature** 

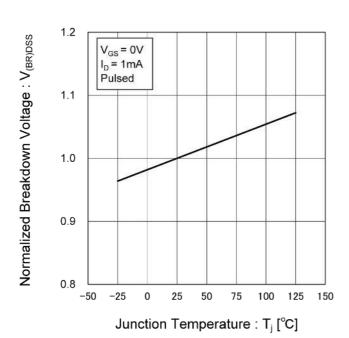


Fig.8 Typical Transfer Characteristics

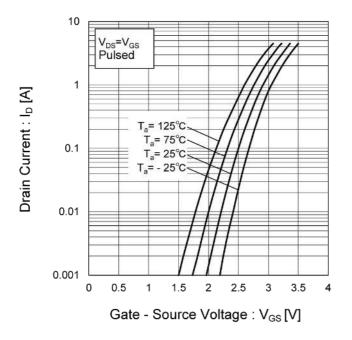
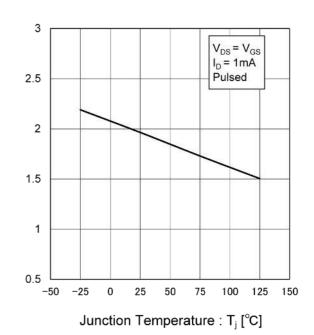


Fig.9 Gate Threshold Voltage vs.
Junction Temperature



Gate Threshold Voltage: V<sub>GS(th)</sub> [V]

Fig.10 Forward Transfer Admittance vs.
Drain Current

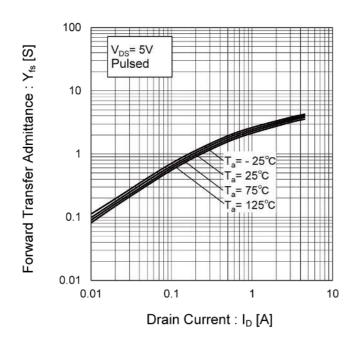


Fig.11 Drain Current Derating Curve

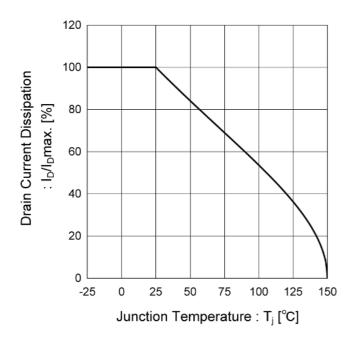


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

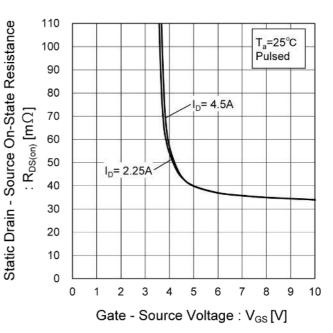


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

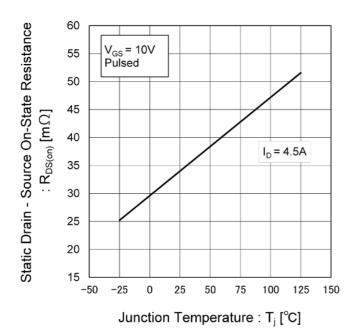


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (I)

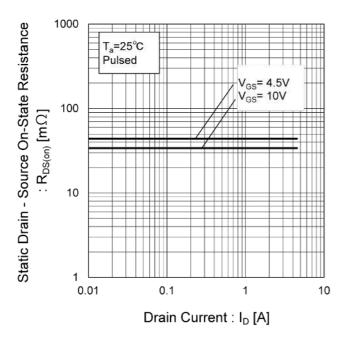


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

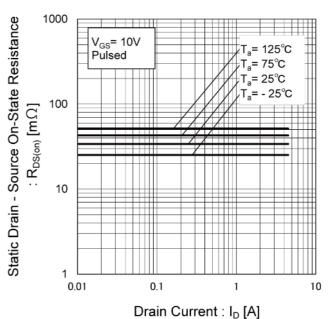


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)

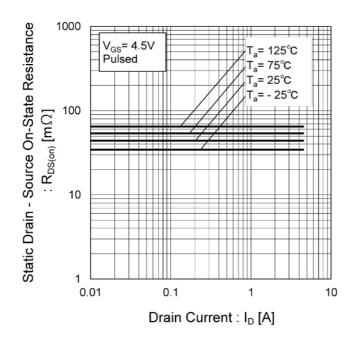


Fig.17 Typical Capacitances vs.

Drain - Source Voltage

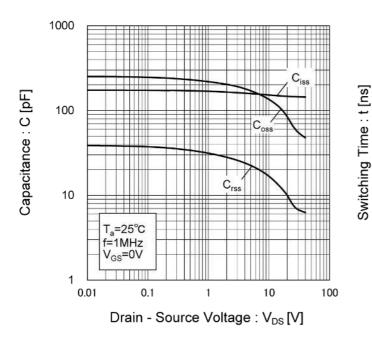


Fig.18 Switching Characteristics

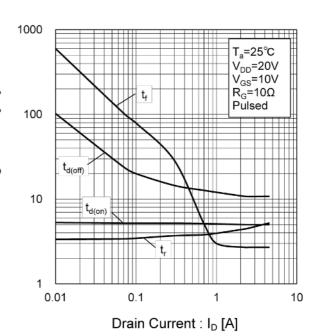


Fig.19 Typical Gate Charge

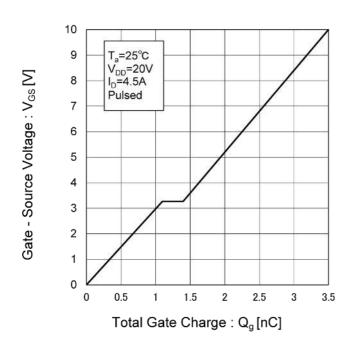
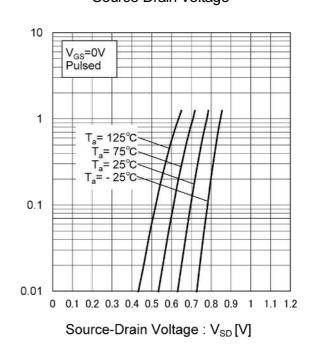


Fig.20 Source Current vs.

Source Drain Voltage



Source Current : Is [A]

Fig.1 Power Dissipation Derating Curve

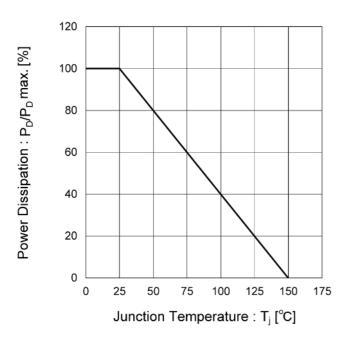
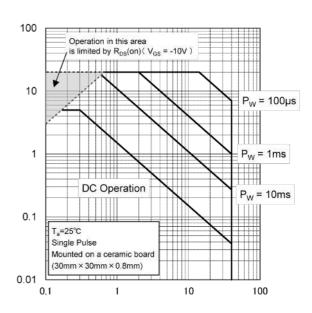


Fig.2 Maximum Safe Operating Area



Drain Current: -I<sub>D</sub> [A]

Drain - Source Voltage : -V<sub>DS</sub> [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

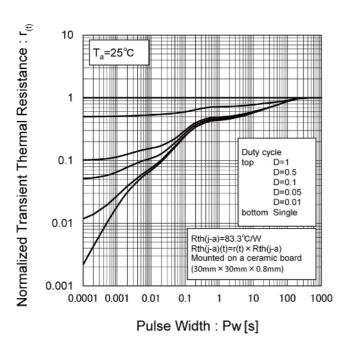
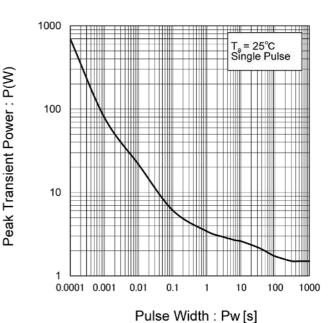


Fig.4 Single Pulse Maximum Power Dissipation



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Fig.5 Typical Output Characteristics(I)

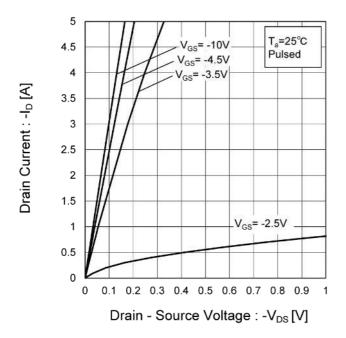
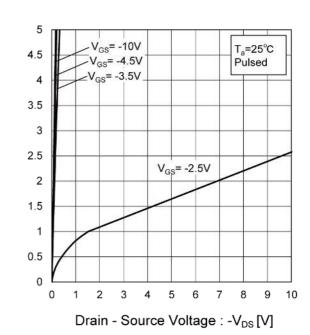


Fig.6 Typical Output Characteristics(II)



Drain Current: -I<sub>D</sub> [A]

Fig.7 Breakdown Voltage vs.
Junction Temperature

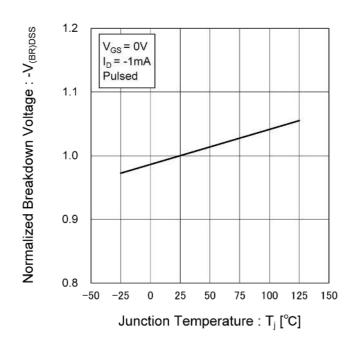


Fig.8 Typical Transfer Characteristics

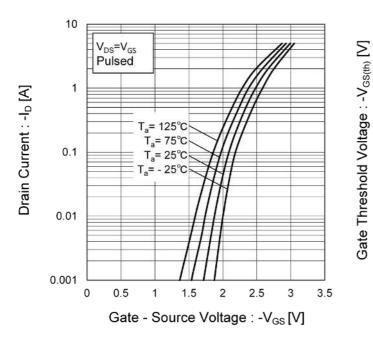


Fig.9 Gate Threshold Voltage vs. **Junction Temperature** 

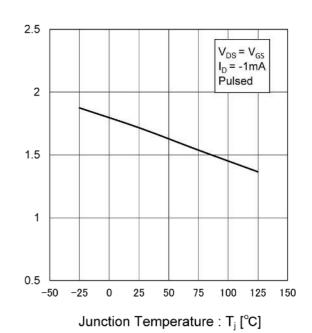
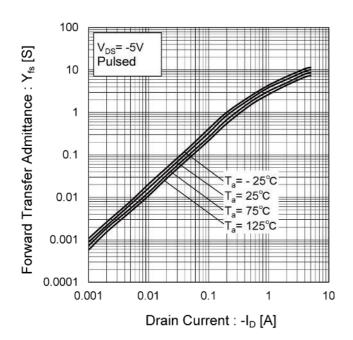


Fig.10 Forward Transfer Admittance vs. **Drain Current** 



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Fig.11 Drain Current Derating Curve

120 100 **Drain Current Dissipation** 80 : I<sub>D</sub>/I<sub>D</sub>max. [%] 60 40 20 0 -25 25 50 100 125 150 Junction Temperature : T<sub>j</sub> [°C]

Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

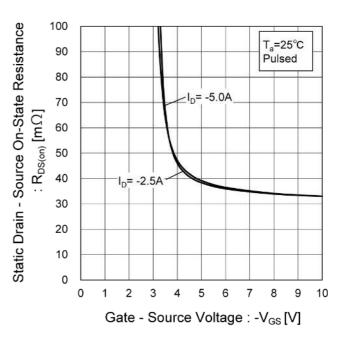


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

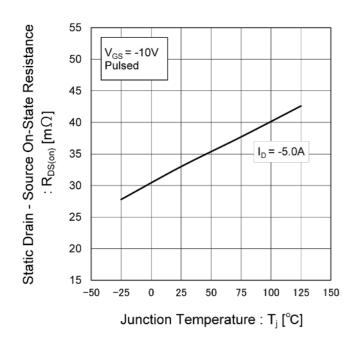


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (I)

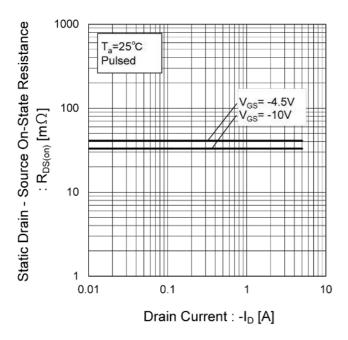


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

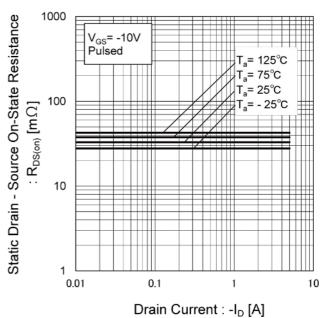


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)

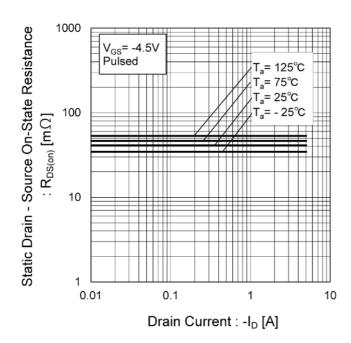


Fig.17 Typical Capacitances vs.

Drain - Source Voltage

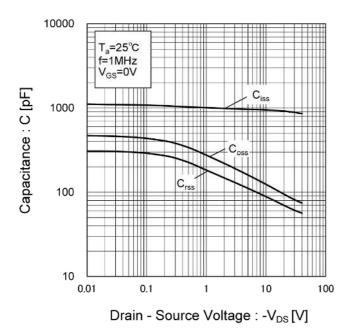


Fig.18 Switching Characteristics

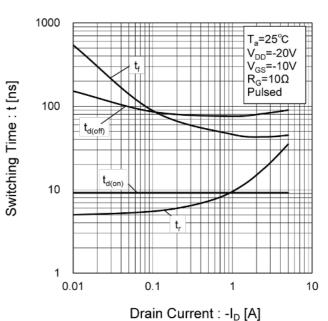


Fig.19 Typical Gate Charge

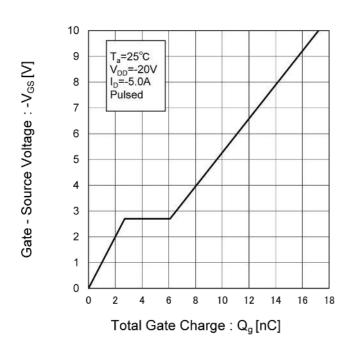
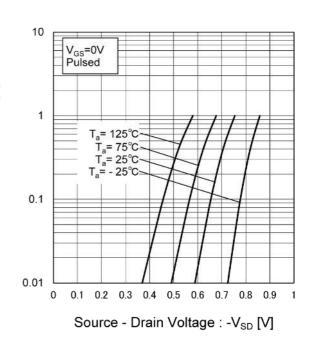


Fig.20 Source Current vs.

Source Drain Voltage



Source Current: -Is [A]

# ● Measurement circuits < Tr1>

Fig.1-1 Switching Time Measurement Circuit

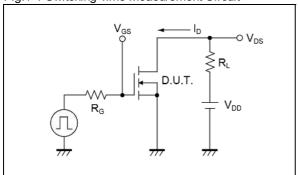


Fig.2-1 Gate Charge Measurement Circuit

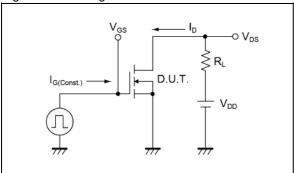


Fig.3-1 Avalanche Measurement Circuit

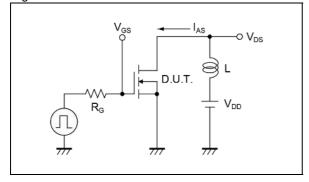


Fig.1-2 Switching Waveforms

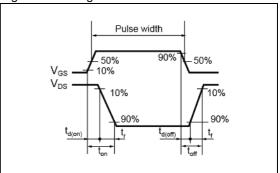


Fig.2-2 Gate Charge Waveform

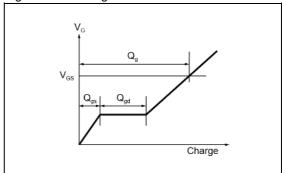
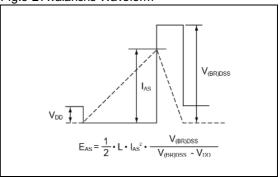


Fig.3-2 Avalanche Waveform



# ● Measurement circuits < Tr2>

Fig.4-1 Switching Time Measurement Circuit

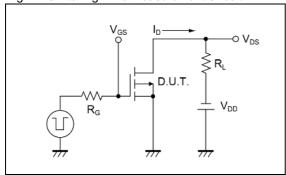


Fig.5-1 Gate Charge Measurement Circuit

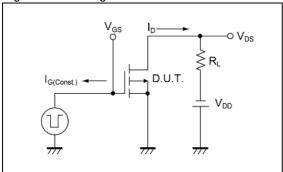


Fig.6-1 Avalanche Measurement Circuit

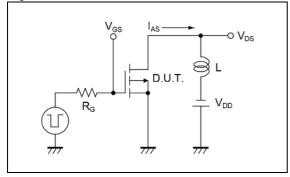


Fig.4-2 Switching Waveforms

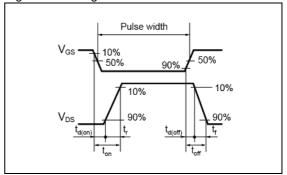


Fig.5-2 Gate Charge Waveform

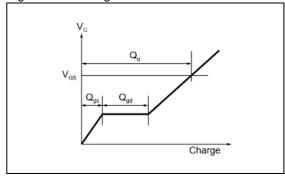
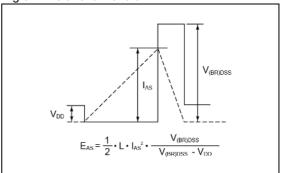


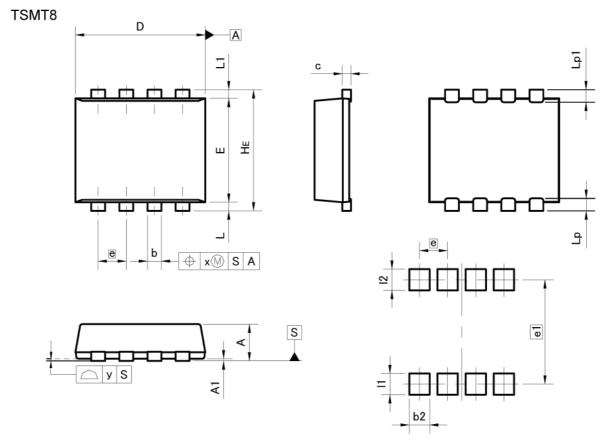
Fig.6-2 Avalanche Waveform



#### Notice

This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.

## Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIM	ETERS	INC	HES
DIM [	MIN	MAX	MIN	MAX
Α	0.75	0.85	0.030	0.033
A1	0.00	0.05	0.000	0.002
b	0.27	0.37	0.011	0.015
С	0.12	0.22	0.005	0.009
D	2.90	3.10	0.114	0.122
E	2.30	2.50	0.091	0.098
е	0.	65	0.0	26
HE	2.70	2.90	0.106	0.114
L	0.10	0.30	0.004	0.012
L1	0.10	0.30	0.004	0.012
Lp	0.19	0.39	0.007	0.015
Lp1	0.19	0.39	0.007	0.015
х	855	0.10	-	0.004
У	15.	0.10	-	0.004

DIM	MILIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
b2	100	0.47		0.019
e1	2.41		0.095	
11	33 <del>00</del>	0.49		0.019
12	-	0.49	3 <del></del> 3	0.019

Dimension in mm/inches



# **Notice**

#### **Precaution on using ROHM Products**

Our Products are designed and manufactured for application in ordinary electronic equipment (such as AV equipment, OA equipment, telecommunication equipment, home electronic appliances, amusement equipment, etc.). If you intend to use our Products in devices requiring extremely high reliability (such as medical equipment (Note 1), transport equipment, traffic equipment, aircraft/spacecraft, nuclear power controllers, fuel controllers, car equipment including car accessories, safety devices, etc.) and whose malfunction or failure may cause loss of human life, bodily injury or serious damage to property ("Specific Applications"), please consult with the ROHM sales representative in advance. Unless otherwise agreed in writing by ROHM in advance, ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of any ROHM's Products for Specific Applications.

(Note1) Medical Equipment Classification of the Specific Applications

JAPAN	USA	EU	CHINA
CLASSⅢ	CL ACCTI	CLASS II b	СГУССШ
CLASSIV	CLASSII	CLASSⅢ	CLASSⅢ

- 2. ROHM designs and manufactures its Products subject to strict quality control system. However, semiconductor products can fail or malfunction at a certain rate. Please be sure to implement, at your own responsibilities, adequate safety measures including but not limited to fail-safe design against the physical injury, damage to any property, which a failure or malfunction of our Products may cause. The following are examples of safety measures:
  - [a] Installation of protection circuits or other protective devices to improve system safety
  - [b] Installation of redundant circuits to reduce the impact of single or multiple circuit failure
- 3. Our Products are designed and manufactured for use under standard conditions and not under any special or extraordinary environments or conditions, as exemplified below. Accordingly, ROHM shall not be in any way responsible or liable for any damages, expenses or losses arising from the use of any ROHM's Products under any special or extraordinary environments or conditions. If you intend to use our Products under any special or extraordinary environments or conditions (as exemplified below), your independent verification and confirmation of product performance, reliability, etc, prior to use, must be necessary:
  - [a] Use of our Products in any types of liquid, including water, oils, chemicals, and organic solvents
  - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
  - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

#### Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

#### **Precautions Regarding Application Examples and External Circuits**

- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
- 2. You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

#### **Precaution for Electrostatic**

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

#### **Precaution for Storage / Transportation**

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
  - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
  may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
  exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

#### **Precaution for Product Label**

A two-dimensional barcode printed on ROHM Products label is for ROHM's internal use only.

#### **Precaution for Disposition**

When disposing Products please dispose them properly using an authorized industry waste company.

#### **Precaution for Foreign Exchange and Foreign Trade act**

Since concerned goods might be fallen under listed items of export control prescribed by Foreign exchange and Foreign trade act, please consult with ROHM in case of export.

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