Preferred Device

# **One Watt High Current PNP Transistor**

### Features

• Pb–Free Packages are Available\*

### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector – Emitter Voltage	V <sub>CE</sub>	50	Vdc
Collector-Base Voltage	V <sub>CB</sub>	50	Vdc
Emitter-Base Voltage	V <sub>EB</sub>	5.0	Vdc
Collector Current – Continuous	۱ <sub>C</sub>	2.0	Adc
Total Power Dissipation @ $T_A = 25^{\circ}C$ Derate above $25^{\circ}C$	P <sub>D</sub>	900 5.0	mW mW/°C
Total Power Dissipation @ $T_C = 25^{\circ}C$ Derate above $25^{\circ}C$	PD	1.5 12	W mW/°C
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	–55 to +150	°C

### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	125	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	83.3	°C/W

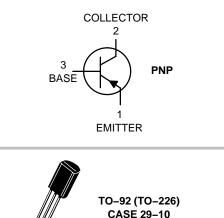
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.



### **ON Semiconductor®**

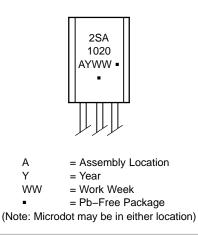
http://onsemi.com

## VOLTAGE AND CURRENT ARE NEGATIVE FOR PNP TRANSISTORS





### MARKING DIAGRAM



### **ORDERING INFORMATION**

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

Preferred devices are recommended choices for future use

and best overall value.

\*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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### ELECTRICAL CHARACTERISTICS (T<sub>C</sub> = 25°C unless otherwise noted)

Symbol	Min	Max	Unit
V <sub>(BR)CEO</sub>	50	-	Vdc
I <sub>CBO</sub>	_	1.0	μAdc
I <sub>EBO</sub>	-	1.0	μAdc
	V <sub>(BR)CEO</sub>	V <sub>(BR)CEO</sub> 50	V <sub>(BR)CEO</sub> 50 - I <sub>CBO</sub> - 1.0

### **ON CHARACTERISTICS** (Note 2)

DC Current Gain ( $I_C = 500 \text{ mA}, V_{CE} = 2.0 \text{ V}$ ) ( $I_C = 1.5 \text{ A}, V_{CE} = 2.0 \text{ V}$ )	h <sub>FE</sub>	70 40	240 -	-
Collector – Emitter Saturation Voltage $(I_C = 1.0 \text{ A}, I_B = 50 \text{ mA})$	V <sub>CE(sat)</sub>	-	0.5	Vdc
Base – Emitter Saturation Voltage ( $I_C = 1.0 \text{ A}$ , $I_B = 50 \text{ mA}$ )	V <sub>BE(sat)</sub>	-	1.2	Vdc
SMALL-SIGNAL CHARACTERISTICS				
Current-Gain - Bandwidth Product (Note 3)	f <sub>T</sub>	100	_	MHz

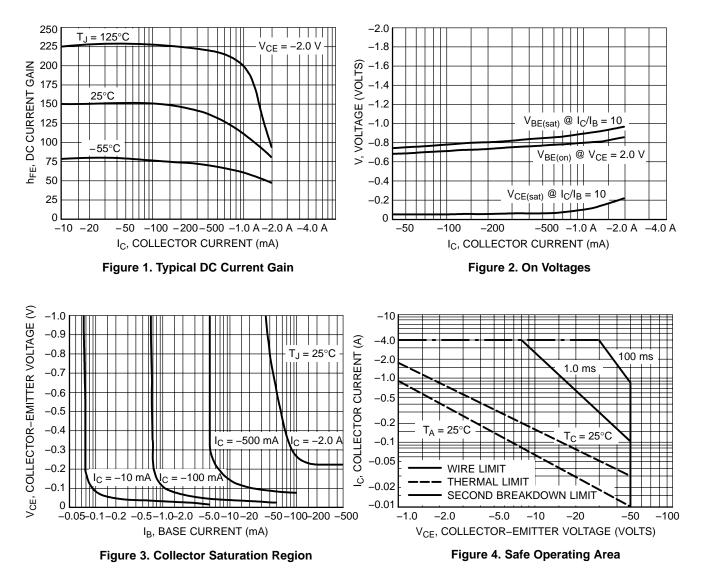
 $(I_{C} = 500 \text{ mAdc}, V_{CE} = 2.0 \text{ Vdc}, f = 100 \text{ MHz})$ 

1. Pulse Test: Pulse Width  $\leq 300 \ \mu$ s, Duty Cycle = 2.0%. 2. Pulse Test: Pulse Width  $\leq 300 \ \mu$ s, Duty Cycle = 2.0%. 3. f<sub>T</sub> is defined as the frequency at which |h<sub>fe</sub>| extrapolates to unity.

### **ORDERING INFORMATION**

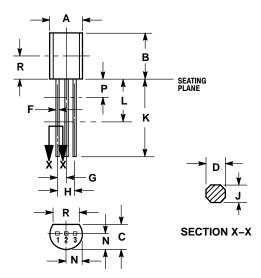
Device	Package	Shipping <sup>†</sup>
2SA1020	TO-92	
2SA1020G	TO-92 (Pb-Free)	5000 Units / Box
2SA1020RLRA	TO-92	
2SA1020RLRAG	TO-92 (Pb-Free)	2000 / Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.



#### PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-10 ISSUE AL



NOTES

- DIMENSIONING AND TOLERANCING PER ANSI 1.
- Y14.5M, 1982. CONTROLLING DIMENSION: INCH. 2
- CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED. 3.
- DIMENSION F APPLIES BETWEEN P AND L 4 DIMENSION F AFFLIES BEI WEEN F AND L. DIMENSIONS D AND J APPLY BETWEEN LAND K MIMIMUM. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.44	5.21
В	0.290	0.310	7.37	7.87
С	0.125	0.165	3.18	4.19
D	0.018	0.021	0.457	0.533
F	0.016	0.019	0.407	0.482
G	0.045	0.055	1.15	1.39
Η	0.095	0.105	2.42	2.66
L	0.018	0.024	0.46	0.61
K	0.500		12.70	
L	0.250		6.35	
Ν	0.080	0.105	2.04	2.66
Ρ		0.100		2.54
R	0.135		3.43	
STYLE 14:				

PIN 1. EMITTER 2. 3. COLLECTOR

BASE

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