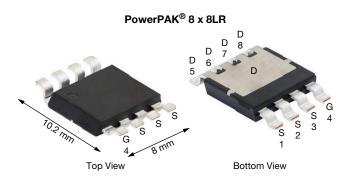
SQJQ144AER

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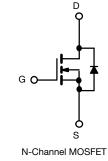
Automotive N-Channel 40 V (D-S) 175 °C MOSFET



PRODUCT SUMMARY				
V _{DS} (V)	40			
$R_{DS(on)} (\Omega)$ at $V_{GS} = 10 V$	0.0009			
I _D (A)	575			
Configuration	Single			

FEATURES

- TrenchFET[®] Gen IV power MOSFET
- AEC-Q101 qualified
- 100 % R_q and UIS tested
- Thin 1.6 mm package
- Very low thermal resistance
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912



ORDERING INFORMATION	
Package	PowerPAK 8 x 8LR
Lead (Pb)-free and halogen-free	SQJQ144AER (for detailed order number please see www.vishay.com/doc?79776)

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unles	s otherwise noted)		
PARAMETER	SYMBOL	LIMIT	UNIT		
Drain-source voltage		V _{DS}	40		
Gate-source voltage		V _{GS}	± 20	V	
Continuous drain current	T _C = 25 °C	– I _D –	575		
	T _C = 125 °C		330		
Continuous source current (diode conduction)		۱ _S	545	А	
Pulsed drain current ^a		I _{DM}	1800		
Single pulse avalanche current	L = 0.1 mH	I _{AS}	60		
Single pulse avalanche energy	L = 0.1 MH	E _{AS}	180	mJ	
Maximum power dissipation	T _C = 25 °C	- P _D	600	W	
	T _C = 125 °C		200		
Operating junction and storage temperature range		T _J , T _{stg}	-55 to +175	°C	
Soldering recommendations (peak temperature) ^c			260	C	

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction-to-ambient	PCB mount ^b	R _{thJA}	44	°C/W	
Junction-to-case (drain)		R _{thJC}	0.25	0/10	

Notes

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %

b. When mounted on 1" square PCB (FR4 material)

c. See solder profile (<u>www.vishay.com/doc?73257</u>). The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection

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PARAMETER	SYMBOL	L TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT	
Static		•						
Drain-source breakdown voltage	V _{DS}	$V_{GS} = 0, I_D = 250 \ \mu A$		40	-	-	- V	
Gate-source threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$		2	3	3.5		
Gate-source leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$		-	-	± 100	nA	
Zero gate voltage drain current		$V_{GS} = 0 V$	V _{DS} = 40 V	-	-	1		
	I _{DSS}	$V_{GS} = 0 V$	V _{DS} = 40 V, T _J = 125 °C	-	-	50	μA	
		$V_{GS} = 0 V$	V _{DS} = 40 V, T _J = 175 °C	-	-	150		
On-state drain current ^a	I _{D(on)}	$V_{GS} = 10 V$	$V_{DS} \ge 5 V$	100	-	-	Α	
Drain-source on-state resistance ^a		$V_{GS} = 10 V$	I _D = 20 A	-	0.0007	0.0009		
	R _{DS(on)}	$V_{GS} = 10 V$	I _D = 20 A, T _J = 125 °C	-	-	0.0015	Ω	
		V _{GS} = 10 V	I _D = 20 A, T _J = 175 °C	-	-	0.0019		
Forward transconductance b	g _{fs}	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 60 \text{ A}$		-	160	-	S	
Dynamic ^b								
Input capacitance	C _{iss}		V V _{DS} = 25 V, f = 1 MHz	-	7220	9020	pF	
Output capacitance	C _{oss}	V _{GS} = 0 V		-	2290	2860		
Reverse transfer capacitance	C _{rss}			-	175	220		
Total gate charge ^c	Qg		V _{DS} = 20 V, I _D = 30 A	-	116	145	nC	
Gate-source charge c	Q _{gs}	V _{GS} = 10 V		-	36	-		
Gate-drain charge ^c	Q _{gd}			-	25	-	-	
Gate resistance	Rg	f = 1 MHz		0.9	1.6	2.6	Ω	
Turn-on delay time ^c	t _{d(on)}	V_{DD} = 20 V, R _L = 0.66 Ω I _D \cong 30 A, V _{GEN} = 10 V, R _g = 1 Ω		-	17	27		
Rise time ^c	tr			-	27	41	- ns	
Turn-off delay time ^c	t _{d(off)}			-	41	62		
Fall time ^c	t _f			-	18	27		
Source-Drain Diode Ratings and Ch	aracteristics ^b	•						
Reverse recovery time	t _{rr}	V _{DD} = 32 V, I _{FM} = 15 A, di/dt = 100 A/μs		-	66	-	ns	
Reverse recovery charge	Q _{rr}			-	94	-	nC	
Reverse recovery current	I _{RM}			-	-	-3.6	Α	
Pulsed current ^a	I _{SM}			-	-	1600	Α	
Forward voltage	V _{SD}	$I_{\rm F} = 50 \text{ A}, V_{\rm GS} = 0$		-	0.8	1.1	V	

Notes

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %

b. Guaranteed by design, not subject to production testing

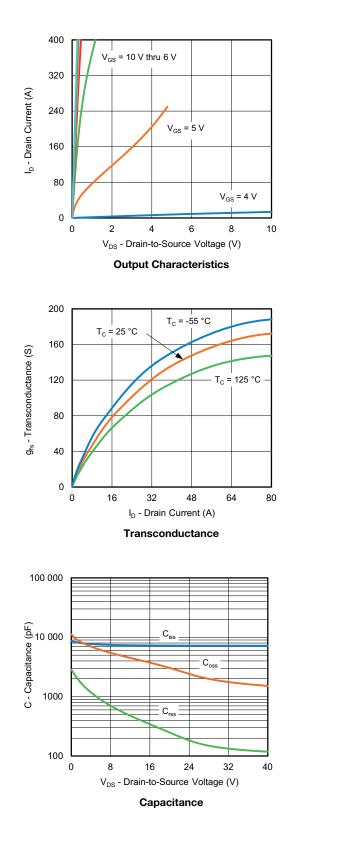
c. Independent of operating temperature

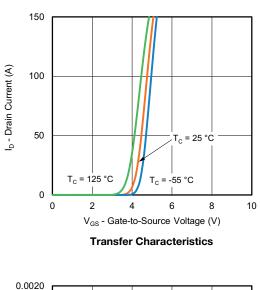
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

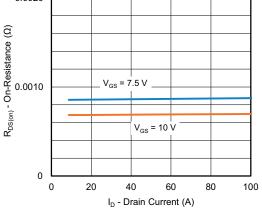


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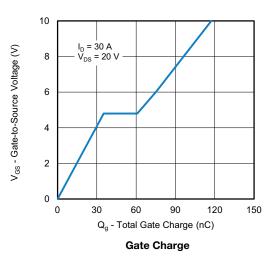
TYPICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$, unless otherwise noted)







On-Resistance vs. Drain Current



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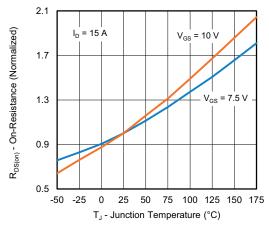
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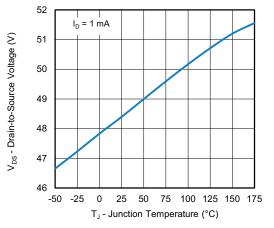
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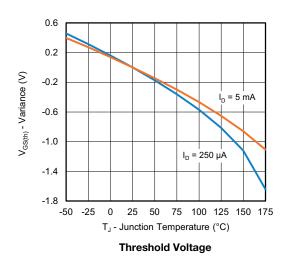
TYPICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$, unless otherwise noted)

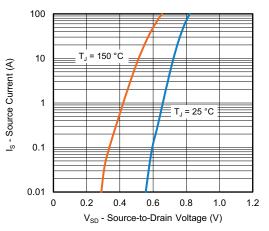


On-Resistance vs. Junction Temperature

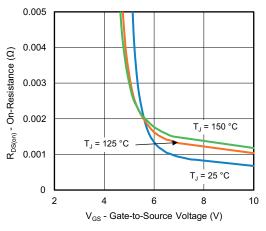


Drain Source Breakdown vs. Junction Temperature

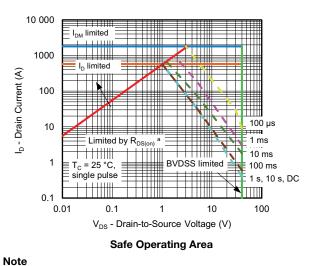




Source Drain Diode Forward Voltage



On-Resistance vs. Gate-to-Source Voltage



a. V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

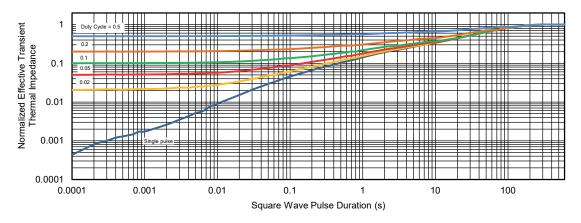
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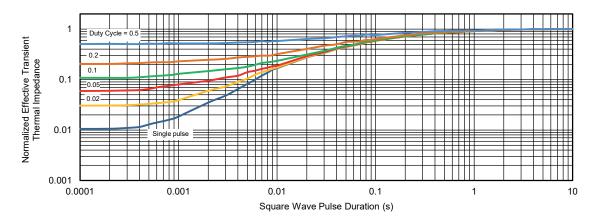


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THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

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