



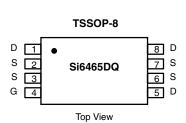
# P-Channel 1.8-V (G-S) MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	$R_{DS(on)}\left(\Omega\right)$	I <sub>D</sub> (A)		
	0.012 at V <sub>GS</sub> = - 4.5 V	± 8.8		
- 8	0.017 at V <sub>GS</sub> = - 2.5 V	± 7.4		
	0.025 at V <sub>GS</sub> = - 1.8 V	± 6.0		

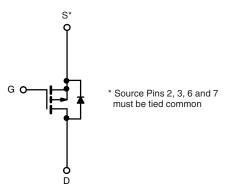
#### **FEATURES**

- · Halogen-free
- TrenchFET® Power MOSFETs: 1.8 V Rated





Ordering Information: Si6465DQ-T1-GE3 (Lead (Pb)-free and Halogen-free)



P-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b>	T <sub>A</sub> = 25 °C, unles	ss otherwise no	oted	
Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V <sub>DS</sub>	- 8	.,
Gate-Source Voltage		V <sub>GS</sub>	± 8	V
Continuous Dunin Courant /T 450 00/8 h	T <sub>A</sub> = 25 °C	1	± 8.8	
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a, b</sup>	T <sub>A</sub> = 70 °C	'D	± 7.1	
Pulsed Drain Current		I <sub>DM</sub>	± 30	Α
Continuous Source Current (Diode Conduction) <sup>a, b</sup>		I <sub>S</sub>	- 1.5	
	T <sub>A</sub> = 25 °C	В	1.5	14/
Maximum Power Dissipation <sup>a, b</sup>	T <sub>A</sub> = 70 °C	P <sub>D</sub>	1.0	w
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stq</sub>	- 55 to 150	°C

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Mariana la Ambienta	t ≤ 10 s	$R_{thJA}$		83	°C/W	
Maximum Junction-to-Ambient <sup>a</sup>	Steady State	' 'thJA	90			

#### Notes:

a. Surface Mounted on FR4 board.

 $b. \ t \leq 10 \ s.$ 

## **Si6465DQ**

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static			1				
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = -250 \mu A$	- 0.45			V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = - 6.4 V, V <sub>GS</sub> = 0 V		-1			
		V <sub>DS</sub> = - 6.4 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 70 °C			- 25	μΑ	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	- 20			Α	
Drain-Source On-State Resistance <sup>a</sup>		V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 8.8 A		0.009	0.012	Ω	
	R <sub>DS(on)</sub>	$V_{GS} = -2.5 \text{ V}, I_D = -7.4 \text{ A}$		0.0125	0.017		
		$V_{GS} = -1.8 \text{ V}, I_D = -6.0 \text{ A}$		0.0185	0.025		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 5 V, I <sub>D</sub> = - 8.8 A		34		S	
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	I <sub>S</sub> = - 1.5 A, V <sub>GS</sub> = 0 V		- 0.65	- 1.1	V	
Dynamic <sup>b</sup>							
Total Gate Charge	Qg			50	80		
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = -6 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -8.8 \text{ A}$		10		nC	
Gate-Drain Charge	$Q_{gd}$			8		1	
Turn-On Delay Time	t <sub>d(on)</sub>			30	60		
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 6 V, $R_L$ = 6 $\Omega$		60	100	ns	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D\cong$ - 1 A, $V_{GEN}$ = - 4.5 V, $R_G$ = 6 $\Omega$		210	400		
Fall Time	t <sub>f</sub>			130	250		
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = - 1.5 A, di/dt = 100 A/μs		70	120		

#### Notes:

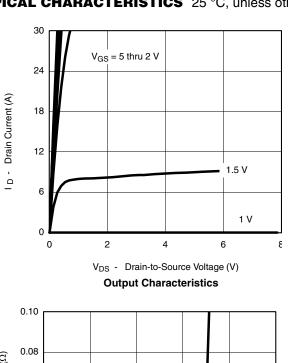
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

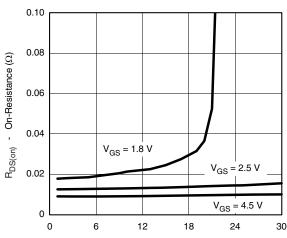
a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.

b. Guaranteed by design, not subject to production testing.

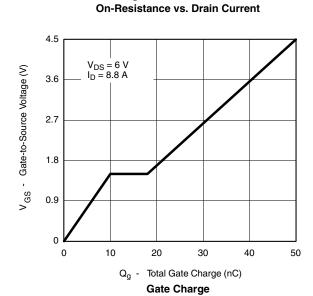


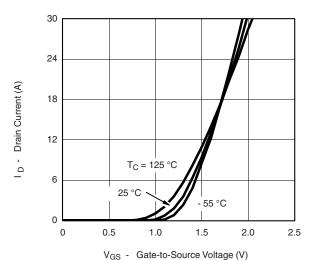
#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

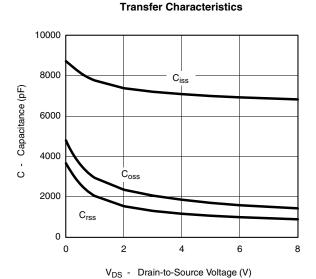


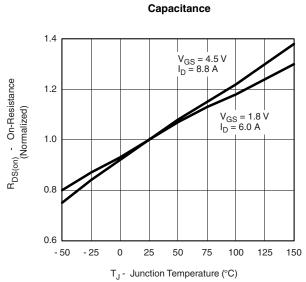


I<sub>D</sub> - Drain Current (A)







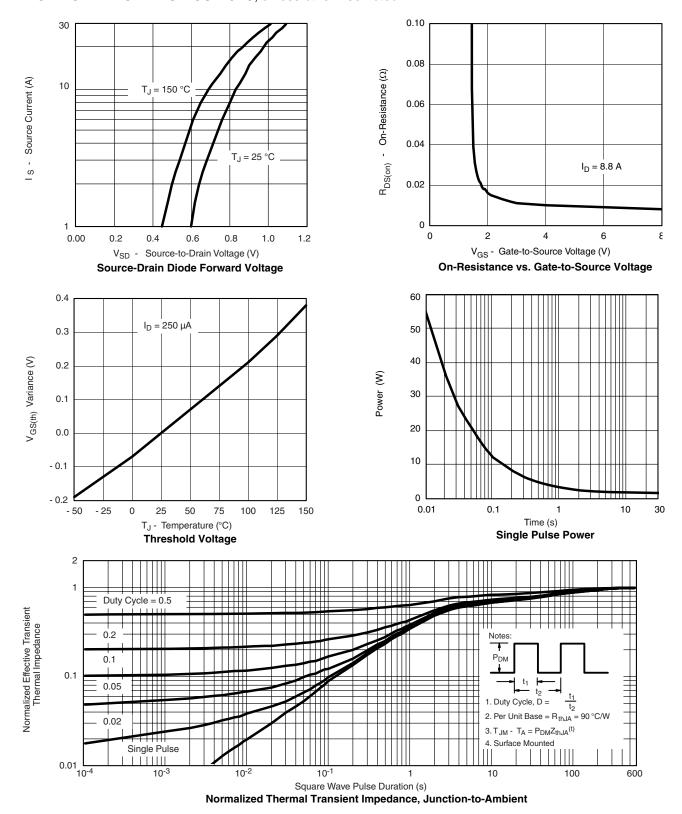


On-Resistance vs. Junction Temperature

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## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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