



Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
	0.99Ω @ V _{GS} = 4.5V	0.78A
20V	1.2Ω @ V _{GS} = 2.5V	0.71A
	1.8Ω @ V _{GS} = 1.8V	0.58A

Description and Applications

This MOSFET is designed to minimize the on-state resistance $(R_{DS(ON)})$ and yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

- Battery Charging
- Power Management Functions
- DC-DC Converters

20V N-CHANNEL ENHANCEMENT MODE MOSFET

Features and Benefits

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Ultra-Small Surface Mount Package
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: X1-DFN1006-3
- Case Material: Molded Plastic, "Green" Molding Compound; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish NiPdAu over Copper Leadframe; Solderable per MIL-STD-202, Method 208 (e4)
- Weight: 0.001 grams (Approximate)



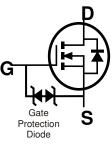


X1-DFN1006-3

Bottom View

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D	;_ <u>S</u> _
	G .

Top View Package Pin Configuration



Equivalent Circuit

Ordering Information (Note 4)

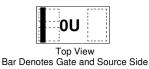
	Part Number	Case	Packaging	
	DMN2990UFB-7B	X1-DFN1006-3	10,000/Tape & Reel	
Notes:	Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.			

No purposely added lead. Fully ED Enective 2002/30/ED (Horis), 20 Hos/ED (Horis 2) & 20 hos/ED (Horis 3) compliant.
 See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



0U = Part Marking Code



Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	20	V
Gate-Source Voltage			V _{GSS}	±8	V
Continuous Drain Current (Note 6) V_{GS} = 4.5V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	ID	0.78 0.62	А
Maximum Continuous Body Diode Forward Current (Note 6)			Is	0.72	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I _{DM}	1.5	А

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		PD	0.52	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	R _{0JA}	239	°C/W
Total Power Dissipation (Note 6)		PD	0.92	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R _{0JA}	137	°C/W
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Tum	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)	Symbol	MIN	Тур	Max	Unit	Test Condition	
· · · · ·	51	00					
Drain-Source Breakdown Voltage	BV _{DSS}	20	_	—	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current ($T_J = +25^{\circ}C$)	I _{DSS}	_	_	100	nA	$V_{DS} = 16V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}			±1	μA	$V_{GS} = \pm 5V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(TH)}	0.4	0.74	1.0	V	$V_{DS} = V_{GS}, I_D = 250A$	
			496	990		$V_{GS} = 4.5V, I_{D} = 100mA$	
Static Drain-Source On-Resistance	R _{DS(ON)}	_	606	1200	mΩ	$V_{GS} = 2.5V, I_D = 50mA$	
			761	1800		$V_{GS} = 1.8V, I_D = 20mA$	
Diode Forward Voltage	V _{SD}	—	0.8	1.0	V	$V_{GS} = 0V, I_{S} = 150mA$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss		31		pF		
Output Capacitance	Coss	_	3.6	_	pF	V _{DS} = 15V, V _{GS} = 0V, f = 1.0MHz	
Reverse Transfer Capacitance	C _{rss}	_	2.5	_	pF		
Gate Resistance	Rg	_	187	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	
Total Gate Charge	Qg	_	0.41	_	nC		
Gate-Source Charge	Q _{gs}	_	0.06	_	nC	$V_{GS} = 4.5V, V_{DS} = 10V,$	
Gate-Drain Charge	Q _{gd}	_	0.05	_	nC	$I_D = 250 \text{mA}$	
Turn-On Delay Time	t _{D(ON)}	_	4.5		ns		
Turn-On Rise Time	t _R	_	3.5	_	ns		
Turn-Off Delay Time	t _{D(OFF)}	_	24	_	ns	$R_{G} = 2\Omega, I_{D} = 200 \text{mA}$	
Turn-Off Fall Time	t _F	_	12	_	ns		
Reverse Recovery Time	t _{RR}	_	7.1	—	ns		
Reverse Recovery Charge	Q _{RR}	_	1.2	_	nC	I _F = 200mA, di/dt = 100A/μs	

Notes:

5. Device mounted on FR-4 PCB, with minimum recommended pad layout.
6. Device mounted on minimum recommended pad layout test board, 10µs pulse duty cycle = 1%.
7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to product testing.



DMN2990UFB

, = 150°C

= 125°C

T_I = 85°C

2.5

3

= 25°C

-55°C

2

5

6

= 20mA

 $V_{GS} = 1.2V, I_{D} = 1mA$

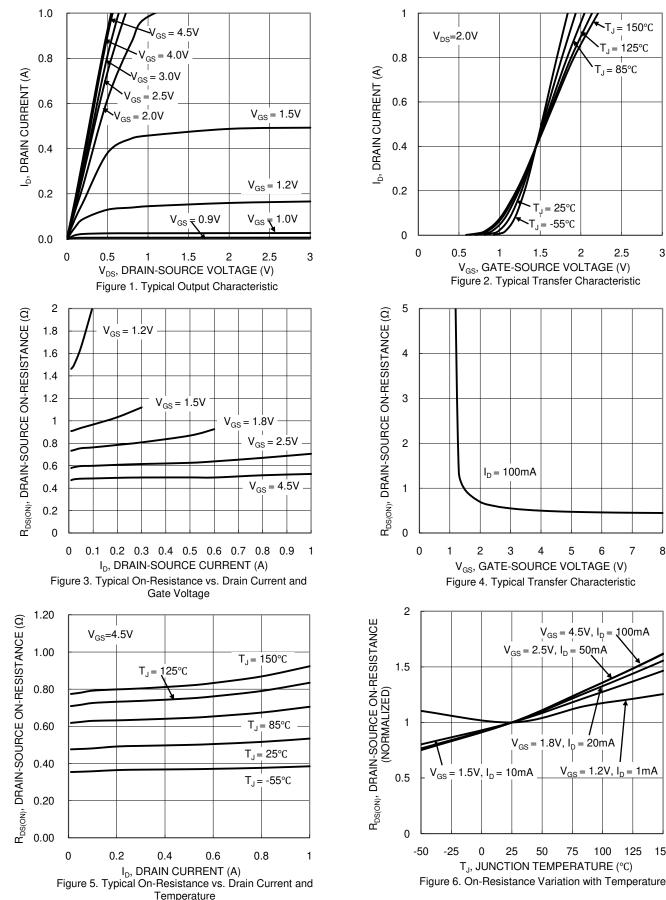
100

125

75

7

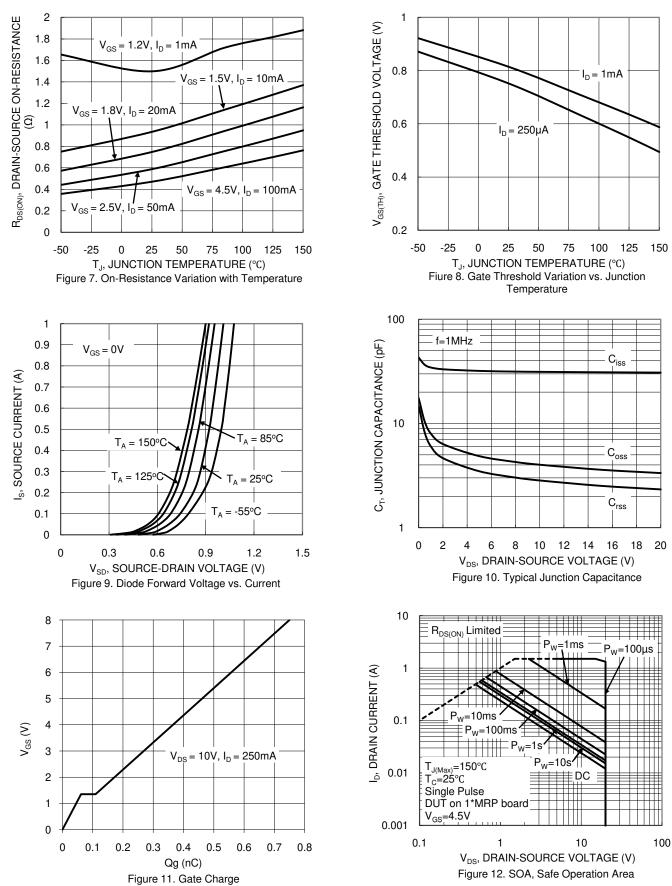
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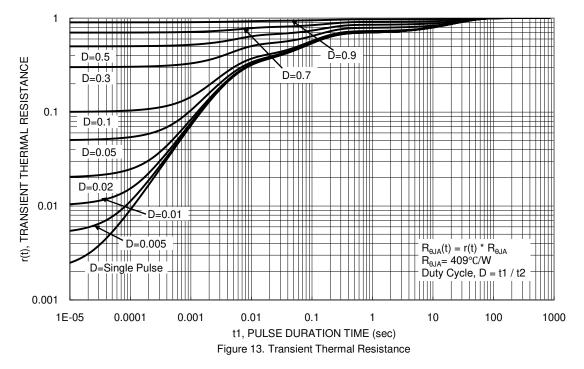
DMN2990UFB Document number: DS39019 Rev. 3 - 2 150







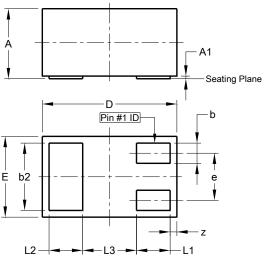






Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.



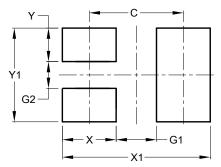
X1-DFN1006-3						
Dim	Min	Max	Тур			
Α	0.47	0.53	0.50			
A1	0.00	0.05	0.03			
b	0.10	0.20	0.15			
b2	0.45	0.55	0.50			
D	0.95	1.075	1.00			
Е	0.55	0.675	0.60			
е	-	-	0.35			
L1	0.20	0.30	0.25			
L2	0.20	0.30	0.25			
L3	-	-	0.40			
Z	0.02	0.08	0.05			
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



X1-DFN1006-3



Dimensions	Value (in mm)
С	0.70
G1	0.30
G2	0.20
Х	0.40
X1	1.10
Y	0.25
Y1	0.70



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