

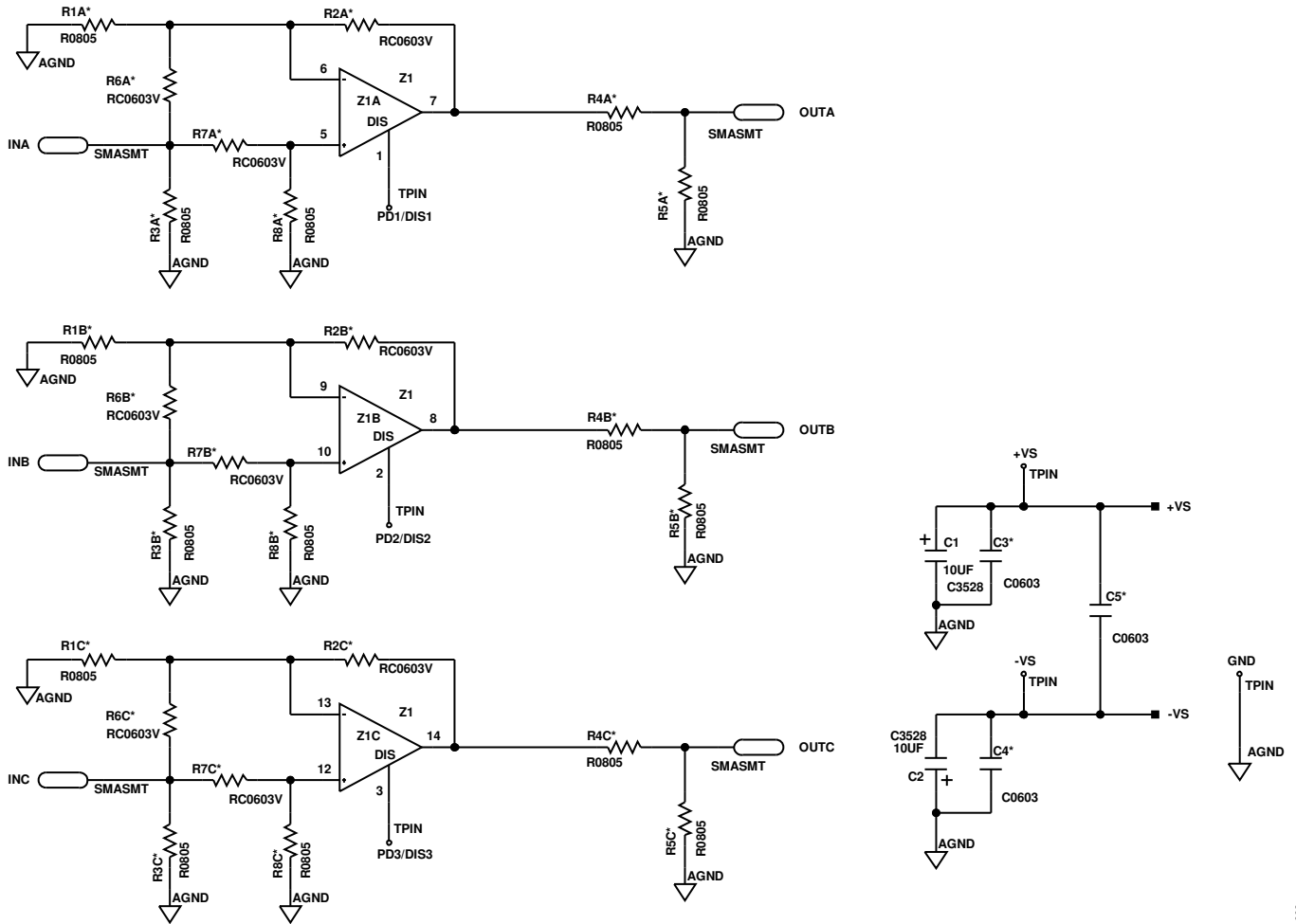
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REVISION HISTORY

4/10—Revision 0: Initial Version

EVALUATION BOARD SCHEMATIC, ASSEMBLY DRAWINGS, AND LAYOUT PATTERNS



* USER-DEFINED VALUE.

Figure 3. Universal Evaluation Board Schematic

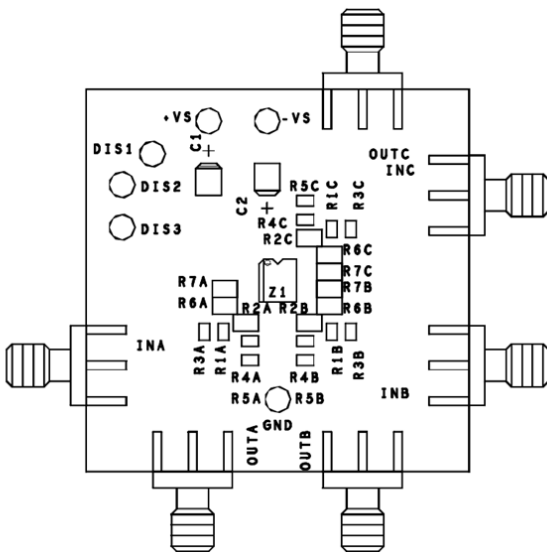


Figure 4. Component Side Assembly Drawing

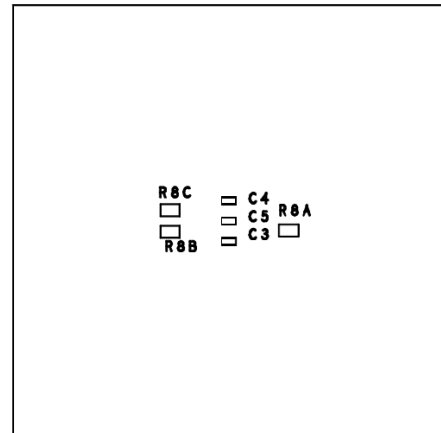


Figure 5. Circuit Side Assembly Drawing

08152-004

08152-005

08152-003

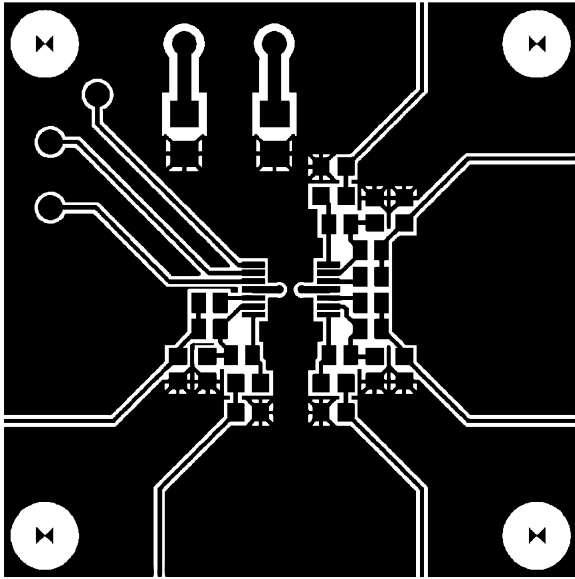


Figure 6. Component Side Layout Pattern

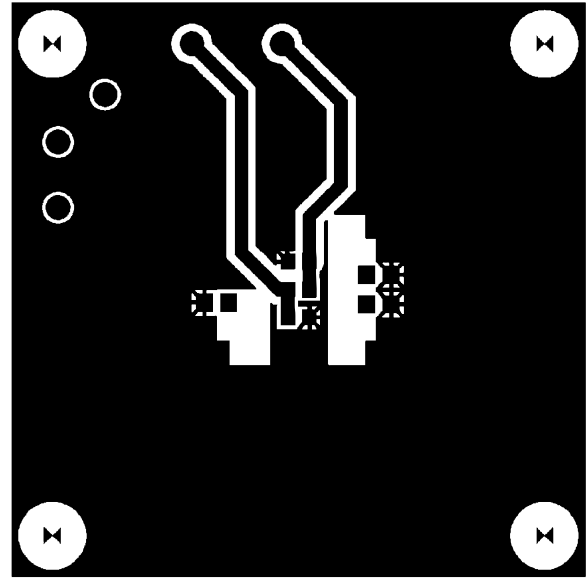


Figure 7. Circuit Side Layout Pattern

ORDERING INFORMATION**BILL OF MATERIALS**

Table 1.

Quantity	Reference Designator	Description	Package
6	+VS, -VS, PD1/DIS1, PD2/DIS2, PD3/DIS3, GND	Connectors	TP1
2	C1, C2	10 μ F	3528
3	C3, C4, C5	Capacitor, user defined	C0603
6	INA, INB, INC, OUTA, OUTB, OUTC	SMA SMT	SMA SMT
15	R1A, R1B, R1C, R3A, R3B, R3C, R4A, R4B, R4C, R5A, R5B, R5C, R8A, R8B, R8C	Resistor, user defined	R0805
9	R2A, R2B, R2C, R6A, R6B, R6C, R7A, R7B, R7C	Resistor, user defined	R0603
1	DUT	Amplifier	14-lead TSSOP

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**ESD Caution**

ESD (electrostatic discharge) sensitive device. Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection circuitry, damage may occur on devices subjected to high energy ESD. Therefore, proper ESD precautions should be taken to avoid performance degradation or loss of functionality.

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