RGW60TS65HR

650V 30A Field Stop Trench IGBT

Datasheet

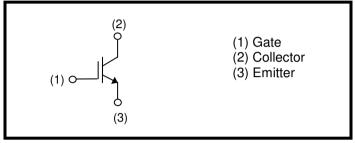
V _{CES}	650V
I _{C (100°C)}	30A
V _{CE(sat) (Typ.)}	1.5V
P_{D}	178W

Outline TO-247N (1) (2)(3)

Features

- 1) AEC-Q101 Qualified
- 2) Low Collector Emitter Saturation Voltage
- 3) Low Switching Loss & Soft Switching
- 4) Pb free Lead Plating; RoHS Compliant

●Inner Circuit



Application

Automotive

On & Off Board Chargers

DC-DC Converters

PFC

Industrial Inverter

Packaging Specifications

• Packaging Specifications					
	Packaging	Tube			
	Reel Size (mm)	-			
Type	Tape Width (mm)	-			
Туре	Basic Ordering Unit (pcs)	450			
	Packing Code	C11			
	Marking	RGW60TS65			

● **Absolute Maximum Ratings** (at T_C = 25°C unless otherwise specified)

Parameter		Symbol	Value	Unit
Collector - Emitter Voltage		V _{CES}	650	V
Gate - Emitter Voltage		V _{GES}	±30	V
Collector Current	T _C = 25°C	I _C	64	Α
	$T_C = 100$ °C	I _C	39	Α
Pulsed Collector Current	Pulsed Collector Current		120	Α
Power Dissipation	T _C = 25°C	P _D	178	W
	$T_C = 100$ °C	P _D	89	W
Operating Junction Temperature		T _j	-40 to +175	°C
Storage Temperature		T _{stg}	-55 to +175	°C

^{*1} Pulse width limited by T_{imax.}

●Thermal Resistance

Parameter	Symbol	Values			Unit
Farameter	Syllibol	Min.	Тур.	Max.	Offic
Thermal Resistance IGBT Junction - Case	$R_{\theta(j-c)}$	-	ı	0.84	°C/W

●IGBT Electrical Characteristics (at T_j = 25°C unless otherwise specified)

Parameter	Cymbol	Conditions	Values			Unit
- Farameter	Parameter Symbol Conditions -		Min.	Тур.	Max.	Utill
Collector - Emitter Breakdown Voltage	BV _{CES}	$I_{C} = 10 \mu A, V_{GE} = 0 V$	650	ı	ı	V
Collector Cut - off Current	I _{CES}	$V_{CE} = 650V, V_{GE} = 0V$	1	ı	10	μΑ
Gate - Emitter Leakage Current	I _{GES}	$V_{GE} = \pm 30V$, $V_{CE} = 0V$	1	1	±200	nA
Gate - Emitter Threshold Voltage	$V_{\text{GE(th)}}$	$V_{CE} = 5V, I_{C} = 20.0 \text{mA}$	5.0	6.0	7.0	٧
Collector - Emitter Saturation Voltage	V _{CE(sat)}	$I_{C} = 30A, V_{GE} = 15V,$ $T_{j} = 25^{\circ}C$ $T_{j} = 175^{\circ}C$	-	1.5 1.85	1.9 -	V

ullet IGBT Electrical Characteristics (at $T_j = 25$ °C unless otherwise specified)

Davamatav	Symbol	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Input Capacitance	C _{ies}	$V_{CE} = 30V$,	-	2530	-	
Output Capacitance	C _{oes}	$V_{GE} = 0V$,	-	65	-	рF
Reverse transfer Capacitance	C_{res}	f = 1MHz	-	46	-	
Total Gate Charge	Q_g	$V_{CE} = 400V$,	-	84	-	
Gate - Emitter Charge	Q_{ge}	$I_{\rm C} = 30A$,	-	17	-	nC
Gate - Collector Charge	Q_{gc}	$V_{GE} = 15V$	-	31	-	
Turn - on Delay Time	t _{d(on)}		-	36	-	
Rise Time	t _r	$I_C = 15A, V_{CC} = 400V,$ $V_{GF} = 15V, R_G = 10\Omega,$	-	9	-	ns
Turn - off Delay Time	t _{d(off)}	$T_i = 25^{\circ}C$	-	107	-	
Fall Time	t _f	Inductive Load	-	55	-	
Turn - on Switching Loss	E _{on}	*E _{on} include diode reverse recovery	-	0.16	-	
Turn - off Switching Loss	E _{off}	,	-	0.24	-	mJ
Turn - on Delay Time	t _{d(on)}		-	34	-	
Rise Time	t _r	$I_{C} = 15A, V_{CC} = 400V,$ $V_{GF} = 15V, R_{G} = 10\Omega,$	-	10	-	no
Turn - off Delay Time	$t_{d(off)}$	V _{GE} = 15V, h _G - 10Ω, T _j = 175°C Inductive Load *E _{on} include diode reverse recovery	-	139	-	ns
Fall Time	t _f		-	76	-	
Turn - on Switching Loss	E _{on}		-	0.17	-	mJ
Turn - off Switching Loss	E _{off}		-	0.33	-	IIIJ
Reverse Bias Safe Operating Area	RBSOA	$I_C = 120A$, $V_{CC} = 520V$, $V_P = 650V$, $V_{GE} = 15V$, $R_G = 100\Omega$, $T_j = 175^{\circ}C$	FU	LL SQUA	RE	-

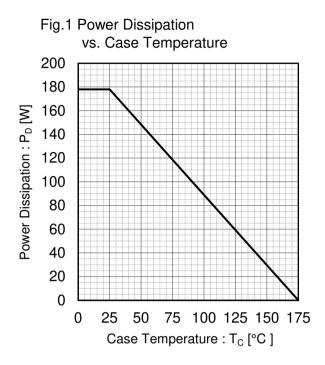
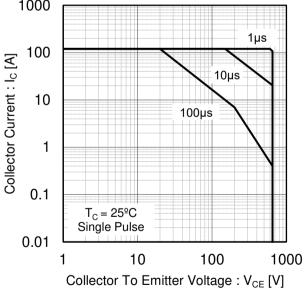


Fig.2 Collector Current vs. Case Temperature 70 60 Collector Current : Ic [A] 50 40 30 20 10 T_j ≤ 175°C V_{GE} ≥ 15V 0 25 50 75 100 125 150 175 Case Temperature: T_C [°C]

Fig.3 Forward Bias Safe Operating Area

1000



160 140 Collector Current : Ic [A] 120 100 80 60 40 20 $T_i \le 175^{\circ}C$ V_{GF} = 15V 0 200 400 600 800 Collector To Emitter Voltage: V_{CE} [V]

Fig.4 Reverse Bias Safe Operating Area

Fig.5 Typical Output Characteristics

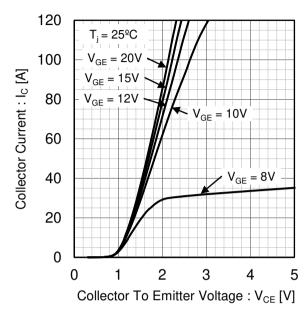


Fig.6 Typical Output Characteristics

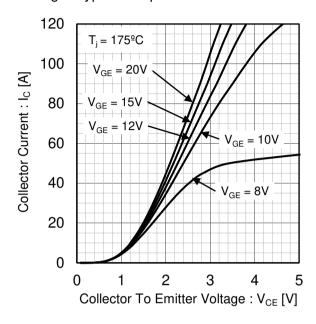


Fig.7 Typical Transfer Characteristics

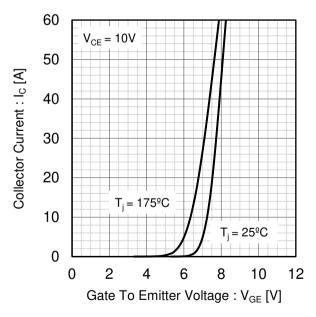
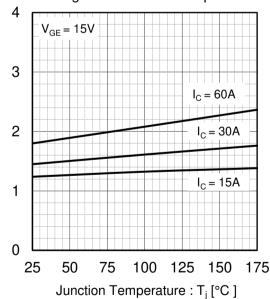


Fig.8 Typical Collector to Emitter Saturation Voltage vs. Junction Temperature



Collector To Emitter Saturation

Voltage: V_{CE(sat)} [V]

Fig.9 Typical Collector to Emitter Saturation Voltage vs. Gate to Emitter Voltage 20 T_i = 25^oC Collector To Emitter Saturation $I_C = 60A$ 15 Voltage: V_{CE(sat)} [V] $I_C = 30A$ $I_C = 15A$ 10 5 0 5 10 15 20 Gate To Emitter Voltage: VGE [V]

Fig.10 Typical Collector to Emitter Saturation Voltage vs. Gate to Emitter Voltage

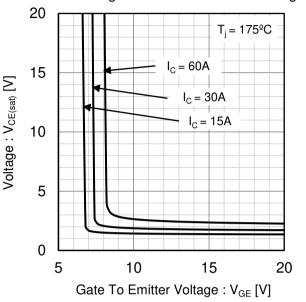
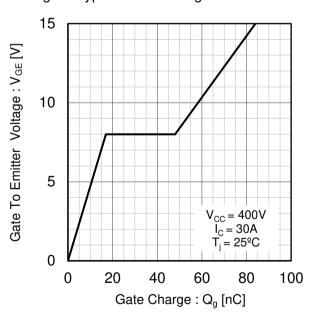


Fig.11 Typical Capacitance vs. Collector to Emitter Voltage 10000 C_{ies} 1000 Capacitance [pF] Coes 100 10 C_{res} f = 1MHz $V_{GE} = 0V$ = 25ºC 1 0.01 0.1 1 10 100 Collector To Emitter Voltage: V_{CE} [V]

Fig.12 Typical Gate Charge



Collector To Emitter Saturation

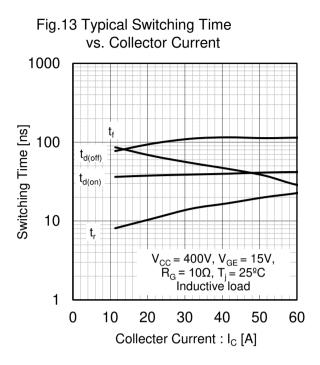


Fig.14 Typical Switching Time vs. Gate Resistance 1000 $t_{\text{d}(\text{off})}$ Switching Time [ns] 100 10 $V_{CC} = 400 \text{ V}, V_{GE} = 15 \text{ V},$ $I_{C} = 15 \text{ A}, T_{j} = 25^{\circ}\text{ C}$ Inductive load 1 0 10 20 30 50 Gate Resistance : R_g [Ω]

Fig.15 Typical Switching Energy Losses vs. Collector Current 10 Switching Energy Losses [mJ] 1 0.1 V_{CC} = 400V, V_{GE} = 15V, R_G = 10 Ω , T_j = 25 $^{\circ}$ C Inductive load 0.01 0 10 20 30 40 50 60 Collecter Current : I_C [A]

vs. Gate Resistance

10

See Story

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Eoff

V_{CC} = 400V, V_{GE} = 15V,
I_C = 15A, T_j = 25°C
Inductive load

0 10 20 30 40 50

Gate Resistance : $R_G[\Omega]$

Fig.16 Typical Switching Energy Losses

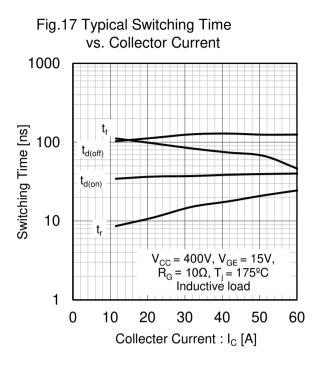


Fig.18 Typical Switching Time vs. Gate Resistance 1000 $t_{d(off)}$ Switching Time [ns] 100 $t_{d(on)}$ 10 $V_{CC} = 400 \text{V}, V_{GE} = 15 \text{V}, \\ I_{C} = 15 \text{A}, T_{j} = 175 ^{\circ} \text{C} \\ \text{Inductive load}$ 1 0 10 20 30 50 Gate Resistance : R_g [Ω]

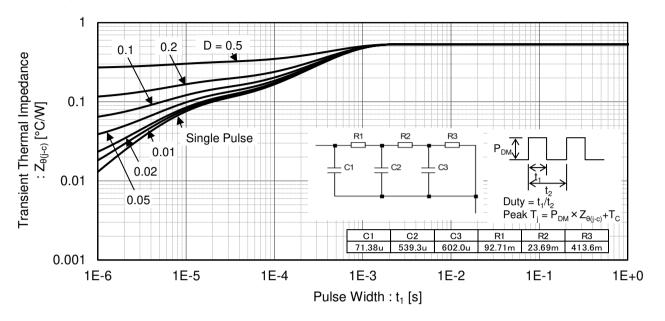
Fig.19 Typical Switching Energy Losses vs. Collector Current 10 Switching Energy Losses [mJ] 1 $\mathsf{E}_{\mathsf{off}}$ 0.1 Eon $V_{CC} = 400V, V_{GE} = 15V,$ $R_G = 10\Omega, T_j = 175^{\circ}C$ Inductive load 0.01 0 10 20 30 40 50 60 Collecter Current : I_C [A]

vs. Gate Resistance 10

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Fig.20 Typical Switching Energy Losses

Fig.21 Typical IGBT Transient Thermal Impedance



●Inductive Load Switching Circuit and Waveform

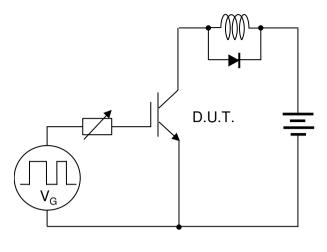


Fig.22 Inductive Load Circuit

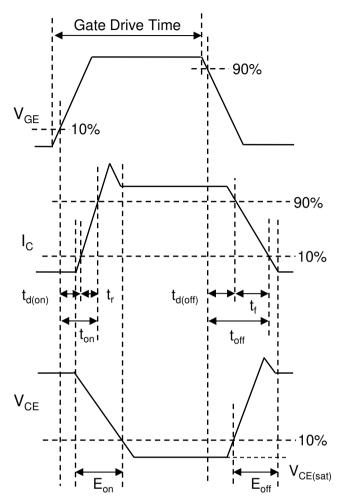


Fig.23 Inductive Load Waveform

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