MTP20N15E

Power MOSFET 20 Amps, 150 Volts

N-Channel TO-220

This Power MOSFET is designed to withstand high energy in the avalanche and commutation modes. The energy efficient design also offers a drain-to-source diode with a fast recovery time. Designed for low voltage, high speed switching applications in power converters and PWM motor controls, these devices are particularly well suited for bridge circuits where diode speed and commutating safe operating areas are critical and offer additional safety margin against unexpected voltage transients.

- Avalanche Energy Specified
- Source-to-Drain Diode Recovery Time Comparable to a Discrete Fast Recovery Diode
- Diode is Characterized for Use in Bridge Circuits
- I_{DSS} and V_{DS(on)} Specified at Elevated Temperature
- This is a Pb-Free Device*

MAXIMUM RATINGS (T_C = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
Drain-Source Voltage	V_{DSS}	150	Vdc
Drain-Gate Voltage ($R_{GS} = 1.0 \text{ M}\Omega$)	V_{DGR}	150	Vdc
Gate–Source Voltage - Continuous - Non–Repetitive (t _p ≤ 10 ms)	V _{GS} V _{GSM}	± 20 ± 32	Vdc
Drain – Continuous – Continuous @ 100°C – Single Pulse (t _p ≤ 10 μs)	I _D I _D I _{DM}	20 12 60	Adc
Total Power Dissipation Derate above 25°C	P _D	112 0.9	Watts W/°C
Operating and Storage Temperature Range	T _J , T _{stg}	-55 to 150	°C
Single Drain-to-Source Avalanche Energy - Starting $T_J = 25^{\circ}C$ ($V_{DD} = 120$ Vdc, $V_{GS} = 10$ Vdc, $I_L = 20$ Apk, $L = 0.3$ mH)	E _{AS}	60	mJ
Thermal Resistance – Junction to Case – Junction to Ambient	R _{θJC} R _{θJA}	1.1 62.5	°C/W
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds	T _L	260	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

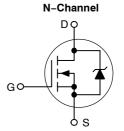


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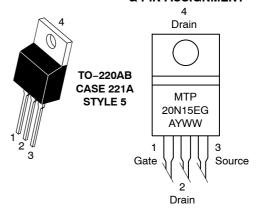
http://onsemi.com

20 AMPERES 150 VOLTS

 $R_{DS(on)} = 130 \text{ m}\Omega$



MARKING DIAGRAM & PIN ASSIGNMENT



 MTP20N15E
 = Device Code

 A
 = Assembly Location

 Y
 = Year

 WW
 = Work Week

 G
 = Pb-Free Package

ORDERING INFORMATION

Device	Package	Shipping
MTP20N15EG	TO-220AB (Pb-Free)	50 Units/Rail

^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

MTP20N15E

ELECTRICAL CHARACTERISTICS ($T_J = 25^{\circ}C$ unless otherwise noted)

	racteristic	Symbol	Min	Tim	Max	Unit
	acteristic	Зунион	IVIII	Тур	IVIAX	Unit
OFF CHARACTERISTICS						1
Drain-Source Breakdown Voltage (V _{GS} = 0 Vdc, I _D = 0.25 mAdc) Temperature Coefficient (Positive)		V _{(BR)DSS}	150 -	- TBD	- -	Vdc mV/°C
Zero Gate Voltage Collector Currer $(V_{DS} = 150 \text{ Vdc}, V_{GS} = 0 \text{ Vdc})$ $(V_{DS} = 150 \text{ Vdc}, V_{GS} = 0 \text{ Vdc}, T_{GS})$		I _{DSS}	- -	_ _	10 100	μAdc
Gate-Body Leakage Current (V _{GS}	$= \pm 20 \text{ Vdc}, \text{ V}_{DS} = 0)$	$I_{GSS(f)}$ $I_{GSS(r)}$	_ _	- -	100 100	nAdc
ON CHARACTERISTICS (Note 1.)						
Gate Threshold Voltage (V _{DS} = V _{GS} , I _D = 0.25 mAdc) Temperature Coefficient (Negative)		V _{GS(th)}	2.0	- TBD	4.0 _	Vdc mV/°C
Static Drain-Source On-Resistance	ee (V _{GS} = 10 Vdc, I _D = 10 Adc)	R _{DS(on)}	-	0.12	0.13	Ohm
Drain–Source On–Voltage ($V_{GS} = \frac{1}{2}$ ($I_D = 20$ Adc) ($I_D = 10$ Adc, $T_J = 125$ °C)	10 Vdc)	V _{DS(on)}	- -	_ _ _	2.8 2.6	Vdc
Forward Transconductance (V _{DS} =	13 Vdc, I _D = 10 Adc)	9FS	8.0	11	-	mhos
DYNAMIC CHARACTERISTICS			•	•	•	•
Input Capacitance		C _{iss}	_	1133	1627	pF
Output Capacitance	(V _{DS} = 25 Vdc, V _{GS} = 0 Vdc, f = 1.0 MHz)	C _{oss}	_	332	474	
Transfer Capacitance	1.027	C _{rss}	_	105	174	
SWITCHING CHARACTERISTICS (Note 2.)					
Turn-On Delay Time		t _{d(on)}	_	11	25	ns
Rise Time	$(V_{DD} = 75 \text{ Vdc}, I_D = 20 \text{ Adc},$	t _r	-	77	153	
Turn-Off Delay Time	V_{GS} = 10 Vdc, R_G = 9.1 Ω)	t _{d(off)}	-	33	67	
Fall Time		t _f	-	49	97	
Gate Charge		Q _T	-	39.1	55.9	nC
	(V _{DS} = 120 Vdc, I _D = 20 Adc,	Q ₁	-	7.5	-	
	V _{GS} = 10 Vdc)	Q ₂	-	22	-	
	Q_3	-	17	-		
SOURCE-DRAIN DIODE CHARAC	TERISTICS				-	
Forward On–Voltage (Note 1.)	$(I_S = 20 \text{ Adc}, V_{GS} = 0 \text{ Vdc})$ $(I_S = 20 \text{ Adc}, V_{GS} = 0 \text{ Vdc}, T_J = 125^{\circ}\text{C})$	V _{SD}	_ _	_ _	1.5 -	Vdc
Reverse Recovery Time		t _{rr}	-	160	-	ns
	$(I_{S} = 20 \text{ Adc}, V_{GS} = 0 \text{ Vdc}, \\ \text{dI}_{S}/\text{dt} = 100 \text{ A}/\mu\text{s})$	ta	-	123	-	1
		t _b	_	36.5	_	1
Reverse Recovery Stored Charge		Q _{RR}	-	1.1	_	μC

Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2%.
 Switching characteristics are independent of operating junction temperature.

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