TOSHIBA Transistor Silicon PNP Epitaxial Type (PCT PROCESS)

2SA1931

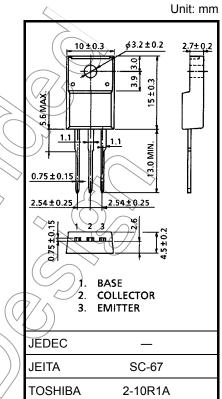
High-Current Switching Applications

• Low saturation voltage: VCE (sat) = -0.4 V (ma

- High-speed switching time: t_{stg} = 1.0 µs (typ.)
- Complementary to 2SC4881

Absolute Maximum Ratings (Tc = 25°C)

Characteristic		Symbol	Rating	Unit
Collector-base voltage		V _{CBO}	-60	$(\mathcal{N} \wedge \mathbb{Z})$
Collector-emitter voltage		V _{CEO}	-50	$\langle \psi \rangle$
Emitter-base voltage		V _{EBO}	-7) V
Collector current		Ι _C	_5	Ă
Base current		Ι _Β	1	A
Collector power dissipation	Ta = 25°C	Da	2.0	W
	Tc = 25°C	Pc	20	VV
Junction temperature		Тј	150	< c
Storage temperature range		T _{stg}	-55 to 150	3°



Weight: 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the

reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

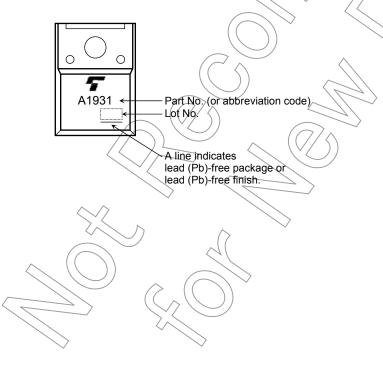
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

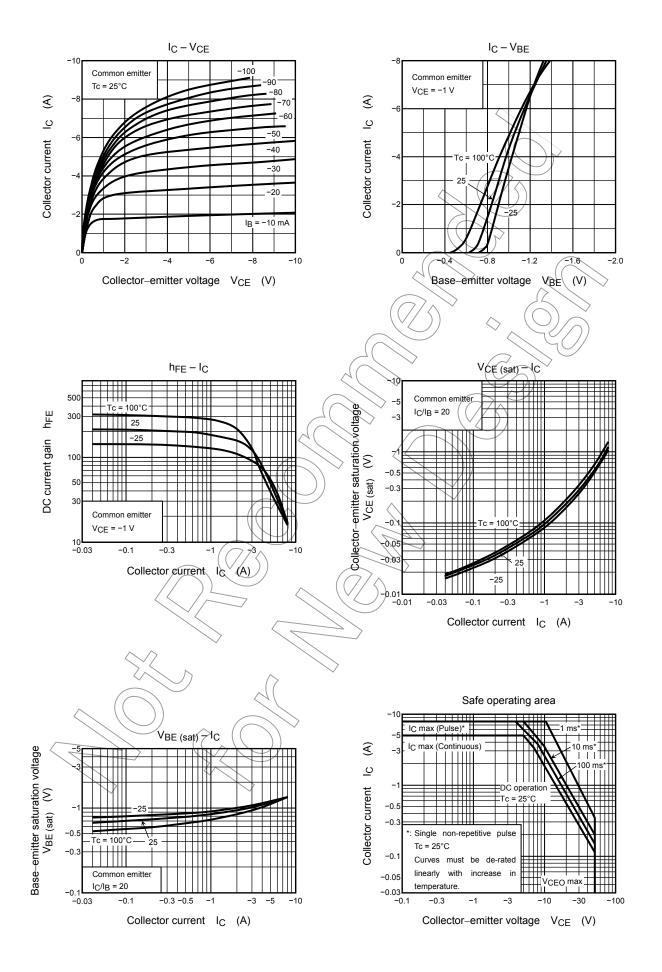
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Electrical Characteristics (Tc = 25°C)

Char	acteristic	Symbol	Test Conditions	Min	Тур.	Max	Unit
Collector cut-off of	current	I _{CBO}	V _{CB} = -50 V, I _E = 0	—	_	-1	μA
Emitter cut-off cu	rrent	I _{EBO}	$V_{EB} = -7 V, I_C = 0$	_	_	-1	μA
Collector-emitter	breakdown voltage	V (BR) CEO	I _C = -10 mA, I _B = 0	-50	_		V
DC current gain		h _{FE (1)}	V _{CE} = -1 V, I _C = 1 A	100		300	
		h _{FE (2)}	V _{CE} = -1 V, I _C = -3 A	60)/-		
Collector-emitter	saturation voltage	V _{CE (sat)}	$I_{\rm C} = -2$ A, $I_{\rm B} = -0.2$ A		-0.2	-0.4	V
Base-emitter satu	uration voltage	V _{BE (sat)}	I _C = -2 A, I _B = -0.2 A	\bigcirc	-0.9	-1.5	V
Transition freque	ncy	fT	$V_{CB} = -1 V, I_C = -1 A$	_	60	_	MHz
Collector output of	capacitance	C _{ob}	V _{CB} = -10 V, I _E = 0, f = 1 MHz	_	100	_	pF
Switching time	Turn-on time	t _{on}	20 µs Input B2 Output	_	0.1	\checkmark	μs
	Storage time	t _{stg}			1.0) –	
	Fall time	t _f	$-I_{B1} = I_{B2} = 0.15 \text{ A}$, dúty cycle $\le 1\%$	\mathcal{I}	0.1	_	

Marking





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