



100V 175°C N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	RDS(ON) Max	I _D Max Tc = +25°C
100V	14mΩ @ V _{GS} = 10V	59A
	20mΩ @ V _{GS} = 6V	50A

Description

This new generation MOSFET features low on-resistance and fast switching, making it ideal for high efficiency power management applications.

Applications

- Power Management Functions
- DC-DC Converters
- Backlighting

Features

- Rated to +175°C Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching Ensures More Reliable and Robust End Application
- Low RDS(ON) Minimizes Power Losses
- Low Q_g Minimizes Switching Losses
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at

https://www.diodes.com/products/automotive/automotive-products/.

 This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.

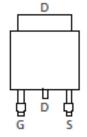
https://www.diodes.com/quality/product-definitions/

Mechanical Data

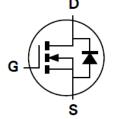
- Case: TO252
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.33 grams (Approximate)







Pin Out Top View



Equivalent Circuit

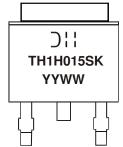
Ordering Information (Note 4)

- 5			
	Part Number	Case	Packaging
	DMTH10H015SK3-13	TO252 (DPAK)	2500/Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information



Dill = Manufacturer's Marking
TH1H015SK = Product Type Marking Code
YYWW = Date Code Marking
YY = Last Two Digits of Year (ex: 21 = 2021)
WW = Week Code (01 to 53)



Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V _{DSS}	100	V
Gate-Source Voltage	Vgss	±20	V
Continuous Drain Current, V _{GS} = 10V	ID	59 42	А
Pulsed Drain Current (10µs Pulse, T _C = +25°C, Package Limited)	I _{DM}	235	Α
Maximum Continuous Body Diode Forward Current (Note 6)	ls	2.7	Α
Pulsed Body Diode Forward Current (10µs Pulse, T _C = +25°C, Packa	I _{SM}	235	Α
Avalanche Current, L = 0.1mH	I _{AS}	15.8	Α
Avalanche Energy, L = 0.1mH	Eas	12.5	mJ
Avalanche Current, L = 3mH (Note 8)	las	7.5	Α
Avalanche Energy, L = 3mH (Note 8)	Eas	85	mJ

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

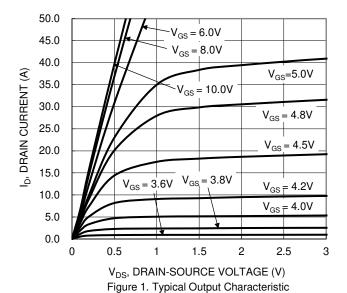
Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		PD	2	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	75	°C/W
Total Power Dissipation (Note 6)		P _D	3.7	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	40	°C/W
Thermal Resistance, Junction to Case		R ₀ JC	2	C/VV
Operating and Storage Temperature Range		TJ, TSTG	-55 to +175	°C

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

	T	1	1		1		
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BVDSS	100	_	_	V	$V_{GS} = 0V$, $I_D = 1mA$	
Zero Gate Voltage Drain Current	IDSS	_	_	1	μΑ	$V_{DS} = 80V$, $V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(TH)}	2	_	4	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Static Drain-Source On-Resistance		_	11.1	14	mΩ	V _{GS} = 10V, I _D = 20A	
Static Diain-Source On-Nesistance	RDS(ON)	_	14.7	20	11122	$V_{GS} = 6V, I_D = 20A$	
Diode Forward Voltage	V _{SD}	_	0.86	1.3	V	V _G S = 0V, I _S = 20A	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss	_	2343	_		V _{DS} = 50V, V _{GS} = 0V f = 1MHz	
Output Capacitance	Coss	_	487	_	рF		
Reverse Transfer Capacitance	Crss	_	26	_		I = IIVITZ	
Gate Resistance	Rg	_	0.69	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge	Qg	_	30.1	_		. 501/ 1 404	
Gate-Source Charge	Qgs	_	7.5	_	nC	$V_{DD} = 50V, I_D = 10A,$ $V_{GS} = 10V$	
Gate-Drain Charge	Q_{gd}	_	6.5	_			
Turn-On Delay Time	td(ON)	_	9.8	_			
Turn-On Rise Time	tr	_	7.8	_		$\begin{split} V_{DD} &= 50 V, V_{GS} = 10 V, \\ I_D &= 10 A, R_g = 6 \Omega \end{split}$	
Turn-Off Delay Time	tD(OFF)	_	22.5	_	ns		
Turn-Off Fall Time	t _F	_	9.6	_			
Reverse Recovery Time	trr	_	43	_	ns	I_ 10A di/dt 100A/	
Reverse Recovery Charge	Q _{RR}		65.1	_	nC	$T_{F} = 10A$, di/dt = 100A/ μ s	

 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing. Notes:





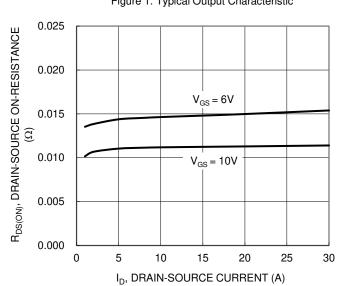


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

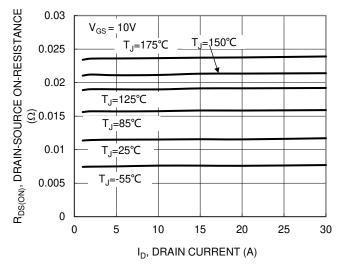
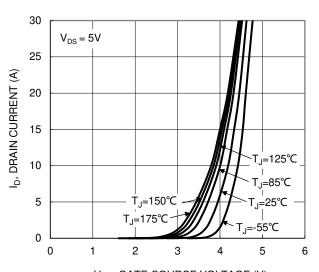
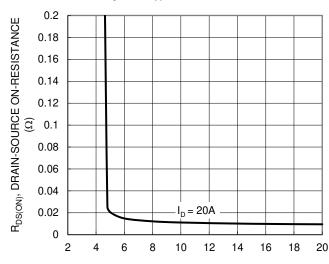


Figure 5. Typical On-Resistance vs. Drain Current and Temperature



V_{GS}, GATE-SOURCE VOLTAGE (V) Figure 2. Typical Transfer Characteristic



V_{GS}, GATE-SOURCE VOLTAGE (V) Figure 4. Typical Transfer Characteristic

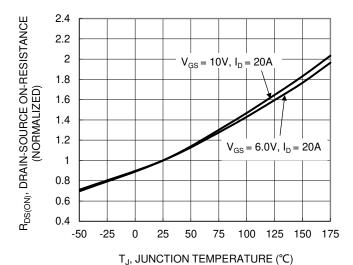


Figure 6. On-Resistance Variation with Temperature



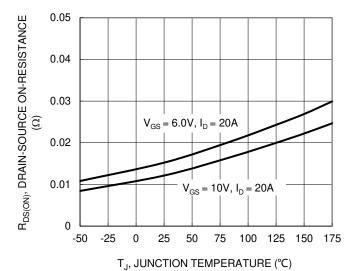
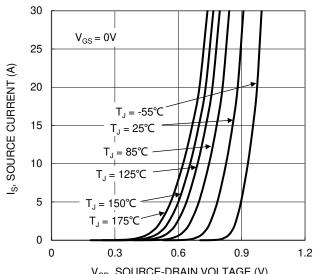
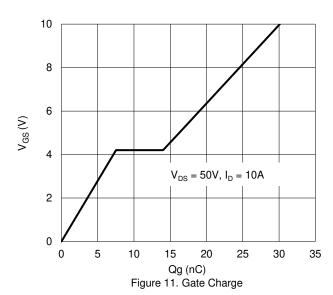


Figure 7. On-Resistance Variation with Temperature

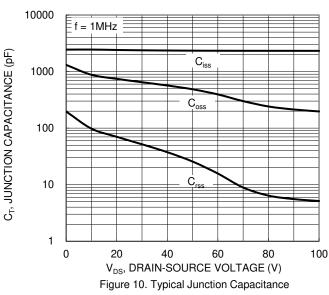


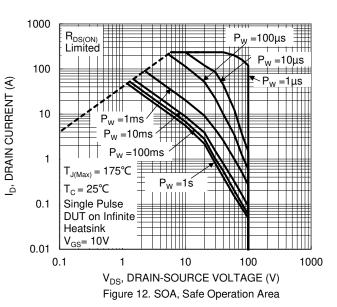
V_{SD}, SOURCE-DRAIN VOLTAGE (V) Figure 9. Diode Forward Voltage vs. Current



4 $V_{\text{GS}(\text{TH})},$ GATE THRESHOLD VOLTAGE (V) 3.5 3 2.5 $I_D = 1 \text{mA}$ 2 $I_{D} = 250 \mu A$ 1.5 1 0.5 0 100 125 150 175 -50 -25 25 50 75

 T_J , JUNCTION TEMPERATURE (°C) Figure 8. Gate Threshold Variation vs. Junction Temperature







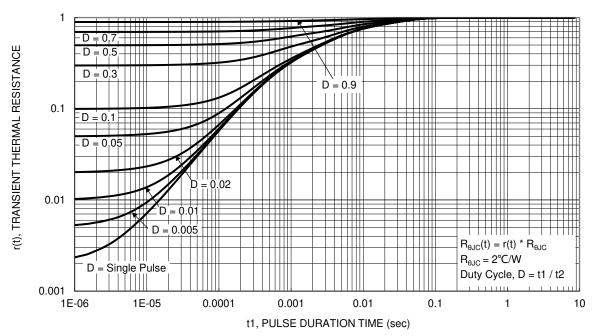


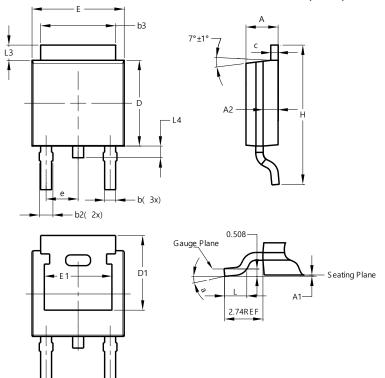
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

TO252 (DPAK)

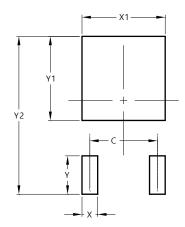


T0050 (DD 416)						
TO252 (DPAK)						
Dim	Min	Max	Тур			
Α	2.19	2.39	2.29			
A 1	0.00	0.13	0.08			
A2	0.97	1.17	1.07			
b	0.64	0.88	0.783			
b2	0.76	1.14	0.95			
b3	5.21	5.46	5.33			
С	0.45	0.58	0.531			
D	6.00	6.20	6.10			
D1	5.21	-	-			
е	-	-	2.286			
Е	6.45	6.70	6.58			
E1	4.32	-	-			
Н	9.40	10.41	9.91			
L	1.40	1.78	1.59			
L3	0.88	1.27	1.08			
L4	0.64	1.02	0.83			
а	0°	10°	-			
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

TO252 (DPAK)



Dimensions	Value (in mm)		
С	4.572		
Х	1.060		
X1	5.632		
Υ	2.600		
Y1	5.700		
Y2	10.700		



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