# Obsolete - Part Discontinued







PI3USB4000B

Ultra-High Voltage Protection USB2 1:2 Mux/DeMux

#### **Features**

- → Differential Bi-Directional 2:1 Mux/DeMux
- → Wide Input Voltage Range: 0-5.5V
- → Wide bandwidth: 1GHz
  - ♦ Ultra-low Con: 7pF
  - ♦ Ultra-low Ron:  $5\Omega$  (typ)
- → Low Propagation Delay, 0.25ns typ
- → Low Off-Isolation, -30dB@240MHz
- → Low Crosstalk: -35dB@240MHz,
- → Low Power Consumption: 35µA typical
- → Wide Supply Voltage 2.7-5.5V
- → Support 1.8V Logic on Control Pins
- → Protection Feature
  - ♦ Off-protection for current leakage in power-down mode
  - ♦ All I/O pins are high voltage tolerance
    - C0+/C0- tolerance to 24V
    - Lx+/- tolerance to 6V
    - V<sub>DD</sub> tolerance to 9V
  - ♦ Over-voltage protection when Vbus short to C0-/C0+ when device is power-on and enabled
- → ESD Protection on (C0+/-)
  - ♦ IEC61000-4-2
- → Wide Temperature Range: -40°C to 85°C
- → Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- → Halogen and Antimony Free. "Green" Device (Note 3)
- → Packaging (Pb-free & Green):
  - ♦ 10-contact, UQFN (ZUA10), 1.5x2mm, 0.5mm(H), 0.6mm pitch
  - ◆ 10-contact, UQFN (ZM10), 1.4x1.8mm, 0.55mm(H), 0.4mm pitch

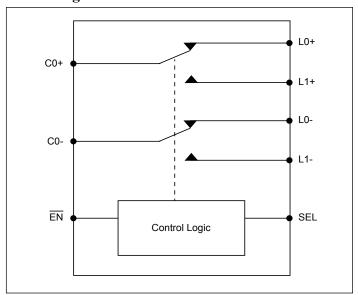
#### **Description**

The PI3USB4000B is a 2-to-1 differential channel multiplexer/demultiplexer switch. C0+/C0- pins can tolerate voltages up to 24V. Over-voltage protection (OVP) is implemented at 4.75V to immediately switch off the channels when over-voltage condition is detected. PI3USB4000B can pass USB2.0 signal with bandwidth 1GHz to maintain signal integrity and eye diagram open.

### **Applications**

→ Smart Phone, type-c application, Tablets, NB, PC

#### **Block Diagram**



#### Notes:

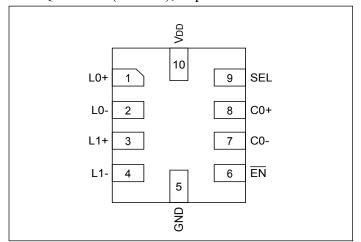
- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.



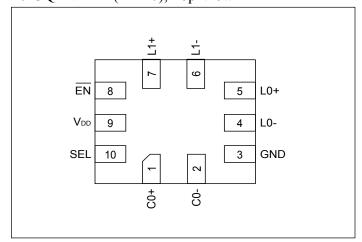


# **Pin Configuration**

10-UQFN Pin# (ZUA10), Top View



# 10-UQFN Pin# (ZM10), Top View



### **Pin Description**

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10-UQFN Pin# (ZUA10)	10-UQFN Pin# (ZM10)	Pin Name	Signal Type	Description		
8,	1,	C0+,	I/O	Sizual I/O Common Port		
7	2	C0-	I/O	Signal I/O, Common Port		
3,	7,	L1+,	I/O	Cimal I/O Chanal I		
4	8	L1-	I/O	Signal I/O, Channel 1		
1,	5,	L0+,	I/O	Signal I/O, Channel 0		
2	4	L0-	I/O			
9	10	SEL	I	Operation mode Select (when SEL=0: C0→L0, when SEL=1: C0→L1)		
6	8	EN	I $\overline{EN}$ = 1, Power down is enabled. Please see Truth Table.			
10	9	VDD	Pwr Positive Supply Voltage			
5	3	GND	Pwr	Power ground		

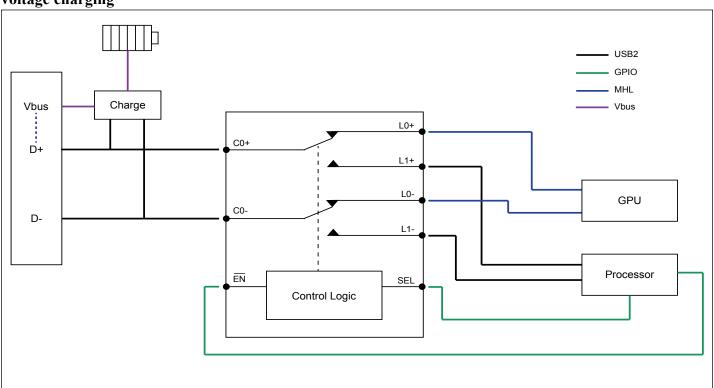
#### **Truth Table**

Function	SEL	EN
C0+/- to L0+/-	L	L
C0+/- to L1+/-	Н	L
All Switches Hi-z	X	Н

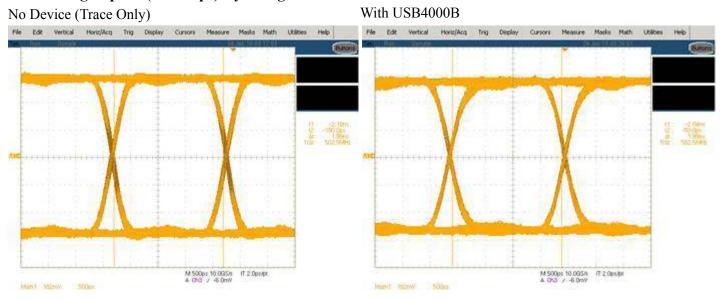




# $PI3USB4000B\ application\ in\ MHL\ Switching\ and\ provide\ overvoltage\ protection\ for\ D+/-\ when\ high\ voltage\ charging$



#### USB2.0 High Speed (480Mbps) Eye Diagram







#### **Maximum Ratings**

(Above which useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	-65°C to +150°C
Supply Voltage (VDD) to Ground Potent	rial0.3V to +9V
Channel Input/Output Voltage (Lx+/-)	0.3V to +6V
Channel Input/Output Voltage (C0+/-)	0.3V to +24V
Control Pins Input Voltage (EN/SEL)	0.3V to +6V
ESD (All Pins)	2KV (HBM) and 1KV (CDM)
Channel Input/Output Current (Lx/C0).	±50mA

#### Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

### **Recommended Operating Conditions**

Symbol	Description	Test Conditions	Min.	Тур.	Max.	Units
$V_{DD}$	Power Supply		2.7	3.3	6.0	V
V <sub>IO</sub>	Analog Voltage Range		0		5.5	V
$V_{\rm I}$	Voltage Range for Control Pins		0		5.5	V
$I_{\mathrm{DD}}$	Current Consumption in Normal Operation	$V_{DD}$ =3.3 $\overline{V}$ , $V_{IO}$ =0 $\overline{V}$ , SEL= GND or $V_{DD}$ , $\overline{EN}$ = Low		35	45	μА
I <sub>DD_OVP</sub>	Current Consumption in OVP	V <sub>DD</sub> =3.3V, V <sub>C0+</sub> /V <sub>C0-</sub> =5.5V, SEL=GND or V <sub>DD</sub> , EN=Low		35		μА
$I_{\mathrm{DDQ}}$	Chip Disabled Current Consumption	$V_{DD}$ =3.3 $\frac{V}{EN}$ =0 $\frac{V}{EN}$ = High		1	2	μА
$T_{A}$	Operating Temperature Range		-40		85	°C

# DC Electrical Characteristics for Switching over Operating Range

 $(T_A = -40^{\circ}\text{C} \text{ to } 85^{\circ}\text{C}, \text{ Typical values are at V}_{DD} = 3.3\text{V}, T_A = 25^{\circ}\text{C}, \text{ (unless otherwise noted))}$ 

Parameter	Description	<b>Test Conditions</b>	Min.	Тур.	Max.	Units	
Control Pins - EN	Control Pins - EN/SEL						
V <sub>IH</sub> - cntrl signals	Input HIGH Voltage for SEL and EN	$V_{\rm DD} = 2.7-5.5 V$	1.2			V	
V <sub>IL</sub> - cntrl signals	Input LOW Voltage for SEL and EN	$V_{\rm DD} = 2.7-5.5 \text{V}$			0.6	V	
I <sub>IH</sub>	Input HIGH Current for SEL and EN	$V_{\rm I} = 0-5.5 \rm V$	-1		1	μΑ	
I <sub>IL</sub>	Input LOW Current for SEL and EN	$V_{I} = 0-5.5V$	-1		1	μΑ	
High Speed IO – L	0/L1/C0						
V <sub>OVP</sub>	OVP trigger voltage		4.6	4.75	5.0	V	
Ron	ON resistance	$V_{I/O} = 0V, 0.4V, I_{on} = -8 \text{ mA}$		5	8	Ω	
Δ Ron	On resistance between + and - channel	$V_{I/O} = 0V, 0.4V, I_{on} = -8 \text{ mA}$		0.5	1	Ω	
Ron_Flat	ON resistance flatness	$V_{I/O} = 0V, 0.4V, I_{on} = -8 \text{ mA}$		0.2	0.5	Ω	
I <sub>off</sub>	Power-off leakage	$V_{DD} = 0V, V_{I/O} = 0 - 3.6V$	-1		1	μA	
I <sub>OC</sub>	Channel off leakage current	$\overline{\mathrm{EN}} = \mathrm{V_{DD}} = 3.3 \mathrm{V},  \mathrm{V_{I/O}} = 0 - 3.6 \mathrm{V}$	-1		1	μA	
I <sub>ON</sub>	Channel on leakage current	$\overline{\rm EN}$ =0V, V <sub>DD</sub> =3.3V, V <sub>I/O</sub> =0-3.6V	-1		1	μΑ	
I <sub>OVP</sub>	Leakage current on C0+/C0- in OVP mode	EN=0V, V <sub>DD</sub> =3.3V, V <sub>C0+</sub> or V <sub>C0-</sub> =20V		3	15	μΑ	
Z <sub>ON_GND</sub>	On-State impedance to GND	EN=low, SEL=H or L V <sub>I/O</sub> =0-18V	5	7		ΜΩ	





# **Dynamic Electrical Characteristics**

 $(T_A = -40^{\circ}C \text{ to } 85^{\circ}C, \text{ Typical values are at V}_{DD} = 3.3V, T_A = 25^{\circ}C, \text{ (unless otherwise noted))}$ 

Parameter	Description	<b>Test Conditions</b>	Min.	Тур.	Max.	Units	
Control Pins -	Control Pins - EN/SEL						
C <sub>I</sub>	Input capacitance	F=1MHz		5		pF	
High Speed IO	- L0/L1/C0		·				
Con	ON Capacitance	f=1MHz		7		pF	
Coff	OFF Capacitance	f=1MHz		9		pF	
DDIL	Insertion Loss	f=240MHz		-0.5		dB	
DDRL	Differential Return Loss	f=240MHz		-15		dB	
DDOI	Differential OFF Isolation	f=240MHz		-30		dB	
		f=100kHz		-80		dB	
DDXT	Differential Crosstalk	f=240MHz		-35		dB	
BW	-3dB Bandwidth			1		GHz	

# Switching Characteristics<sup>(1)</sup>

( $T_A = -40$  °C to 85 °C, Typical values are at  $V_{DD} = 3.3$ V,  $T_A = 25$  °C, (unless otherwise noted))

Parameter	Description	Test Conditions	Min.	Тур.	Max.	Units
t <sub>OVP</sub>	OVP Response Time <sup>(1)</sup>	$R_{LX}=600\Omega$ , time from the voltage on $C0\pm=4{\sim}6V$ to the voltage on $L_X\pm=4.75$		0.5	1	μs
t <sub>PZH</sub> , t <sub>PZL</sub>	Line Enable Time	See Test Circuit for Electrial Characteristics		20		μs
t <sub>PHZ</sub> , t <sub>PLZ</sub>	Line Disable Time		50		ns	
$t_{Pd}$	Propagation Delay		250		ps	
t <sub>b-b</sub>	Bit-to-bit Skew Within the Same Differential Pair <sup>(1)</sup>			8	20	ps
Ton	Device Enable Time			100		μs
T <sub>off</sub>	Device Disable Time			50		ns

#### Note:

1. Guaranteed by design.



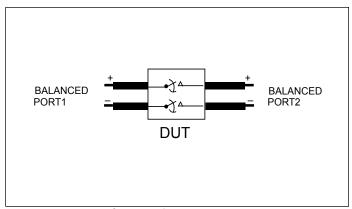


Fig 1. Differential Insertion Loss Setup

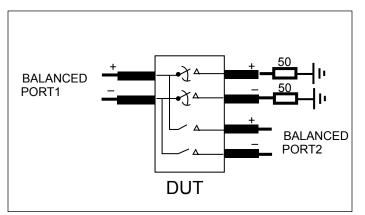


Fig 3. Crosstalk Setup

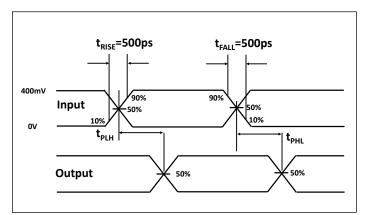


Fig 5. Skew Test

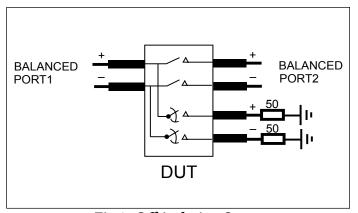


Fig 2. Off-isolation Setup

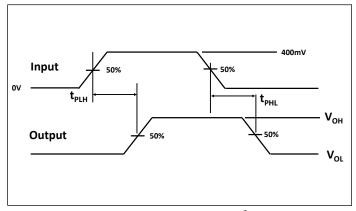
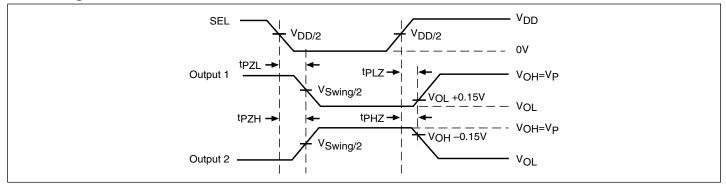


Fig 4. Propagation Delay



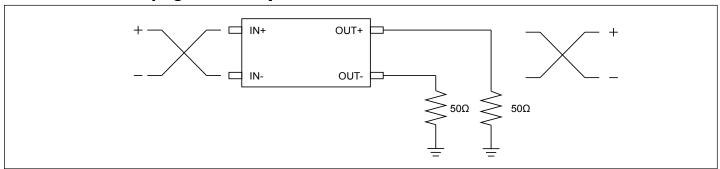


# **Switching Waveforms**



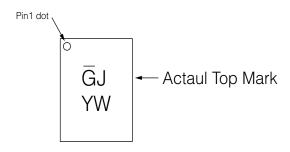
**Voltage Waveforms Enable and Disable Times** 

### **Test Circuit for Propagation Delay**



# **Part Marking**

ZUA Packag and ZM Package



Y: Year

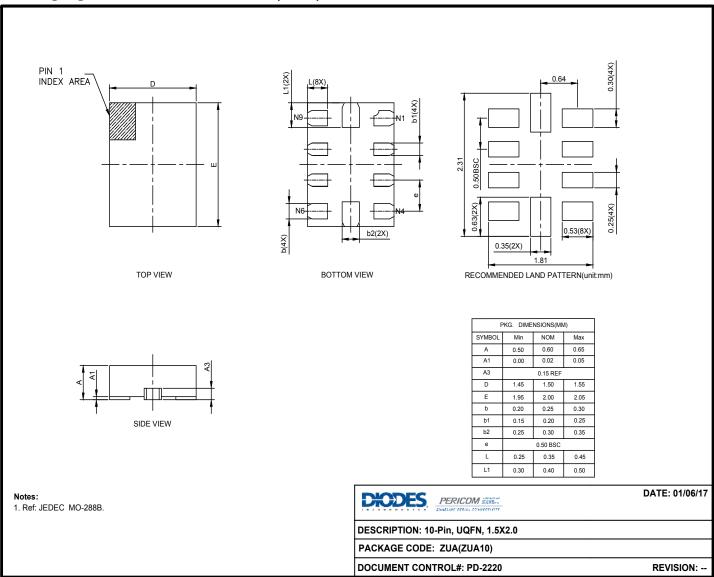
W: Workweek

Note: Date Code per MA-1251





# Packaging Mechanical: 10-UQFN (ZUA)

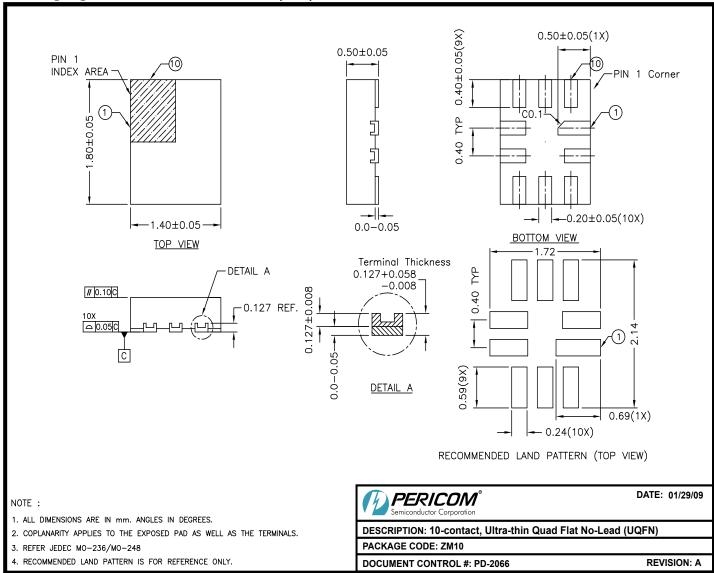


17-0002





### Packaging Mechanical: 10-UQFN (ZM)



09-0072

#### For latest package info.

 $please\ check: http://www.diodes.com/design/support/packaging/pericom-packaging/packaging-mechanicals-and-thermal-characteristics/pericom-packaging/packaging-mechanicals-and-thermal-characteristics/pericom-packaging-pericom-packaging-mechanicals-and-thermal-characteristics/pericom-packaging-perico$ 

#### **Ordering Information**

Ordering Code	Package Code	Package Description
PI3USB4000BZUAEX	ZUA	10-Pin, 1.5x2.0 (UQFN)
PI3USB4000BZMEX	ZM	10-contact, Ultra-thin Quad Flat No-Lead (UQFN)

#### Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. E = Pb-free and Green
- 5. X suffix = Tape/Reel





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