ESP-12E
Brief Spec

规格修改记录（**SPECIFICATION CHANGE HISTORY**）

编号	修订日期	修订内容	修订者
1	2012-5-5	最初版本	Harri

Approve 批准	Check 审核	Preparation 拟制	Date 日期

目录

Product Description

Description

ESP-12E is a low power consumption of the UART-WiFi module, with very competitive prices in the industry and ultra low power consumption technology, designed specifically for mobile devices and IOT applications, user's physical device can be connected to a Wi-Fi wireless network, Internet or intranet communication and networking capabilities. ESP-07 the use of small ceramic antenna package can support IPEX interface. users have a variety of installation options.

Features

- ▪ 802.11 b/g/n protocol
- ▪ Wi-Fi Direct (P2P), soft-AP
- ▪ Integrated TCP/IP protocol stack
- ▪ +19.5dBm output power in 802.11b mode
- ▪ Power down leakage current of < 10uA
- ▪ Integrated low power 32-bit MCU
- ▪ SDIO 2.0, SPI, UART
- ▪ STBC, 1x1 MIMO, 2x1 MIMO
- ▪ A-MPDU & A-MSDU aggregation & 0.4μs guard interval
- ▪ Wake up and transmit packets in < 2ms
- ▪ Standby power consumption of < 1.0mW (DTIM3)

Applications

- Smart power plugs
- Home automation
- Mesh network
- Industrial wireless control
- Baby monitors
- IP Cameras
- Sensor networks
- Wi-Fi location-aware devices
- Security ID tags
- Wi-Fi position system beacons

Electrical performance

Digital IO Pads
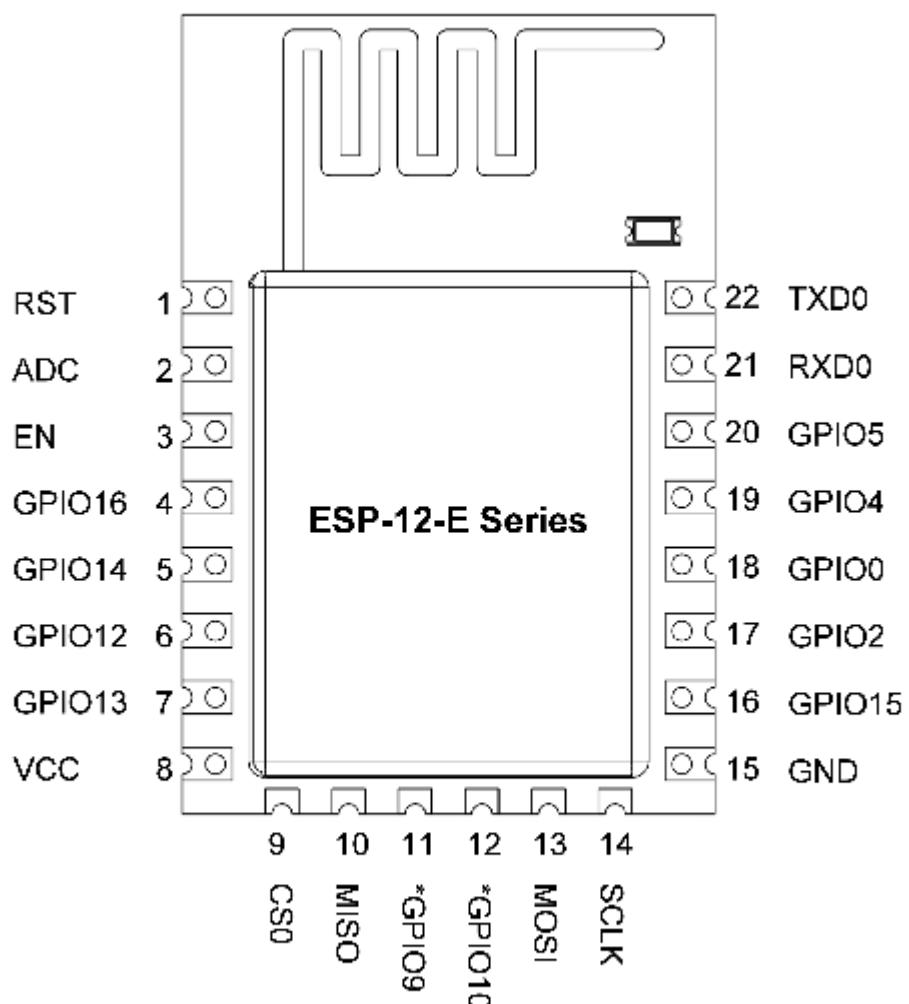
Parameter	Symbol	Min	Max	Unit
InputLow voltage	Vil	-0.3	0.25xViO	V
InputHighVoltage	Vih	0.75xVio	3.3	V
InputLeakageCurrent	Iil		50	nA
OutputLowVoltage	Vol		0.1 xVio	V
OutputHighVoltage	Voh	0.8xVio		V
InputPinCapacitance	Cpad		5	pF
VDDIO	Vio	1.8	3.3	V
MaximumDriveCapability	Imax		12	mA
Temperature	Tamb	-40	125	°C

Receiver Sensitivity

Description	MIN	Typical	MAX	Unit
Input frequency	2412		2484	MHz
Input impedance		50		Ω
Input reflection			-10	dB
Output power of PA for 72.2Mbps	14	15	16	dBm
Output power of PA for 11b mode	17.5	18.5	19.5	dBm
Sensitivity				
CCK 1Mbps		-98		dBm
CCK 11Mbps		-91		dBm
6Mbps(1/2BPSK)		-93		dBm
54Mbps(3/4 64-QAM)		-75		dBm
HT20 · MCS7（65Mbps · 72.2Mbps）		-71		dBm
Adjacent Channel Rejection				
OFDM · 6Mbps		37		dB
OFDM · 54Mbps		21		dB
HT20 · MCS0		37		dB
HT20 · MCS7		20		dB

Current Consumption

Mode	MIN	Typical	MAX	Unit
Send 802.11b · CCK 1Mbps · Pout=+19.5dBm		215		mA
Send 802.11b · CCK 11Mbps · Pout=+18.5dBm		197		mA
Send 802.11g · OFDM54 Mbps · Pout=+16dBm		145		mA
Send 802.11n · MCS7 · Pout=+14dBm		135		mA
Receive 802.11b · Length 1024 Byte · -80dBm		100		mA
Receive 802.11g · Length 1024 Byte · -70dBm		100		mA
Receive 802.11n · Length 1024 Byte · -65dBm		102		mA
Standby		70		mA
Power Down		0.5		μA

RST	1	
ADC	2	
EN	3	
GPIO16	4	
GPIO14	5	
GPIO12	6	
GPIO13	7	
VCC	8	

ESP-12-E Series

22	TXD0	
21	RXD0	
20	GPIO5	
19	GPIO4	
18	GPIO0	
17	GPIO2	
16	GPIO15	
15	GND	

9 CS0
10 MISO
11 *GPIO9
12 *GPIO10
13 MOSI
14 SCLK
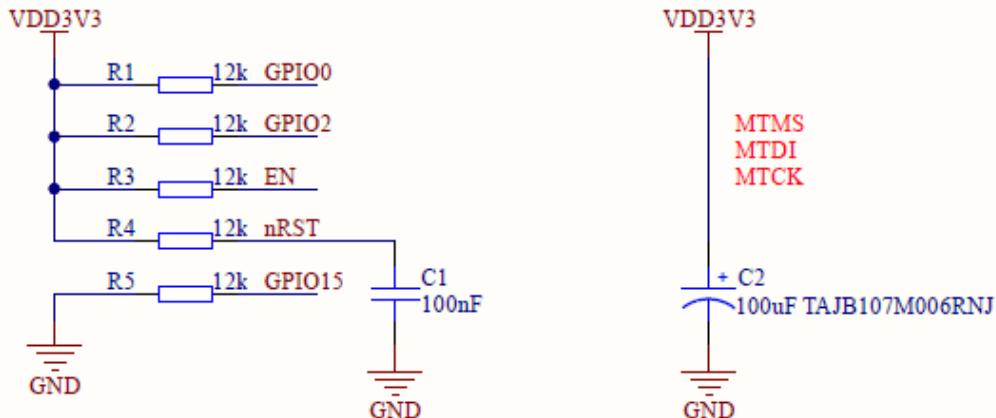
* Can only be used on ESP12-D.

PIN	Function	Description
1	RST	1) Reset Pin. Active low ; 2) NC Or External MCU control ;
2	ADC/TOUT	1) 10-bit ADC Analog Input 0-1V ;
3	EN	1) Module Enable. Active HIGH
4	GPIO16	1) GPIO (WEAK UP)
5	GPIO14	1) GPIO
6	GPIO12	1) GPIO
7	GPIO13	1) GPIO 2) UART2 RXD
8	VDD	1) Power supply . 3.3V IN ;
9	CS0	1) Chip selection of SPI interface.
10	MISO	1) MISO of SPI interface.
11	GPIO9	1) GPIO (Only available on ESP-12-D)
12	GPIO10-	1) GPIO (Only available on ESP-12-D)
13	MOSI	1) MOSI of SPI interface.
14	SCLK	1) Clock of SPI interface.
15	CND	1) Power Ground
16	GPIO15	1) GPIO 2) UART2 TXD
17	GPIO2	1) GPIO 2) WIFI status. Connection inside the module LED
18	GPIO0	1) GPIO
19	GPIO4	1) GPIO
20	GPIO5	1) GPIO
21	RXD0	1) UART0 RXD
22	TXD0	1) UART0 TXD

BOOT Mode

GPIO15	GPIO0	GPIO2	
1	X	X	SDIO/SPI WIFI
0	0	1	UART Download
0	1	1	Flash BOOT

Reference Schematic

VDD3V3

R1 12k GPIO0
R2 12k GPIO2
R3 12k EN
R4 12k nRST
R5 12k GPIO15

C1
100nF

GND

GND

VDD3V3

MTMS
MTDI
MTCK

+ C2
100uF TAJB107M006RNJ

GND
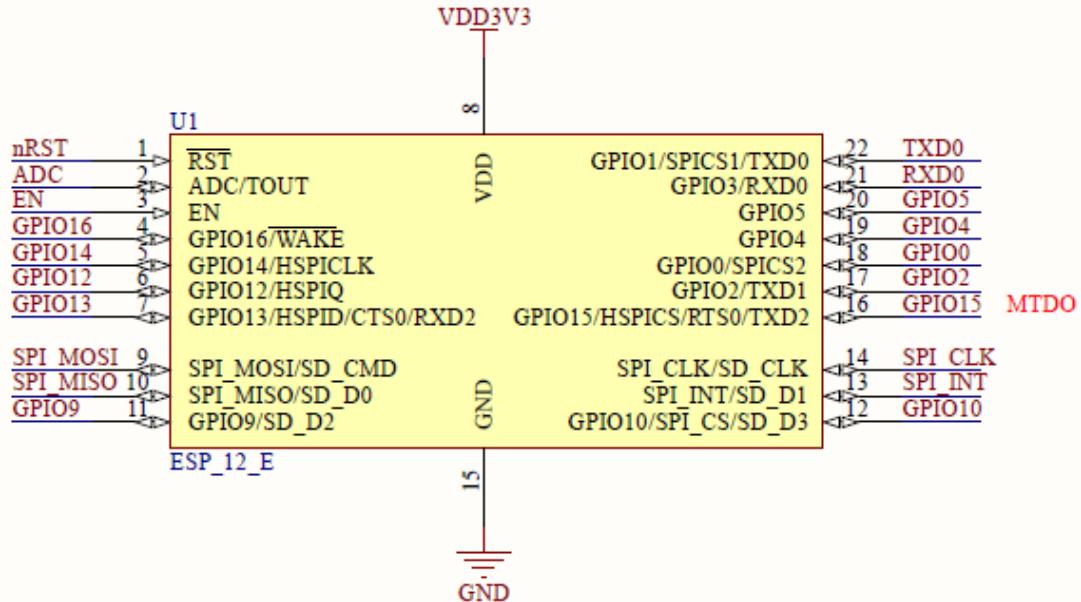
MATTERS NEEDING ATTENTION
On every boot/reset/wakeup,
GPIO15 MUST keep LOW, GPIO2 MUST keep HIGH.
GPIO0 HIGH ->RUN MODE, LOW -> FLASH MODE.
When you need to use the sleep mode,GPIO16 and RST should be connected,
and GPIO16 will output LOW to reset the system at the time of wakeup.

VDD3V3

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U1

nRST	1	RST		GPIO1/SPICS1/TXD0	22	TXD0
ADC	2	ADC/TOUT	VDD	GPIO3/RXD0	21	RXD0
EN	3	EN		GPIO5	20	GPIO5
GPIO16	4	GPIO16/WAKE		GPIO4	19	GPIO4
GPIO14	5	GPIO14/HSPICLK		GPIO0/SPICS2	18	GPIO0
GPIO12	6	GPIO12/HSPIQ		GPIO2/TXD1	17	GPIO2
GPIO13	7	GPIO13/HSPID/CTS0/RXD2		GPIO15/HSPICS/RTS0/TXD2	16	GPIO15 MTDO

SPI_MOSI 9 SPI_MOSI/SD_CMD   SPI_CLK/SD_CLK 14 SPI_CLK
SPI_MISO 10 SPI_MISO/SD_D0   GND SPI_INT/SD_D1 13 SPI_INT
GPIO9 11 GPIO9/SD_D2   GPIO10/SPI_CS/SD_D3 12 GPIO10

ESP_12_E

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GND

Reßow Profile

Refer to IPC/JEDEC standard; Peak Temperature : <250°C;   Number of Times: 2 times;

Slope: *1-2C/sec* лах.
(217C to peak)

j/   ;2;

217.C

Preheat: 150~200℃

60 — 120 sec   40 -60sec

25℃

Ramp up rate
Max. *2' C/sec.*

Time (sec)

2 (mm)

2 (mm)

24 (mm)

16 (mm)

2 (mm)

1 (mm)

2.2 (mm)

1 (mm)

1.5 (mm)