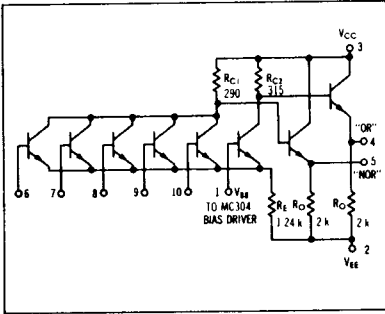


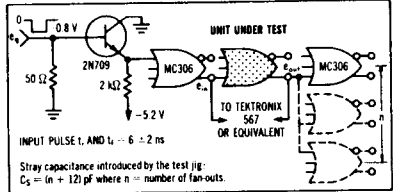
5-INPUT GATE

MC301

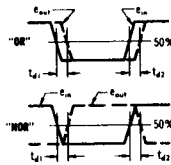
A 5-input gate that provides the positive logic "OR" function and its complement simultaneously.



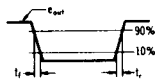
SWITCHING TIME TEST CIRCUIT



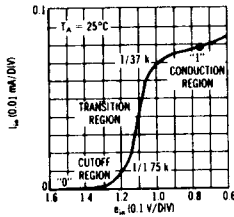
PROPAGATION DELAY



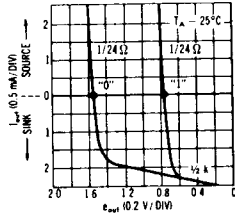
RISE AND FALL TIME



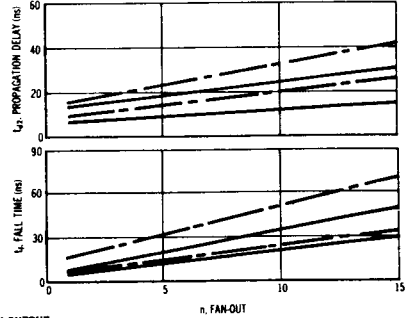
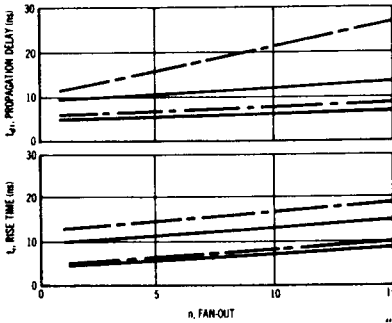
TYPICAL INPUT CHARACTERISTICS



TYPICAL OUTPUT CHARACTERISTICS



SWITCHING CHARACTERISTICS (10% to 90% distribution)



"NOR" OUTPUT

— -55°C and +25°C
 - -125°C

MC301 (continued)

ELECTRICAL CHARACTERISTICS

Characteristic		Test Conditions										Test Limits						Unit						
		V _{dc} = 1%										-55°C		+25°C		+125°C								
		V _{in}		V _{max}		V _L		V _{ST}		V _{SA}		dV _{in}		I _L		Ground			Symbol		Pin No.		I _{in} []	
Pin No.	Pin No.	Pin No.	Pin No.	Pin No.	Pin No.	Pin No.	Pin No.	Pin No.	Pin No.	Pin No.	Pin No.	Pin No.	Pin No.	Pin No.	Pin No.	Pin No.	Pin No.	Pin No.	Pin No.	Pin No.	Pin No.	Pin No.	Pin No.	
Power Supply Brain Current		---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---
Input Current		6	---	---	---	2.7.8.9.10	1	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---
"00" Logical "1" Output Voltage		---	---	---	---	2.7.8.9.10	1	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---
"00" Logical "0" Output Voltage		---	---	---	---	2.7.8.9.10	1	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---
"01" Logical "1" Output Voltage		---	---	---	---	2.7.8.9.10	1	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---
"01" Logical "0" Output Voltage		---	---	---	---	2.7.8.9.10	1	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---
"00" Output Voltage Change (No load to full load)		---	---	---	---	2.7.8.9.10	1	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---
"01" Output Voltage Change (No load to full load)		---	---	---	---	2.7.8.9.10	1	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---
"00" Saturation Breakpoint Voltage		---	---	---	---	2.7.8.9.10	1	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---
Switching Times		Pulse In	Pulse Out	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---
Propagation Delay Time		6	4	---	---	2.7.8.9.10	1	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---
Rise Time		6	4	---	---	2.7.8.9.10	1	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---
Fall Time		6	4	---	---	2.7.8.9.10	1	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---

Pins not listed are left open (1) Input voltage is adjusted to obtain dV_{in}/V_{in} = 0% (2) Current test conditions: no load (3) full load (4) -2.5mADC ±5%

