IGBT

This Insulated Gate Bipolar Transistor (IGBT) features a robust and cost effective Field Stop (FS) Trench construction, and provides superior performance in demanding switching applications, offering both low on state voltage and minimal switching loss. The IGBT is well suited for half bridge resonant applications. Incorporated into the device is a soft and fast co–packaged free wheeling diode with a low forward voltage.

Features

- Low Saturation Voltage using Trench with Fieldstop Technology
- Low Switching Loss Reduces System Power Dissipation
- Low Gate Charge
- Soft, Fast Free Wheeling Diode
- These are Pb-Free Devices

Typical Applications

• Inverter Welding

ABSOLUTE MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-emitter voltage	V_{CES}	600	V
Collector current @ Tc = 25°C @ Tc = 100°C	lс	120 60	A
Diode forward current @ Tc = 25°C @ Tc = 100°C	l _F	120 60	Α
Pulsed collector current, T _{pulse} limited by T _{Jmax}	I _{CM}	240	Α
Diode pulsed current, T _{pulse} limited by T _{Jmax}	I _{FM}	240	Α
Gate-emitter voltage	V_{GE}	±20	V
Power Dissipation @ Tc = 25°C @ Tc = 100°C	P_D	298 119	W
Operating junction temperature range	T_J	-55 to +150	°C
Storage temperature range	T _{stg}	-55 to +150	°C
Lead temperature for soldering, 1/8" from case for 5 seconds	T _{SLD}	260	°C

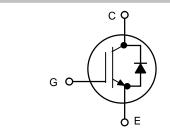
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

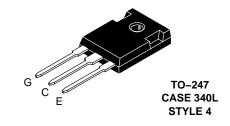


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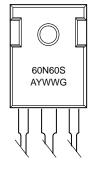
http://onsemi.com

60 A, 600 V V_{CEsat} = 2.0 V E_{off} = 0.60 mJ





MARKING DIAGRAM



A = Assembly Location

Y = Year WW = Work Week G = Pb-Free Package

ORDERING INFORMATION

Device	Package	Shipping
NGTB60N60SWG	TO-247 (Pb-Free)	30 Units / Rail

THERMAL CHARACTERISTICS

Rating	Symbol	Value	Unit
Thermal resistance junction-to-case, for IGBT	$R_{ heta JC}$	0.42	°C/W
Thermal resistance junction-to-case, for Diode	$R_{ heta JC}$	1.00	°C/W
Thermal resistance junction-to-ambient	$R_{ heta JA}$	40	°C/W

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise specified)

Parameter	Test Conditions	Symbol	Min	Тур	Max	Unit
STATIC CHARACTERISTIC						
Collector–emitter breakdown voltage, gate–emitter short–circuited	$V_{GE} = 0 \text{ V, I}_{C} = 500 \mu\text{A}$	V _{(BR)CES}	600	_	_	V
Collector–emitter saturation voltage	V _{GE} = 15 V, I _C = 60 A V _{GE} = 15 V, I _C = 60 A, T _J = 150°C	V _{CEsat}	- -	2.0 2.6	2.5 -	V
Gate-emitter threshold voltage	V _{GE} = V _{CE} , I _C = 150 μA	V _{GE(th)}	4.5	5.5	6.5	V
Collector–emitter cut–off current, gate– emitter short–circuited	V _{GE} = 0 V, V _{CE} = 600 V V _{GE} = 0 V, V _{CE} = 600 V, T _J = 150°C	I _{CES}	- -	_ _	0.2 2	mA
Gate leakage current, collector–emitter short–circuited	V _{GE} = 20 V , V _{CE} = 0 V	I _{GES}	-	_	200	nA
DYNAMIC CHARACTERISTIC						
Input capacitance		C _{ies}	-	4112	_	pF
Output capacitance	$V_{CE} = 20 \text{ V}, V_{GE} = 0 \text{ V}, f = 1 \text{ MHz}$	C _{oes}	-	169	_	1
Reverse transfer capacitance	1	C _{res}	-	107	_	1
Gate charge total		Q_g		173		nC
Gate to emitter charge	$V_{CE} = 480 \text{ V}, I_{C} = 60 \text{ A}, V_{GE} = 15 \text{ V}$	Q _{ge}		38		1
Gate to collector charge	1	Q _{gc}		87		
SWITCHING CHARACTERISTIC, INDUC	TIVE LOAD	•		•		•
Turn-on delay time		t _{d(on)}		87		ns
Rise time	1	t _r		48		
Turn-off delay time	$T_J = 25^{\circ}C$ $V_{CC} = 400 \text{ V, } I_C = 60 \text{ A}$	t _{d(off)}		180		
Fall time	$R_g = 10 \Omega$ $V_{GE} = 0 \text{ V/ } 15 \text{ V}$	t _f		70		
Turn-off switching loss	VGE = 0 V/ 13 V	E _{off}		0.60		mJ
Turn-on switching loss	1	Eon		1.41		
Turn-on delay time		t _{d(on)}		85		ns
Rise time	1	t _r		50		
Turn-off delay time	$T_J = 150$ °C $V_{CC} = 400 \text{ V, } I_C = 60 \text{ A}$	t _{d(off)}		186		
Fall time	$R_g = 10 \Omega$ $V_{GE} = 0 \text{ V} / 15 \text{ V}$	t _f		91		
Turn-off switching loss	VGE = 0 V/ 15 V	E _{off}		1.11		mJ
Turn-on switching loss	1	E _{on}		1.77		1
DIODE CHARACTERISTIC	•	•				
Forward voltage	V _{GE} = 0 V, I _F = 30 A V _{GE} = 0 V, I _F = 30 A, T _J = 150°C	V _F		1.98 2.10	2.30	V
Reverse recovery time	T _J = 25°C	t _{rr}		76		ns
Reverse recovery charge	$I_F = 30 \text{ Å}, V_R = 200 \text{ V}$ $di_F/dt = 200 \text{ A/}\mu\text{s}$	Q _{rr}		291		nc
Reverse recovery current	1	I _{rrm}		7		Α

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

TYPICAL CHARACTERISTICS

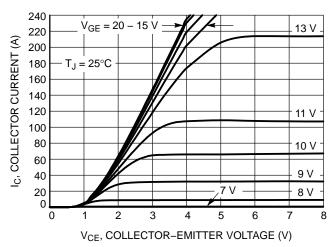


Figure 1. Output Characteristics

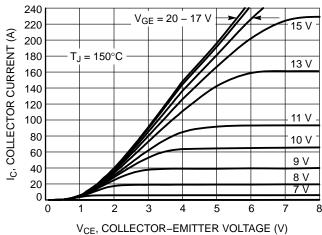


Figure 2. Output Characteristics

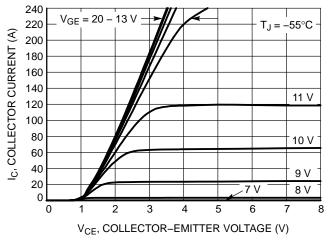


Figure 3. Output Characteristics

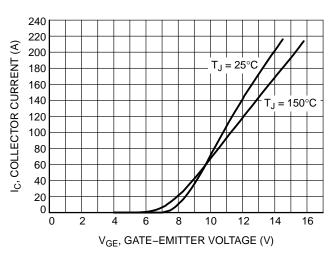


Figure 4. Typical Transfer Characteristics

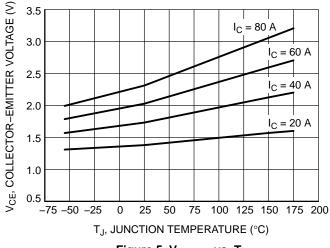


Figure 5. V_{CE(sat)} vs. T_J

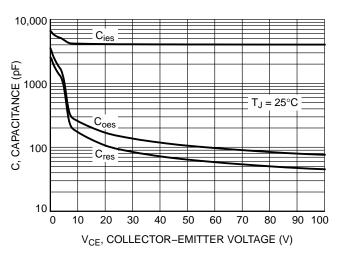
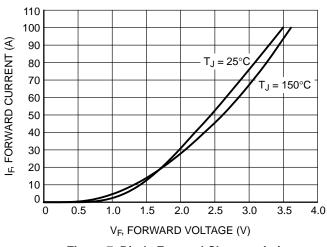


Figure 6. Typical Capacitance

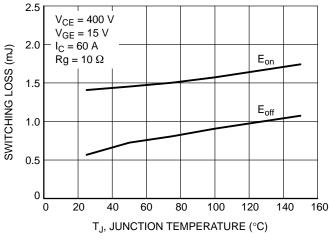
TYPICAL CHARACTERISTICS



20 V_{GE}, GATE-EMITTER VOLTAGE (V) 18 16 14 12 10 8 V_{CE} = 480 V 6 V_{GE} = 15 V $I_{C} = 60 \text{ A}$ 2 0 20 100 120 140 160 180 200 0 40 80 QG, GATE CHARGE (nC)

Figure 7. Diode Forward Characteristics

Figure 8. Typical Gate Charge



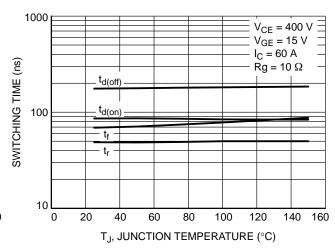
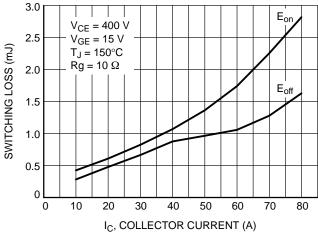


Figure 9. Switching Loss vs. Temperature

Figure 10. Switching Time vs. Temperature



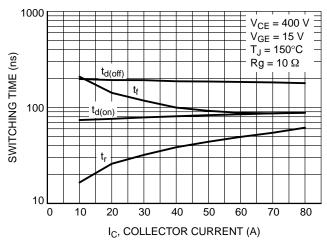
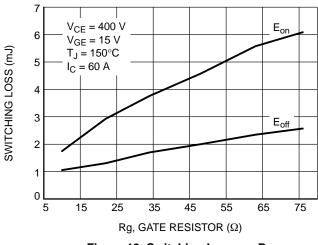


Figure 11. Switching Loss vs. I_C

Figure 12. Switching Time vs. I_C

TYPICAL CHARACTERISTICS

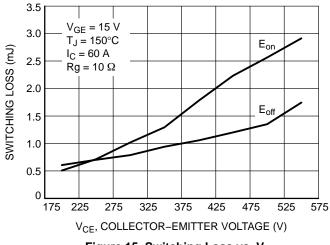
1000



 $\mathsf{t}_{\mathsf{d}(\mathsf{off})}$ SWITCHING TIME (ns) t_{d(on)} 100 $V_{CE} = 400 \text{ V}$ $V_{GE} = 15 \text{ V}$ $T_{J} = 150^{\circ}C$ I_C = 60 A 10 15 35 65 75 5 25 45 55 85 Rg, GATE RESISTOR (Ω)

Figure 13. Switching Loss vs. Rg

Figure 14. Switching Time vs. Rg



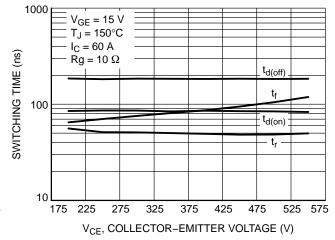
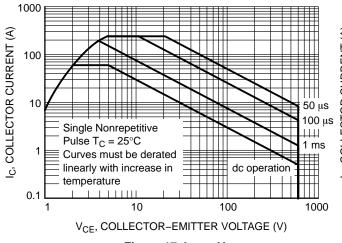


Figure 15. Switching Loss vs. V_{CE}

Figure 16. Switching Time vs. V_{CE}



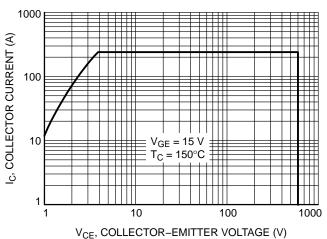


Figure 17. I_C vs. V_{CE}

Figure 18. I_C vs. V_{CE}

TYPICAL CHARACTERISTICS

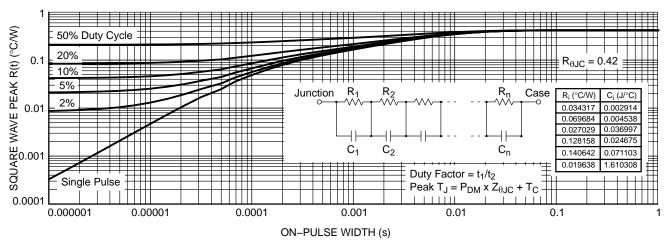


Figure 19. IGBT Transient Thermal Impedance

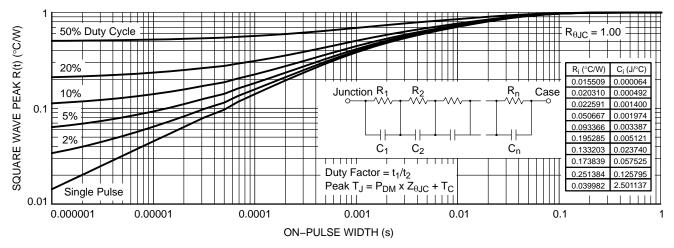
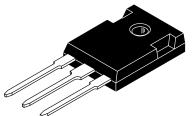


Figure 20. Diode Transient Thermal Impedance





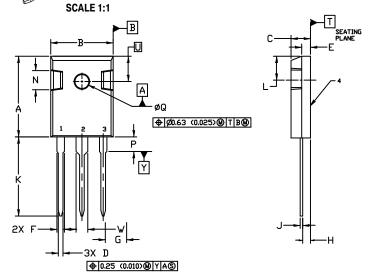
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DATE 06 OCT 2021

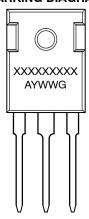
NOTES

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: MILLIMETER

	MILLIMETERS		INC	INCHES	
DIM	MIN.	MAX.	MIN.	MAX.	
Α	20.32	21.08	0.800	0.830	
В	15.75	16.26	0.620	0.640	
С	4.70	5.30	0.185	0.209	
D	1.00	1.40	0.040	0.055	
Ε	1.90	2.60	0.075	0.102	
F	1.65	2.13	0.065	0.084	
G	5.45 BSC		0.215 BSC		
Н	1.50	2.49	0.059	0.098	
J	0.40	0.80	0.016	0.031	
К	19.81	20.83	0.780	0.820	
L	5.40	6.20	0.212	0.244	
N	4.32	5.49	0.170	0.216	
Р		4.50		0.177	
Q	3.55	3.65	0.140	0.144	
U	6.15 BSC		0.242	BSC	
W	2.87	3.12	0.113	0.123	



GENERIC MARKING DIAGRAM*



STYLE 1: PIN 1. GATE 2. DRAIN 3. SOURCE 4. DRAIN

> PIN 1. CATHODE 2. ANODE

STYLE 5:

STYLE 2: PIN 1. ANODE 2. CATHODE (S) 3. ANODE 2 4. CATHODES (S)

PIN 1. MAIN TERMINAL 1 2. MAIN TERMINAL 2

STYLE 6:

STYLE 3:
PIN 1. BASE
2. COLLECTOR
3. EMITTER
4. COLLECTOR

STYLE 4:
PIN 1. GATE
2. COLLECTOR
3. EMITTER
4. COLLECTOR

XXXXX = Specific Device Code A = Assembly Location

Y = Year
WW = Work Week
G = Pb-Free Package

2. ANODE
2. MAIN TERMINAL 2
3. GATE
4. ANODE
4. MAIN TERMINAL 2
4. MAIN TERMINAL 2
5. GATE
6. MAIN TERMINAL 2
6. MAIN TERMINAL 2
7. MAIN TERMINAL 2
7. MAIN TERMINAL 2
8. MAIN TERMINAL 2
8. MAIN TERMINAL 2
9. MAIN TERMINAL

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