October 2015



# KSC388 NPN Epitaxial Silicon Transistor

### Features

- TV Final Picture IF Amplifier Applications
- G<sub>PE</sub> = 33 dB (Typical) at f = 45 MHz
- Suffix "-C" means Center Collector (1. Emitter 2. Collector 3. Base)
- Non Suffix "-C" means Side Collector (1. Emitter 2. Base 3. Collector)



Straight Lead Ben Bulk Packing Tape Ammo

Bent Lead Tape & Reel Ammo Packing

### **Ordering Information**

Part Number	Part Number Top Mark		Packing Method	
KSC388CYTA C388		TO-92 3L	Ammo	

# **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter	Value	Unit
V <sub>CBO</sub>	Collector-Base Voltage	30	V
V <sub>CEO</sub>	Collector-Emitter Voltage	25	V
V <sub>EBO</sub>	Emitter-Base Voltage	4	V
Ι <sub>C</sub>	Collector Current	50	mA
T <sub>J</sub> Junction Temperature		150	°C
T <sub>STG</sub>	Storage Temperature	-55 to 150	°C

# Thermal Characteristics<sup>(1)</sup>

Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Value	Unit
р	Power Dissipation	300	mW
PD	Derate Above 25°C	2.4	mW/°C
R <sub>θJA</sub>	Thermal Resistance, Junction-to-Ambient	416	°C/W

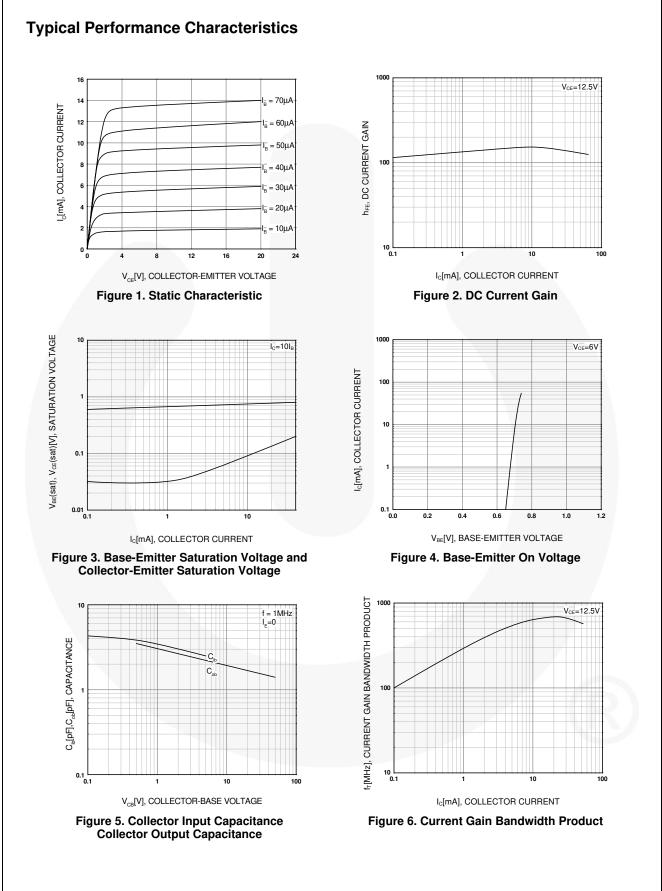
Note:

1. PCB size: FR-4, 76 mm x 114 mm x 1.57 mm (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

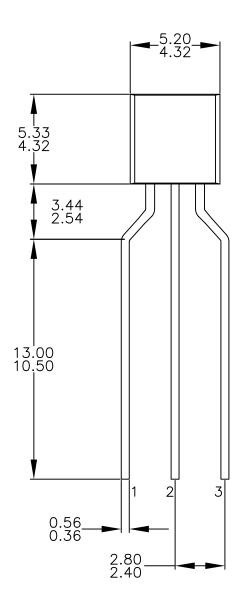
# **Electrical Characteristics**

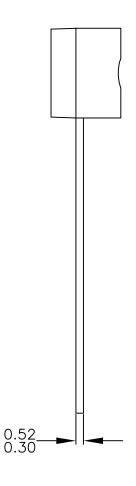
Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>CBO</sub>	Collector-Base Breakdown Voltage	$I_{C} = 10 \ \mu A, \ I_{E} = 0$	30			V
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	$I_{\rm C} = 5 \text{ mA}, I_{\rm B} = 0$	25			V
I <sub>CBO</sub>	Collector Cut-Off Current	$V_{CB} = 30 V, I_{E} = 0$			0.1	μA
I <sub>EBO</sub>	Emitter Cut-Off Current	$V_{EB} = 3 V, I_{C} = 0$			0.1	μA
h <sub>FE</sub>	DC Current Gain	$V_{CE} = 12.5 \text{ V}, I_{C} = 12.5 \text{ mA}$	20		200	
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage	I <sub>C</sub> = 15 mA, I <sub>B</sub> = 1.5 mA			0.2	V
V <sub>BE</sub> (sat)	Base-Emitter Saturation Voltage	I <sub>C</sub> = 15 mA, I <sub>B</sub> = 1.5 mA			1.5	V
C <sub>ob</sub>	Output Capacitance	$V_{CB} = 10 \text{ V}, \text{ I}_{E} = 0,$ f = 1 MHz	0.8		2	pF
C <sub>c·rbb</sub> ´	Collector-Base Time Constant	$V_{CB} = 10 \text{ V}, \text{ I}_{C} = 1 \text{ mA},$ f = 30 MHz			25	ps
f <sub>T</sub>	Current Gain Bandwidth Product	$V_{CE} = 12.5 \text{ V}, I_{C} = 12.5 \text{ mA}$	300			MHz
G <sub>PE</sub>	Power Gain	$V_{CE}$ = 12.5 V, I <sub>C</sub> = 12.5 mA, f = 45 MHz	28	33	36	dB



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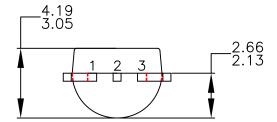


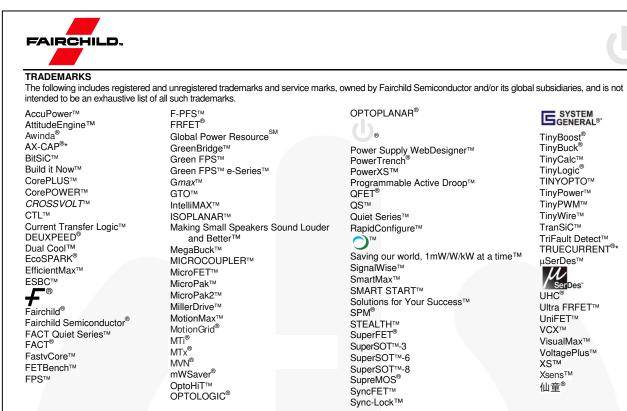


NOTES: UNLESS OTHERWISE SPECIFIED

- DRAWING CONFORMS TO JEDEC MS-013, VARIATION AC. ALL DIMENSIONS ARE IN MILLIMETERS. DRAWING CONFORMS TO ASME Y14.5M-2009. DRAWING FILENAME: MKT-ZA03FREV3. FAIRCHILD SEMICONDUCTOR. Α.

- В. С. D. Е.





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