Amplifier, Power, 8 W 2.5 - 3.5 GHz

Features

- Linear Gain: 27 dB
- Saturated Output Power: +39 dBm Pulsed
- 50 Ω Input / Output Match
- Lead-Free 5 mm 20-lead PQFN Package
- Halogen-Free "Green" Mold Compound
- RoHS* Compliant and 260°C Reflow Compatible

Description

The MAAP-010171 is a 2-stage, 8 W saturated S-band power amplifier in a 5mm 20 lead PQFN package, allowing easy assembly. This product is fully matched to 50 ohms on both the input and output. It can be used as a power amplifier stage or as a driver stage in high power pulsed applications.

It is ideally suited for Air Traffic Control, Weather, Military and S-band radar applications.

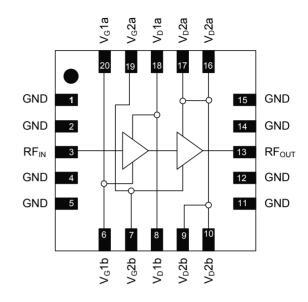
Each device is 100% RF tested to ensure performance compliance.

Ordering Information¹

Part Number	Package	
MAAP-010171-TR0500	500 piece reel	
MAAP-010171-TR1000	1000 piece reel	
MAAP-010171-000SMB	Sample Board	

1. Reference Application Note M513 for reel size information.

Functional Schematic



Pin Configuration²

Pin No.	Function	Pin No.	Function
1	Ground	11	Ground
2	Ground	12	Ground
3	RF _{IN}	13	RF _{OUT}
4	Ground	14	Ground
5	Ground	15	Ground
6	V _G 1b	16	V _D 2a
7	V_{G} 2b	17	V _D 2a
8	$V_{\rm D}$ 1b	18	V _D 1a
9	V _D 2b	19	V _G 2a
10	V _D 2b	20	V _G 1a
		21	Paddle ³

2. MACOM recommends connecting unused package pins to ground.

The exposed pad centered on the package bottom must be connected to RF, DC and thermal ground.

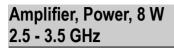
* Restrictions on Hazardous Substances, European Union Directive 2011/65/EU.

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Electrical Specifications: Freq. 2.5 - 3.5 GHz, V_{DD} = 9 V Pulsed, 100 µs Pulse Width, 10% Duty Cycle, Z₀ = 50 Ω

Parameter	Units	Min.	Тур.	Max.
Gain	dB	25	27	_
Input Return Loss	dB	_	10	_
Output Return Loss	dB	_	10	_
P _{SAT}	dBm	37	39	_
Small Signal Current (I _{DD})	А	_	1	_
Efficiency	%	_	38	—

Absolute Maximum Ratings^{4,5}

Parameter	Absolute Maximum		
Input Power	22 dBm		
Supply Voltage	11 V		
Gate Current	25 mA		
Duty Cycle	50 %		
Operating Temperature	-40°C to +85°C		
Junction Temperature ^{6,7}	+150°C		
Storage Temperature	-55°C to +150°C		

4. Exceeding any one or combination of these limits may cause permanent damage to this device.

- 6. Operating at nominal conditions with $T_J \le 150^{\circ}C$ will ensure MTTF > 1 x 10^{6} hours.
- 7. Junction Temperature $(T_J) = T_C + \Theta_{JC} * (V * I)$. Typical thermal resistance $(\Theta_{JC}) = 5.75^{\circ}C/W$

Handling Procedures

Please observe the following precautions to avoid damage:

Static Sensitivity

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these class 1A devices.

MACOM does not recommend sustained operation near these survivability limits.

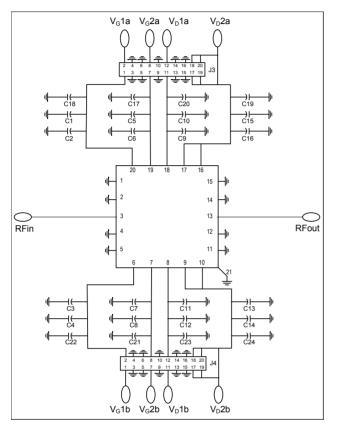
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Schematic



Parts List

Component	Value	Package
C1, C4, C5, C8, C10, C12, C14, C15	1000 pF	0402
C2, C3, C6, C7, C9, C11, C13, C16	100 pF	0402
C17, C18, C21, C22	1 µF	0805
C19, C20, C23, C24	10 nF	0805

V_G2a V_D1a $V_{G}1a$ V_D2a GAP °n °n П n °n°n ίΠ п.п U. 3 II. D C 918 MI 0 U UU C20 C19 C18 C100 00 10 99999999999999999999999999 RFin RFout C120 CB. C24 3 П П П П П п 0 0 0 4 U. U

Recommended PCB Layout

Operating the MAAP-010171

To operate, follow these steps.

1. Apply V_G between -1 V and -0.5 V to set IDQ to 1 A

V_G2b V_D1b

V_D2b

- 2. Apply V_{DD} Pulsed
- 3. Apply RF Power ON

V_G1b

- 4. The RF ports (pins 3 & 13) are not DC blocked. Do not apply DC voltage directly onto these pins.
- 5. Ramp down or shut down in reverse order.

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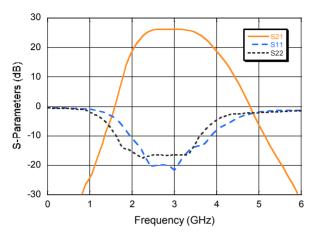
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Vis

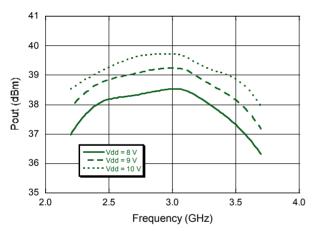
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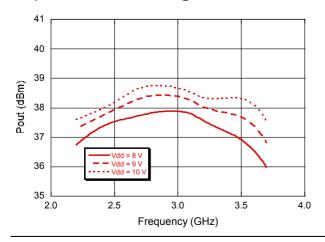
S-Parameters



Output Power, Pin = 19 dBm @ +25°C



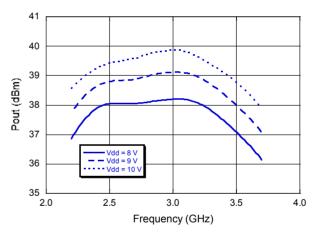
Output Power, Pin = 19 dBm @ +85°C

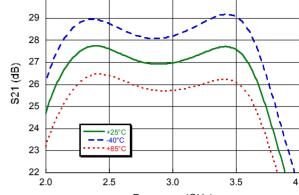


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30 29 28 27 26 25 24 +25°C 23 22 2.0 2.5 3.0 3.5 4.0 Frequency (GHz)

Output Power, Pin = 19 dBm @ -40°C





Small Signal Gain

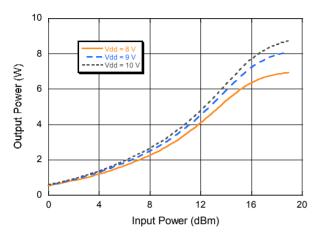
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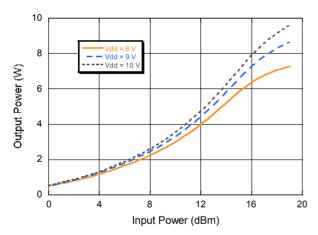
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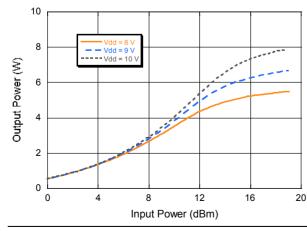
Output Power @ 2.5 GHz



Output Power @ 3.1 GHz



Output Power @ 3.5 GHz

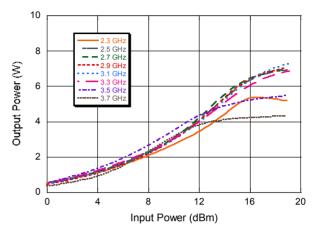


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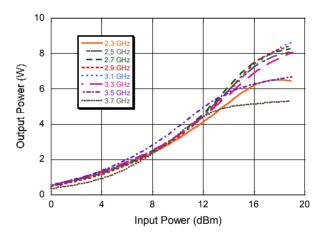


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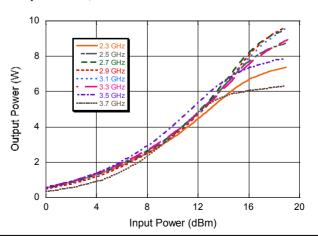
Output Power, V_{DD} = 8 V



Output Power, V_{DD} = 9 V



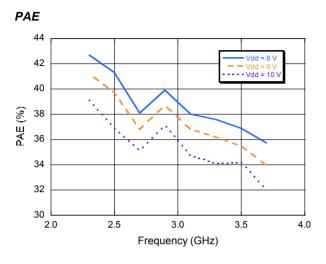
Output Power, $V_{DD} = 10 V$



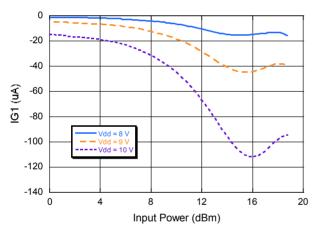
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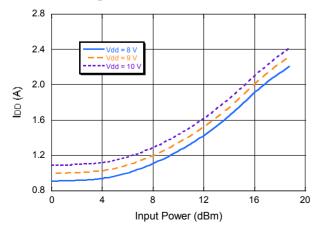
Typical Performance Curves



1st Stage Gate Current @ 2.9 GHz

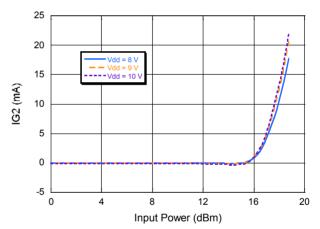


Drain Current @ 2.9 GHz

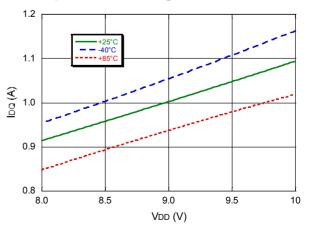


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2nd Stage Gate Current @ 2.9 GHz



Small Signal Drain Current @ 2.9 GHz



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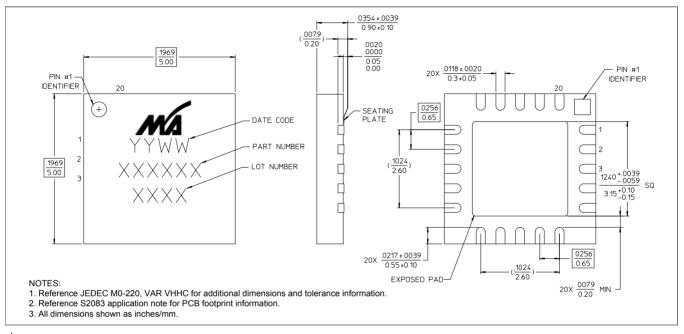




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Lead-Free 5 mm 20-Lead PQFN[†]



[†] Reference Application Note S2083 for lead-free solder reflow recommendations. Meets JEDEC moisture sensitivity level 1 requirements. Plating is 100% matte tin over copper.

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