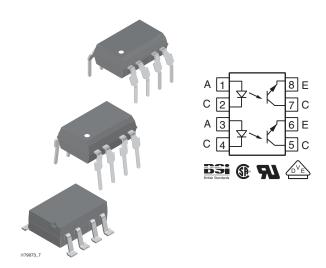


Vishay Semiconductors

Optocoupler, Phototransistor Output, Dual Channel



DESCRIPTION

The ILD610 series is a dual channel optocoupler series for high density applications. Each channel consists of an optically coupled pair with a gallium arsenide infrared LED and silicon NPN phototransistor. Signal information, including a DC level, can be transmitted by the device while maintaining a high degree of electrical isolation between input and output. The ILD610 series is the dual version of SFH610 series and uses a repetitive pin-out configuration instead of the more common alternating pin-out used in most dual couplers.

FEATURES

- Dual version of SFH610 series
- Isolation test voltage, 5300 V_{RMS}
- V_{CEsat} 0.25 (\leq 0.4) V at I_F = 10 mA, I_C = 2.5 mA
- V_{CEO} = 70 V
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC





RoHS COMPLIANT

AGENCY APPROVALS

- UL1577, file no. E52744 system code H or J, double protection
- DIN EN 60747-5-5 (VDE 0884)/DIN EN 60747-5-5 pending
- CSA 93751
- BSI IEC 60950: IEC 60065

ORDERING INFORMATION			
		DIP-#	Option 6
I L D 6 1 0 PART NUMBER	- # X 0 0 # 1 CTR PACKAGE OPTION TAI AN RE	PE Option 7	10.16 mm Option 9

AGENCY CERTIFIED/PACKAGE	CTR (%)				
UL, CSA, BSI	40 to 80	63 to 125	100 to 200	160 to 320	
DIP-8	ILD610-1	-	ILD610-3	-	
DIP-8, 400 mil, option 6	-	-	ILD610-3X006	-	
SMD-8, option 7	-	ILD610-2X007T	-	-	
SMD-8, option 9	-	-	ILD610-3X009	ILD610-4X009	

Note

• Additional options may be possible, please contact sales office.

Vishay Semiconductors Optocoupler, Phototransistor Output, Dual Channel



ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
INPUT	·						
Reverse voltage		V_{R}	6.0	V			
Surge forward current	t ≤ 1.0 ms	I _{FSM}	1.5	Α			
Power dissipation		P _{diss}	100	mW			
Derate linearly from 25 °C			1.3	mW/°C			
Forward continuous current		I _F	60	mA			
OUTPUT							
Collector emitter voltage		V_{CE}	70	V			
Collector current		I _C	50	mA			
	t ≤ 1.0 ms	I _C	100	mA			
Power dissipation		P _{diss}	150	mW			
Derate linearly from 25 °C			2.0	mW/°C			
COUPLER							
Isolation test voltage	t = 1.0 s	V _{ISO}	5300	V_{RMS}			
Isolation resistance	V _{IO} = 500 V, T _{amb} = 25 °C	R _{IO}	≥ 10 ¹²	Ω			
isolation resistance	V _{IO} = 500 V, T _{amb} = 100 °C	R _{IO}	≥ 10 ¹¹	Ω			
Storage temperature		T _{stg}	- 55 to + 150	°C			
Operating temperature		T _{amb}	- 55 to + 100	°C			
Junction temperature		T _j	100	°C			
Lead soldering time at 260 °C			10	s			

Note

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT							
Forward voltage	I _F = 60 mA		V_{F}		1.25	1.65	V
Reverse current	V _R = 6.0 V		I _R		0.01	10	μΑ
Capacitance	$V_R = 0 V, f = 1.0 MHz$		Co		25		pF
OUTPUT							
Collector emitter breakdown	I _C = 10 mA, I _E = 10 μA		BV _{CEO}	70	90		V
voltage			BV _{CEO}	6.0	7.0		V
Collector emitter dark current	V _{CE} = 10 V		I _{CEO}		2.0	50	nA
Collector emitter capacitance	$V_{CE} = 5.0 \text{ V}, f = 1.0 \text{ MHz}$		C _{CE}		7.0		pF
Collector emitter leakage current	V _{CE} = 10 V	ILD610-1	I _{CEO}		2.0	50	nA
		ILD610-2	I _{CEO}		2.0	50	nA
		ILD610-3	I _{CEO}		5.0	100	nA
		ILD610-4	I _{CEO}		5.0	100	nA
COUPLER							
Collector emitter saturation voltage	I _F = 10 mA, I _C = 2.5 mA		V _{CEsat}		0.25	0.40	V
Coupling capacitance			C _C		0.35		pF

Note

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering
evaluation. Typical values are for information only and are not part of the testing requirements.



Optocoupler, Phototransistor Output, Dual Vishay Semiconductors Channel

CURRENT TRANSFER RATIO (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
CTR (1)	I _F = 10 mA, V _{CE} = 5.0 V	ILD610-1	CTR	40		80	%
		ILD610-2	CTR	63		125	%
		ILD610-3	CTR	100		200	%
		ILD610-4	CTR	160		320	%
	$I_{F} = 1.0 \text{ mA}, V_{CE} = 5.0 \text{ V}$	ILD610-1	CTR	13			%
		ILD610-2	CTR	22			%
		ILD610-3	CTR	34			%
		ILD610-4	CTR	56			%

Note

(1) CTR will match within a ratio of 1.7:1

PARAMETER	RACTERISTICS (T _{amb} = 25 °C, unle	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
NON-SATURATED	TEST CONDITION	FANI	STMBOL	WIIIN.	IIF.	IVIAA.	ONT
NON-SATURATED		ILD610-1			2.0		
	V 50V D 750	ILD610-1	-		2.5		
Rise time	$V_{CC} = 5.0 \text{ V}, R_L = 75 \Omega,$ $I_F = 10 \text{ mA}$	ILD610-2	- t _r		2.9		μs
	1	ILD610-3			3.3		
		ILD610-1			2.0		
	V 50V D 750	ILD610-1			2.6		
Fall time	$V_{CC} = 5.0 \text{ V}, R_L = 75 \Omega,$ $I_F = 10 \text{ mA}$	ILD610-2	t _f		3.1		μs
	1	ILD610-3	-		3.5		
		ILD610-4			3.0		
	V 50V D 750	ILD610-1			3.2		
Turn-on time	$V_{CC} = 5.0 \text{ V}, R_L = 75 \Omega,$ $I_F = 10 \text{ mA}$	ILD610-3	t _{on}		3.6		μs
	1	ILD610-4			4.1		
Turn-off time		ILD610-1			2.9		μs
	V 50V D 750	ILD610-2	t _{off}		3.4		
	$V_{CC} = 5.0 \text{ V}, R_L = 75 \Omega,$ $I_F = 10 \text{ mA}$	ILD610-3			3.7		
		ILD610-4	-		4.1		
SATURATED		ILBOTO T					
-	$V_{CC} = 5.0 \text{ V}, R_L = 1.0 \text{ k}\Omega, I_F = 20 \text{ mA}$	ILD610-1			2.0		
	$V_{CC} = 5.0 \text{ V}, R_L = 1.0 \text{ k}\Omega, I_F = 10 \text{ mA}$	ILD610-2			2.8		
Rise time	$V_{CC} = 5.0 \text{ V}, R_L = 1.0 \text{ k}\Omega, I_F = 10 \text{ mA}$	ILD610-3	t _r		2.8		μs
	$V_{CC} = 5.0 \text{ V}, R_L = 1.0 \text{ k}\Omega, I_F = 5 \text{ mA}$	ILD610-4			4.6		
	$V_{CC} = 5.0 \text{ V}, R_L = 1.0 \text{ k}\Omega, I_F = 20 \text{ mA}$	ILD610-1			11		
	$V_{CC} = 5.0 \text{ V}, R_L = 1.0 \text{ k}\Omega, I_F = 10 \text{ mA}$	ILD610-2			14		
Fall time	$V_{CC} = 5.0 \text{ V}, R_L = 1.0 \text{ k}\Omega, I_F = 10 \text{ mA}$	ILD610-3	t _f		14		μs
	$V_{CC} = 5.0 \text{ V}, R_L = 1.0 \text{ k}\Omega, I_F = 5 \text{ mA}$	ILD610-4			15		
	$V_{CC} = 5.0 \text{ V}, R_L = 1.0 \text{ k}\Omega, I_F = 20 \text{ mA}$	ILD610-1			3.0		
	$V_{CC} = 5.0 \text{ V}, R_L = 1.0 \text{ k}\Omega, I_F = 10 \text{ mA}$	ILD610-2			4.3		
Turn-on time	$V_{CC} = 5.0 \text{ V}, R_L = 1.0 \text{ k}\Omega, I_F = 10 \text{ mA}$	ILD610-3	t _{on}		4.3		μs
	$V_{CC} = 5.0 \text{ V}, R_L = 1.0 \text{ k}\Omega, I_F = 5 \text{ mA}$	ILD610-4	1		6.0		
	$V_{CC} = 5.0 \text{ V}, R_L = 1.0 \text{ k}\Omega, I_F = 20 \text{ mA}$	ILD610-1			18		
	$V_{CC} = 5.0 \text{ V}, R_L = 1.0 \text{ k}\Omega, I_F = 10 \text{ mA}$	ILD610-2	1		25		
Turn-off time	$V_{CC} = 5.0 \text{ V}, R_L = 1.0 \text{ k}\Omega, I_F = 10 \text{ mA}$	ILD610-3	t _{off}		25		μs
	$V_{CC} = 5.0 \text{ V}, R_L = 1.0 \text{ k}\Omega, I_F = 5 \text{ mA}$		1		25		

Vishay Semiconductors Optocoupler, Phototransistor Output, Dual Channel



TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

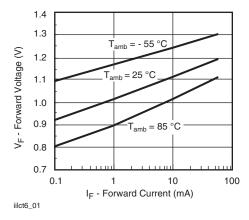


Fig. 1 - Forward Voltage vs. Forward Current

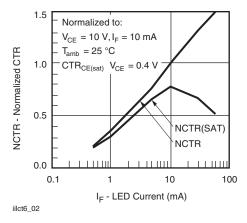


Fig. 2 - Normalized Non-Saturated and Saturated CTR vs. LED Current

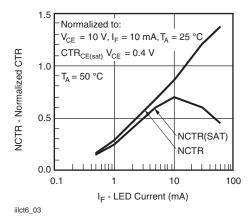


Fig. 3 - Normalized Non-Saturated and Saturated CTR vs. LED Current

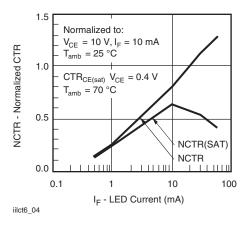


Fig. 4 - Normalized Non-Saturated and Saturated CTR vs. LED Current

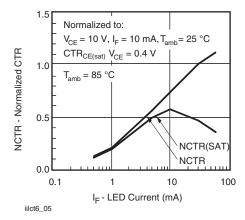


Fig. 5 - Normalized Non-Saturated and Saturated CTR vs. LED Current

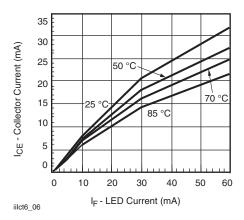


Fig. 6 - Collector Emitter Current vs. Temperature and LED Current



Optocoupler, Phototransistor Output, Dual Vishay Semiconductors Channel

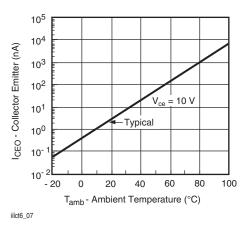


Fig. 7 - Collector Emitter Leakage Current vs.Temperature

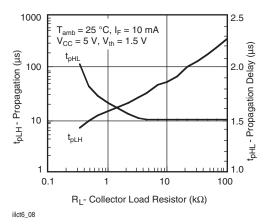


Fig. 8 - Propagation Delay vs. Collector Load Resistor

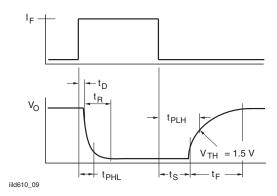


Fig. 9 - Switching Timing

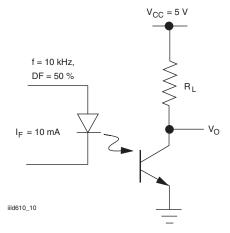


Fig. 10 - Non-Saturated Switching Schematic

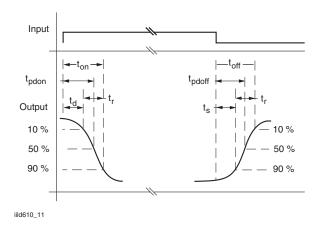
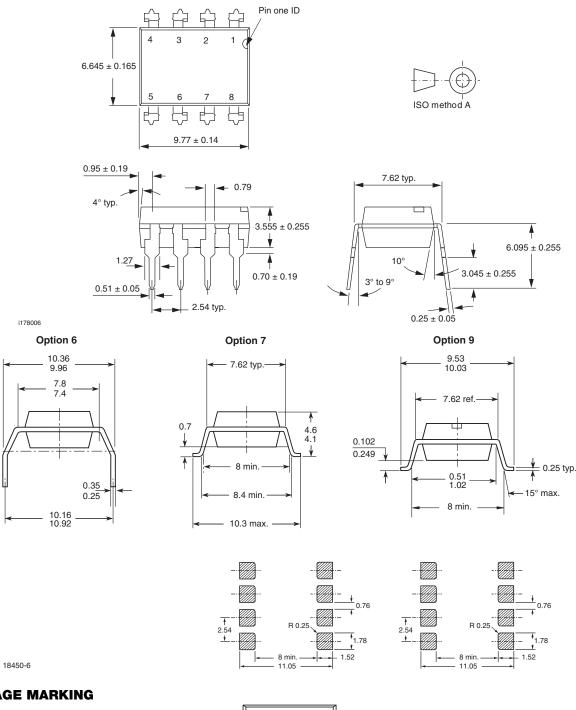


Fig. 11 - Saturated Switching Time Test Waveform

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PACKAGE DIMENSIONS in millimeters



PACKAGE MARKING



Notes

- Only option 1 and 7 reflected in the package marking
- Tape and reel suffix (T) is not part of the package marking



Legal Disclaimer Notice

Vishay

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