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Power MOSFET -40 V, 10.4 mΩ, -77 A, P-Channel

Automotive Power MOSFET designed for compact and efficient designs and including high thermal performance.

AEC-Q101 qualified MOSFET and PPAP capable suitable for automotive applications.

Features

- Low On-Resistance
- High Current Capability
- 100% Avalanche Tested
- AEC-Q101 qualified and PPAP capable
- ATPAK package is pin-compatible with DPAK (TO-252)
- Pb-Free, Halogen Free and RoHS compliance

Typical Applications

- Reverse Battery Protection
- Load Switch
- Automotive Front Lighting
- Automotive Body Controllers

SPECIFICATIONS

ABSOLUTE MAXIMUM RATING at Ta = 25°C (Note 1)

Parameter	Symbol	Value	Unit
Drain to Source Voltage	VDSS	-40	V
Gate to Source Voltage	VGSS	±20	V
Drain Current (DC)	ID	-77	А
Drain Current (Pulse) PW \leq 10 μ s, duty cycle \leq 1%	IDP	-231	А
Power Dissipation Tc = 25°C	PD	72	W
Operating Junction and Storage Temperature	Tj, Tstg	-55 to +175	°C
Avalanche Energy (Single Pulse) (Note 2)	EAS	95	mJ
Avalanche Current (Note 3)	IAV	-35	А

Note 1 : Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

2 : V_{DD} = -15 V, L = 100 μ H, IAV = -35 A

3 : L \leq 100 μ H, Single pulse

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Value	Unit
Junction to Case Steady State (Tc = 25°C)	$R_{\theta JC}$	2.0	°C/W
Junction to Ambient (Note 4)	R _θ JA	79.6	°C/W

Note 4 : Surface mounted on FR4 board using a 130 mm², 1 oz. Cu pad.

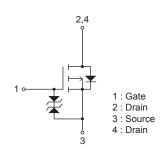


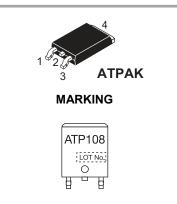
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R _{DS} (on) Max	ID Max
10.4 mΩ @ −10 V	
16.5 mΩ @ –4.5 V	–77 A
	10.4 mΩ @ -10 V

ELECTRICAL CONNECTION P-Channel





ORDERING INFORMATION

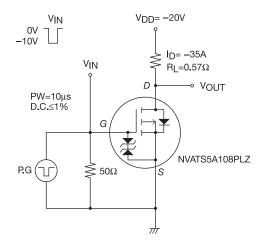
See detailed ordering and shipping information on page 6 of this data sheet.

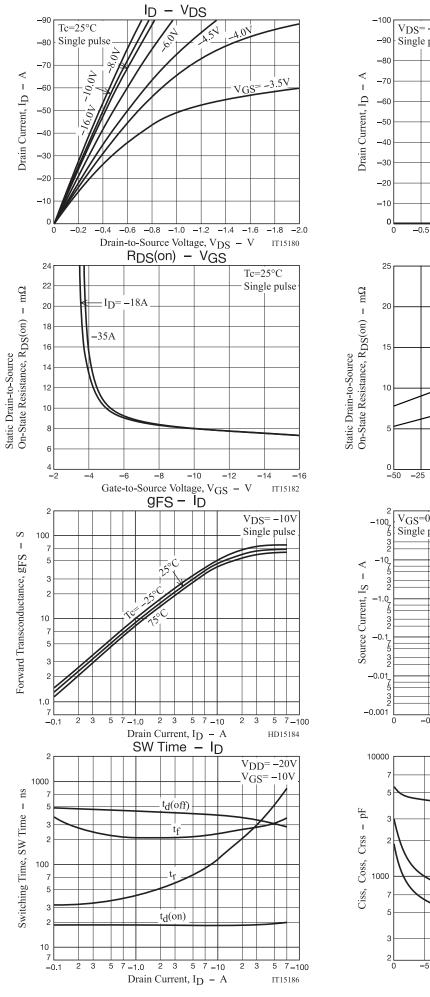
ELECTRICAL CHARACTERISTICS at $Ta = 25^{\circ}C$ (Note 5)

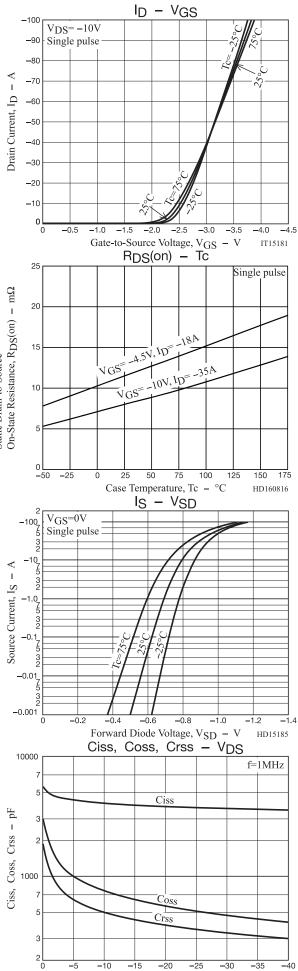
Devenueter	Cumuland	Conditions	Value			1.1
Parameter	Symbol Conditions		min	typ	max	Unit
Drain to Source Breakdown Voltage	V(BR)DSS	ID = -1 mA, VGS = 0 V	-40			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} = -40 V, V _{GS} = 0 V			-1	μA
Gate to Source Leakage Current	IGSS	V_{GS} = ±16 V, V_{DS} = 0 V			±10	μA
Gate Threshold Voltage	VGS(th)	$V_{DS} = -10 \text{ V}, \text{ I}_{D} = -1 \text{ mA}$	-1.2		-2.6	V
Forward Transconductance	9FS	V _{DS} = -10 V, I _D = -35 A		65		S
Static Drain to Source On-State	R _{DS} (on)1	ID = -35 A, VGS = -10 V		8	10.4	mΩ
Resistance	R _{DS} (on)2	I _D = -18 A, V _{GS} = -4.5 V		11.5	16.5	mΩ
Input Capacitance	Ciss			3,850		pF
Output Capacitance	Coss	V _{DS} = -20 V, f = 1 MHz		560		pF
Reverse Transfer Capacitance	Crss			390		pF
Turn-ON Delay Time	t _d (on)			19		ns
Rise Time	tr			340		ns
Turn-OFF Delay Time	t _d (off)	See Fig.1		340		ns
Fall Time	tf			290		ns
Total Gate Charge	Qg	V _{DS} = –20 V, V _{GS} = –10 V, I _D = –70 A		79.5		nC
Gate to Source Charge	Qgs			20		nC
Gate to Drain "Miller" Charge	Qgd			15		nC
Forward Diode Voltage	V _{SD}	Is = -70 A, V _{GS} = 0 V		-1.05	-1.5	V

Note 5 : Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

Fig.1 Switching Time Test Circuit



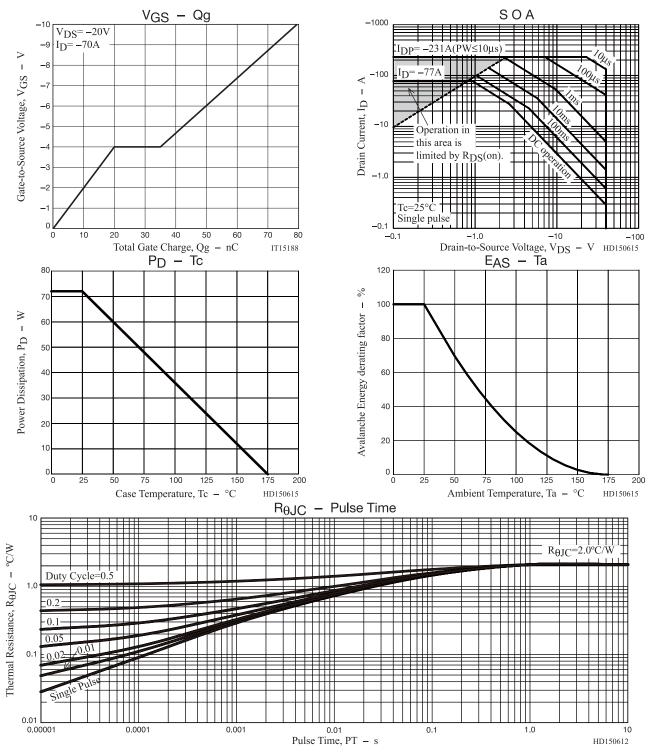




Drain-to-Source Voltage, $V_{DS} - V$

IT15187

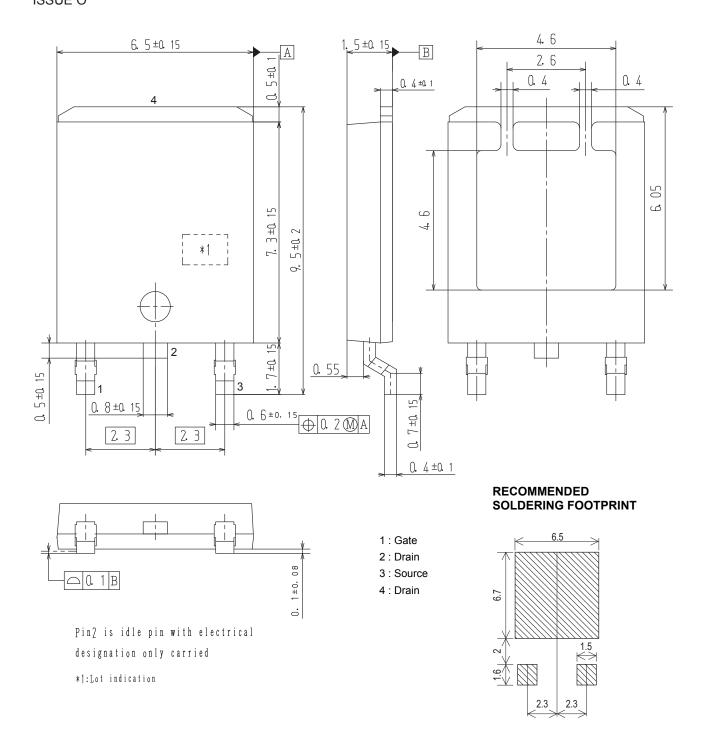
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PACKAGE DIMENSIONS unit : mm

DPAK (Single Gauge) / ATPAK

CASE 369AM ISSUE O



ORDERING INFORMATION

	Device	Marking	Package	Shipping (Qty / Packing)	
	NVATS5A108PLZT4G	ATP108	DPAK(Single Gauge) / ATPAK (Pb-Free / Halogen Free)	3,000 / Tape & Reel	

† For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D. http://www.onsemi.com/pub_link/Collateral/BRD8011-D.PDF

Note on usage : Since the NVATS5A108PLZ is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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