DC-DC Converter (-30V, -2.5A)

RSQ025P03

Features

- 1) Low On-resistance.(120m Ω at 4.5V)
- 2) High Power Package.(PD=1.25W)
- 3) High speed switching.
- 4) Low voltage drive.(4V)

Applications

DC-DC converter

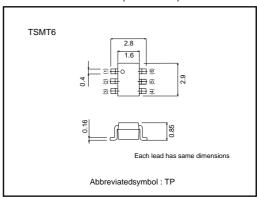
●Structure

Silicon P-channel **MOSFET**

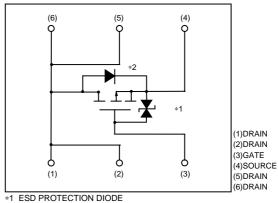
Packaging specifications

	Package	Taping
Туре	Code	TR
	Basic ordering unit (pieces)	3000
RSQ025P03	0	

●External dimensions (Units : mm)



■Equivalent circuit



- *2 BODY DIODE

● Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit	
Drain-source voltage		Voss	-30	V	
Gate-source voltage		Vgss	±20	V	
Drain current	Continuous	ΙD	±2.5	A	
	Pulsed	IDP	±10	A *1	
Source current (Body diode)	Continuous	Is	-1	A	
	Pulsed	Isp	-4	A *1	
Total power dissipation		Po	1.25	W*2	
Channel temperature		Tch	150	°C	
Range of Storage temperature		Tstg	-55~+150	°C	

^{*1} Pw≦10μs, Duty cycle≦1% *2 Mounted on a ceramic board

● Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Gate-source leakage	Igss	_	-	±10	μΑ	Vgs=±20V, Vps=0V	
Drain-source breakdown voltage	V _{(BR)DSS}	-30	_	1	V	ID=-1mA, VGS=0V	
Zero gate voltage drain current	IDSS	_	_	-1	μΑ	VDS=-30V, VGS=0V	
Gate threshold voltage	VGS(th)	-1.0	_	-2.5	V	VDS=-10V, ID=-1mA	
Static drain-source on-state resistance	RDS(on)	_	80	110	mΩ	In=-2.5A, Vgs=-10V	
		_	120	165	mΩ	ID=-1.25A, VGS=-4.5V	
		_	145	200	mΩ	ID=-1.25A, VGS=-4.0V	
Foward transfer admittance	$ Y_{fs} ^*$	1.2	_	ı	S	VDS=-10V, ID=-1.25A	
Input capacitance	Ciss	_	320	-	pF		
Output capacitance	Coss	_	85	-	pF	Vps=-10V,Vgs=0V f=1MHz	
Reverse transfer capacitance	Crss	_	60	ı	pF		
Turn-on delay time	td(on) *	-	8	1	ns	Ip=-1.25A	
Rise time	tr *	_	11	-	ns	V _{DD} ≒−15V	
Turn-off delay time	td(off) *	-	33	ı	ns	V _{GS} =-4.5V R _L =12Ω	
Fall time	t _f *	_	7	ı	ns	Rgs=10Ω	
Total gate charge	Qg	_	4.4	-	nC	V . 45V	
Gate-source charge	Qgs	_	1.0	_	nC	V _{DD} =−15V V _{GS} =−5V	
Gate-drain charge	Qgd	_	1.4	1	nC	ID=-2.5A	

Forward voltage VSD -	-	-	-1.2	٧	Is=-0.9A, Vgs=0V
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Electrical characteristic curves

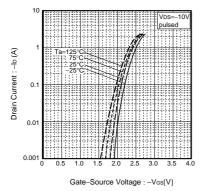


Fig.1 Typical Transfer Characteristics

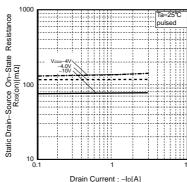


Fig.2 Static Drain–Source On–State Resistance vs. Drain Current

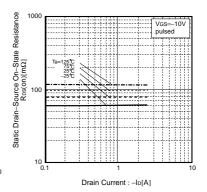


Fig.3 Static Drain–Source On–State Resistance vs.Drain Current

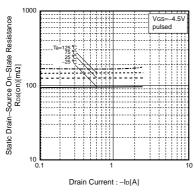


Fig.4 Static Drain–Source On–State Resistance vs. Drain–Current

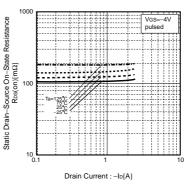


Fig.5 Static Drain–Source On–State Resistance vs.Drain–Current

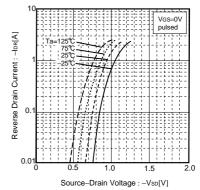


Fig.6 Reverse Drain Current Source-Drain Voltage

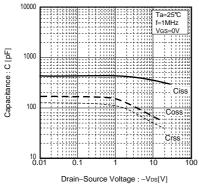


Fig.7 Typical Capactitance vs.Drain-Source Voltage

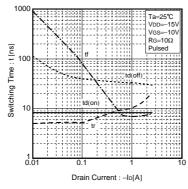
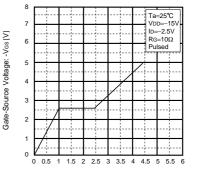


Fig.8 Switching Characteristics



Total Gate Charge : Qg[nC]
Fig.9 Dynamic Input Characteristics

Measurement circuits

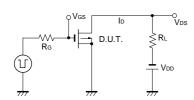


Fig.10 Switching Time Measurement Circuit

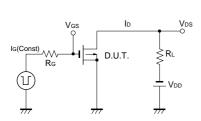


Fig.12 Gate Charge Measurement Circuit

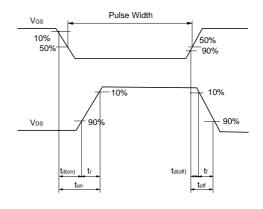


Fig.11 Switching Waveforms

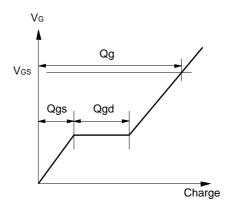


Fig.13 Gate Charge Waveforms

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