

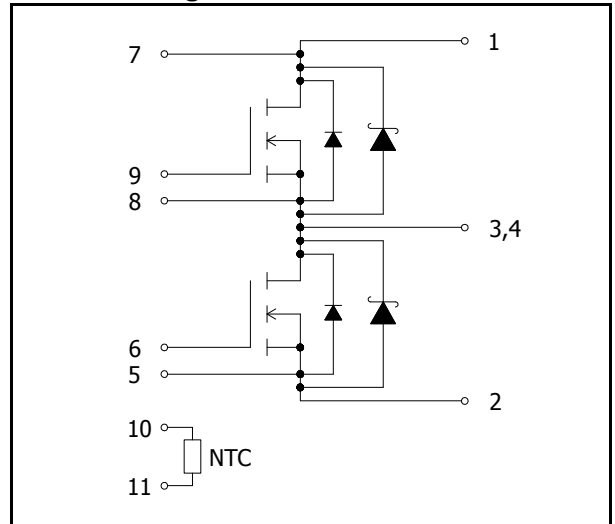
●Application

- Motor drive
- Inverter, Converter
- Photovoltaics, wind power generation.
- Induction heating equipment.

●Features

- 1) Low surge, low switching loss.
- 2) High-speed switching possible.
- 3) Reduced temperature dependence.

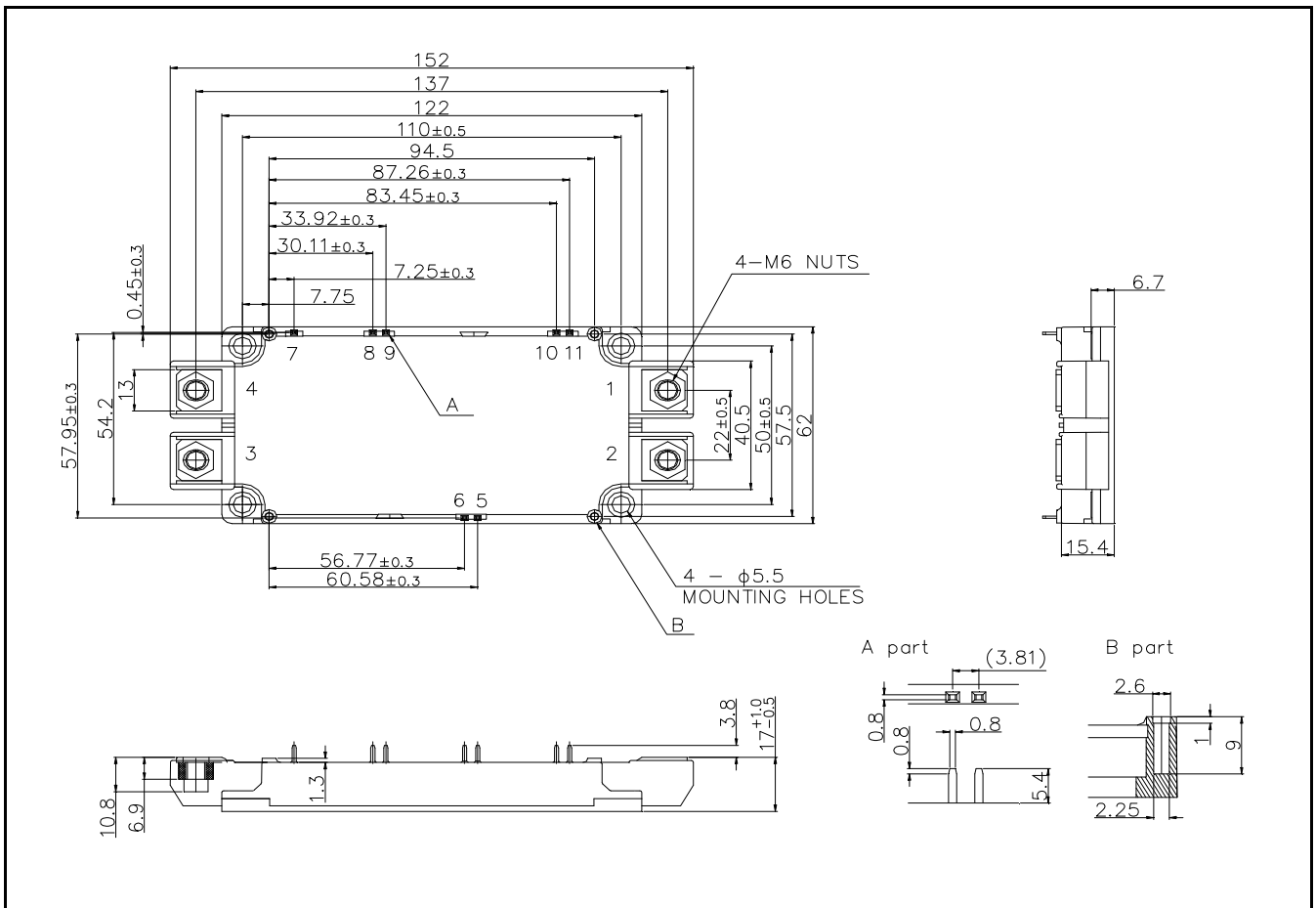
●Circuit diagram



●Construction

This product is a half bridge module consisting of SiC-UMOSFET and SiC-SBD from ROHM.

●Dimensions & Pin layout (Unit : mm)



●Absolute maximum ratings ($T_j = 25^\circ\text{C}$)

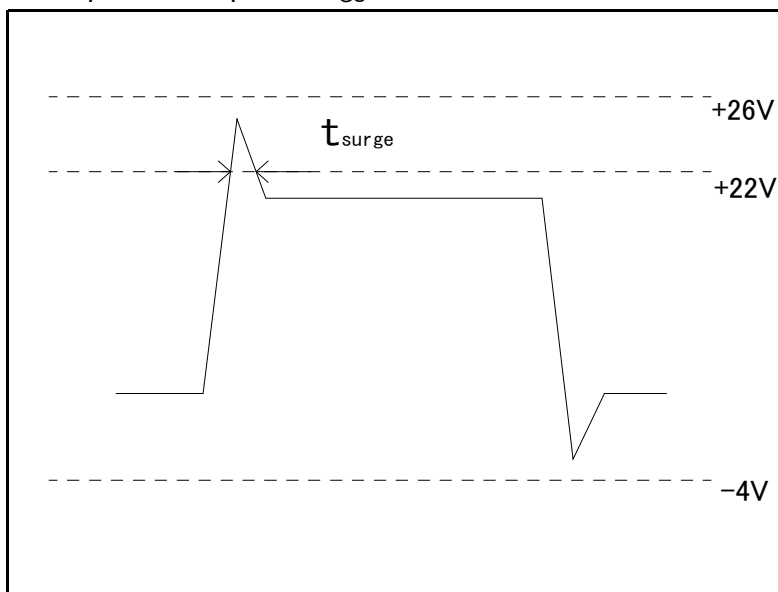
Parameter	Symbol	Conditions	Ratings	Unit
Drain - Source Voltage	V_{DSS}	G-S short	1200	V
Gate - Source Voltage (+)	V_{GSS}	D-S short	22	
Gate - Source Voltage (-)	V_{GSS}	D-S short	-4	
G - S Voltage ($t_{surge} < 300\text{nsec}$)	$V_{GSSsurge}$	D-S short	-4 to 26	
Drain Current <small>Note 1)</small>	I_D	DC($T_c=60^\circ\text{C}$) $V_{GS}=18\text{V}$	294	A
	I_D	DC($T_c=55^\circ\text{C}$) $V_{GS}=18\text{V}$	300	
	I_{DRM}	Pulse ($T_c = 60^\circ\text{C}$) 1ms $V_{GS}=18\text{V}$ <small>Note 2)</small>	600	
Source Current <small>Note 1)</small>	I_S	DC($T_c=60^\circ\text{C}$) $V_{GS}=18\text{V}$	294	
	I_S	DC($T_c=55^\circ\text{C}$) $V_{GS}=18\text{V}$	300	
	I_S	DC($T_c=60^\circ\text{C}$) $V_{GS}=0\text{V}$	218	
	I_{SRM}	Pulse ($T_c = 60^\circ\text{C}$) 1ms $V_{GS}=18\text{V}$ <small>Note 2)</small>	600	
	I_{SRM}	Pulse ($T_c = 60^\circ\text{C}$) 10us $V_{GS}=0\text{V}$ <small>Note 2)</small>	600	
Total Power Dissipation <small>Note 3)</small>	P_{tot}	$T_c = 25^\circ\text{C}$	1260	W
Max Junction Temperature	T_{jmax}		175	$^\circ\text{C}$
Junction Temperature	T_{jop}		-40 to 150	
Storage Temperature	T_{stg}		-40 to 125	
Isolation Voltage	$Visol$	Terminals to baseplate $f = 60\text{Hz AC}$ 1 min.	2500	Vrms
Mounting Torque	-	Main Terminals : M6 screw	4.5	N · m
		Mounting to heat sink M5 screw	3.5	

Note 1) Case temperature (T_c) is defined on the surface of base plate just under the chips.

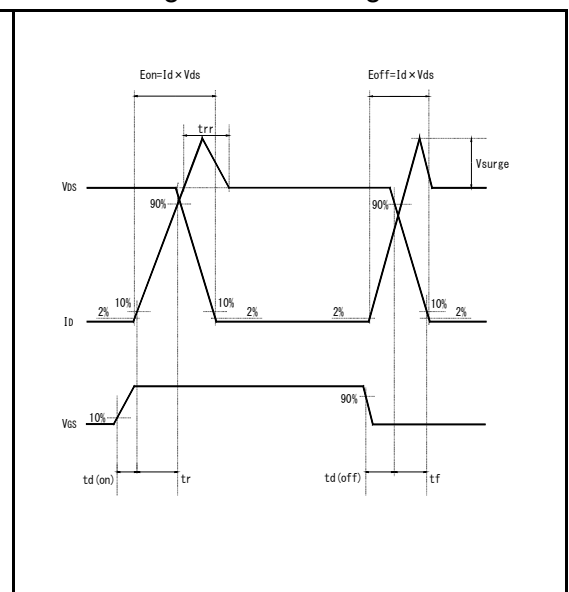
Note 2) Repetition rate should be kept within the range where temperature rise if die should not exceed T_{jmax} .

Note 3) T_j is less than 175°C .

Example of acceptable V_{GS} waveform



<Wavelength for Switching Test>



●Electrical characteristics (T_j=25°C)

Parameter	Symbol	Conditions	Ratings			Unit	
			Min.	Typ.	Max.		
On-state static Drain-Source Voltage	V _{DS(on)}	I _D =300A, V _{GS} =18V	T _j =25°C	—	1.7	2.4	V
			T _j =125°C	—	2.5	—	
			T _j =150°C	—	2.8	4.1	
Drain Cutoff Current	I _{DSS}	V _{DS} =1200V, V _{GS} =0V	—	—	2	mA	
Souce-Drain Voltage	V _{SD}	V _{GS} =0V, I _S =300A	T _j =25°C	—	2.0	2.5	V
			T _j =125°C	—	2.6	—	
			T _j =150°C	—	2.8	4.6	
		V _{GS} =18V, I _S =300A	T _j =25°C	—	1.3	—	
			T _j =125°C	—	1.7	—	
			T _j =150°C	—	1.9	—	
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} =10V, I _D =91mA	2.7	—	5.6	V	
Gate-Source Leak Current	I _{GSS}	V _{GS} =22V, V _{DS} =0V	—	—	0.5	μA	
		V _{GS} =-4V, V _{DS} =0V	-0.5	—	—		
Switching Characteristics	td(on)	V _{GS(on)} =18V, V _{GS(off)} =-2V <small>Note 4)</small> V _{DS} =600V I _D =300A R _{G(on)} =2.2 ohm, R _{G(off)} =1.8 ohm Inductive load	—	30	—	ns	
	tr		—	35	—		
	trr		—	55	—		
	td(off)		—	210	—		
	tf		—	50	—		
Input Capacitance	C _{iss}	V _{DS} =10V, V _{GS} =0V, 200kHz	—	14	—	nF	
Gate Registance	R _{Gint}	T _j =25°C	—	2.8	—	Ω	
NTC Rated Resistance	R ₂₅		—	5.0	—	kΩ	
NTC B Value	B _{50/25}		—	3370	—	K	
Stray Inductance	L _s		—	14.0	—	nH	
Creepage Distance	-	Terminal to heat sink	—	14.5	—	mm	
		Terminal to terminal	—	15.0	—	mm	
Clearance Distance	-	Terminal to heat sink	—	12.0	—	mm	
		Terminal to terminal	—	9.0	—	mm	
Junction-to -Case Thermal Resistance	R _{th(j-c)}	UMOSFET (1/2 module) <small>Note 5)</small>	—	—	0.12	°C/W	
		SBD (1/2 module) <small>Note 5)</small>	—	—	0.16		
Case-to -heat sink Thermal Resistance	R _{th(c-f)}	Case to heat sink, per 1 module. Thermal grease applied. <small>Note 6)</small>	—	0.035	—		

Note 4) In order to prevent self turn-on, it is recommended to apply negative gate bias.

Note 5) Measurement of T_c is to be done at the point just under the chip.

Note 6) Typical value is measured by using thermally conductive grease of λ=0.9W/(m·K).

Note 7) SiC devices have lower short cuicuit withstand capability due to high current density. Please be advised to pay careful attention to short cuicuit accident and try to adjust protection time to shutdown them as short as possible.

Note 8) If the Product is used beyond absolute maximum ratings defined in the Specifications, as its internal structure may be damaged, please replace such Product with a new one.

●Electrical characteristic curves (Typical)

Fig.1 Output characteristic 25°C (TYP)

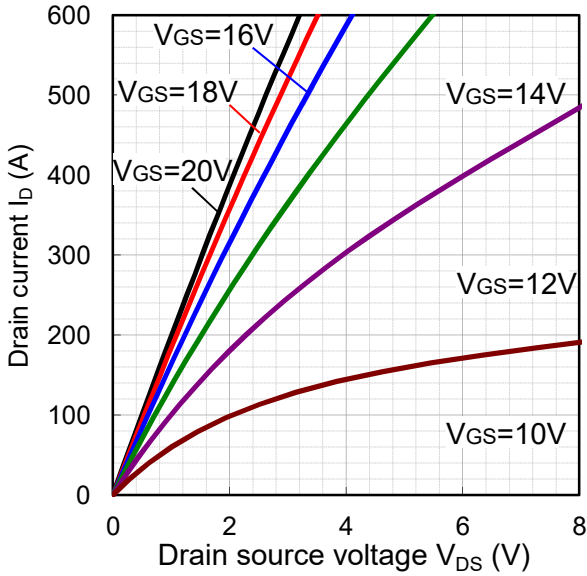


Fig.2 Drain source voltage characteristic (TYP)

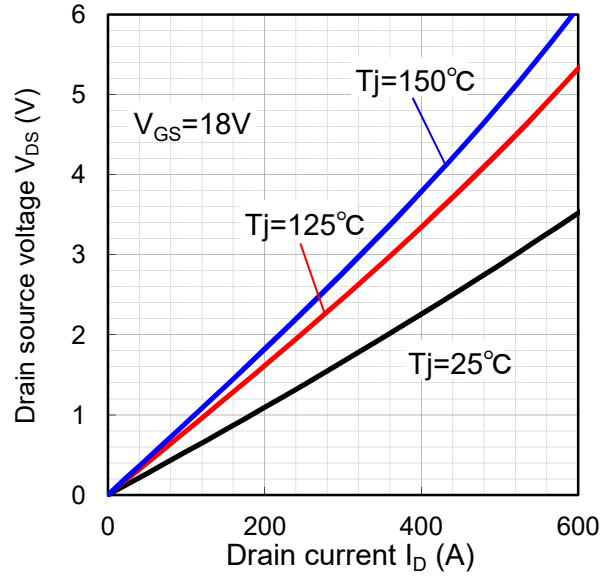


Fig.3 Drain source voltage characteristic 25°C (TYP)

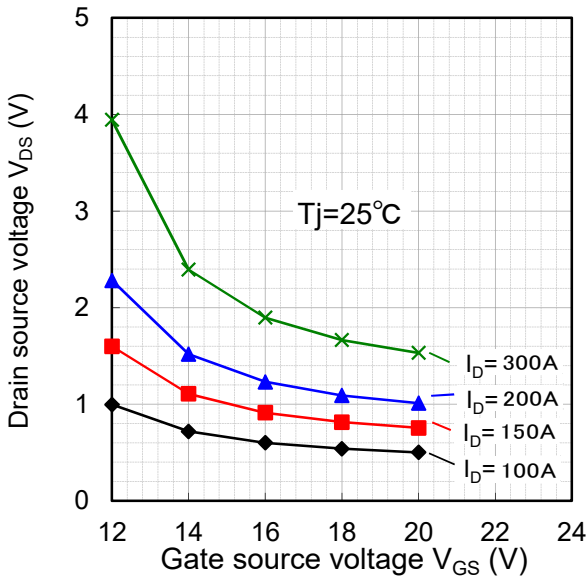
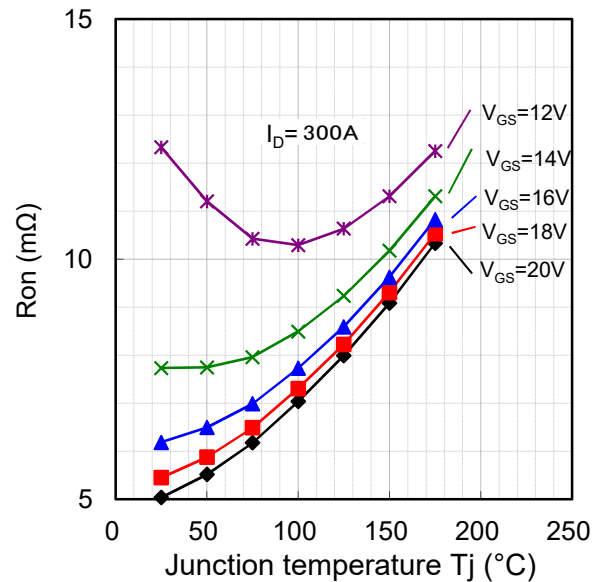


Fig.4 Ron vs Tj characteristic (TYP)



●Electrical characteristic curves (Typical)

Fig.5 Forward characteristic of Diode (TYP)

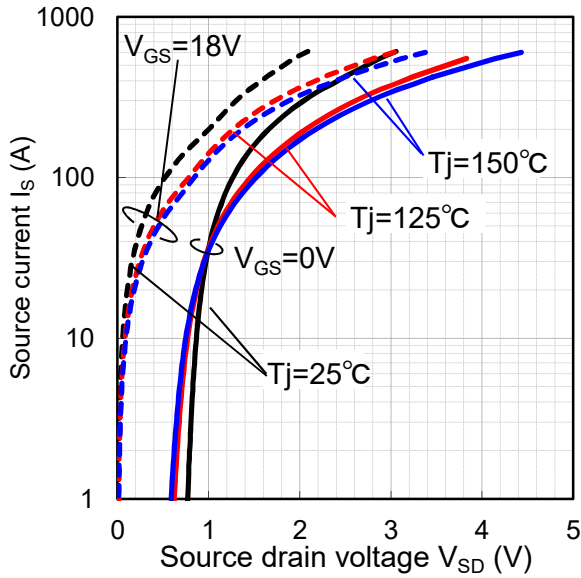


Fig.6 Forward characteristic of Diode (TYP)

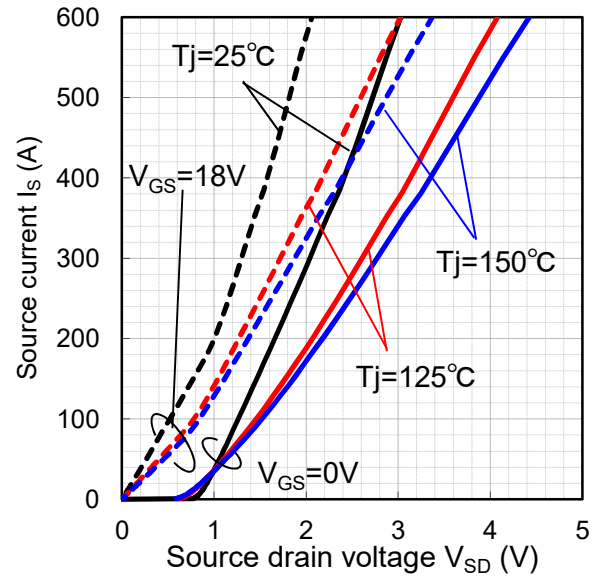


Fig.7 Drain Current vs Gate Voltage (TYP)

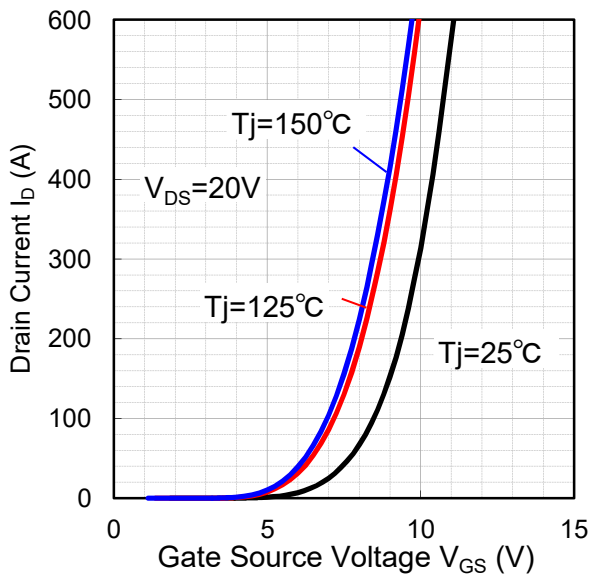
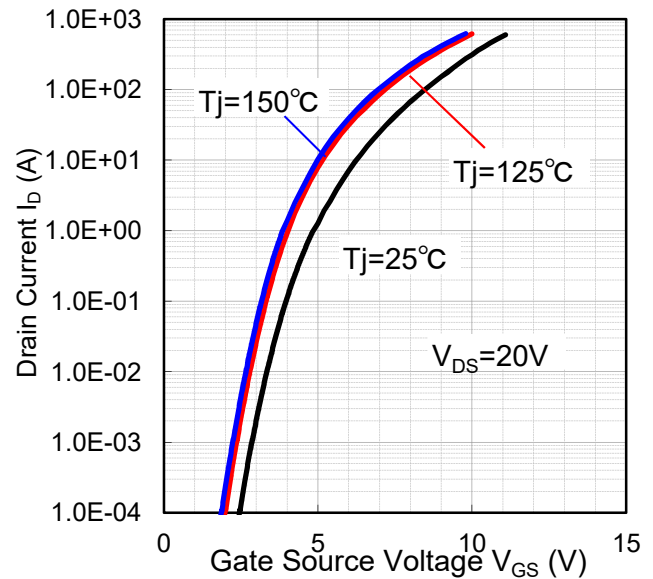


Fig.8 Drain Current vs Gate Voltage (TYP)



●Electrical characteristic curves (Typical)

Fig.9 Switching time vs drain current at 25°C (TYP)

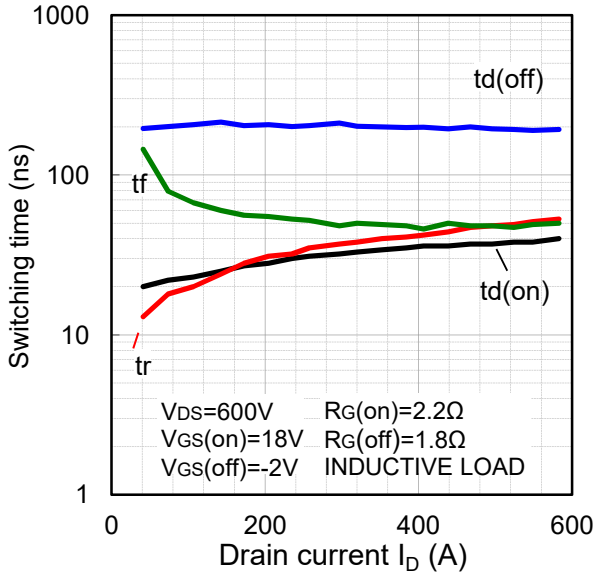


Fig.10 Switching time vs drain current at 125°C (TYP)

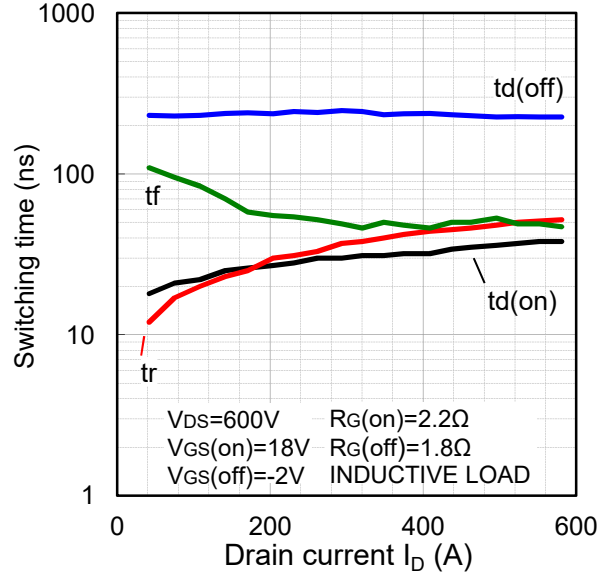


Fig.11 Switching time vs drain current at 150°C (TYP)

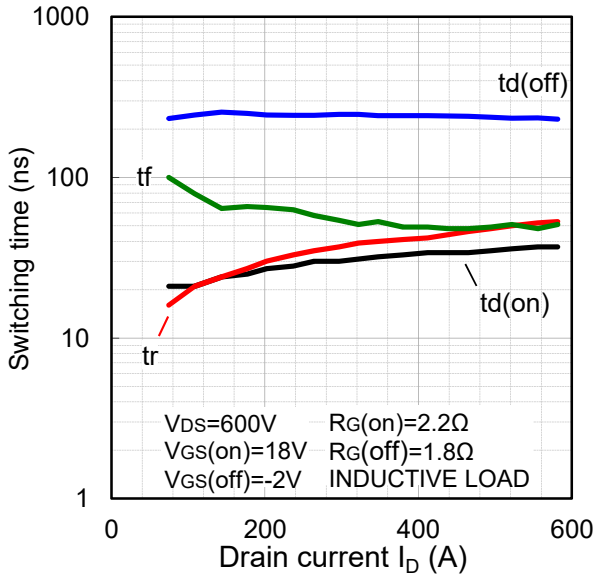
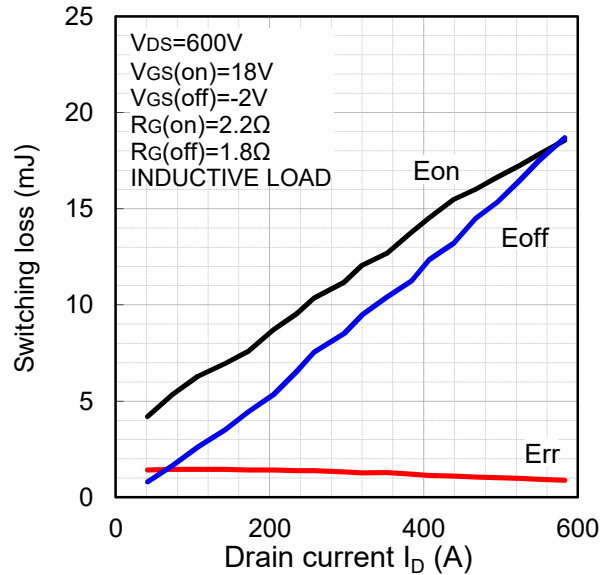


Fig.12 Switching loss vs drain current at 25°C (TYP)



●Electrical characteristic curves (Typical)

Fig.13 Switching loss vs drain current at 125°C (TYP)

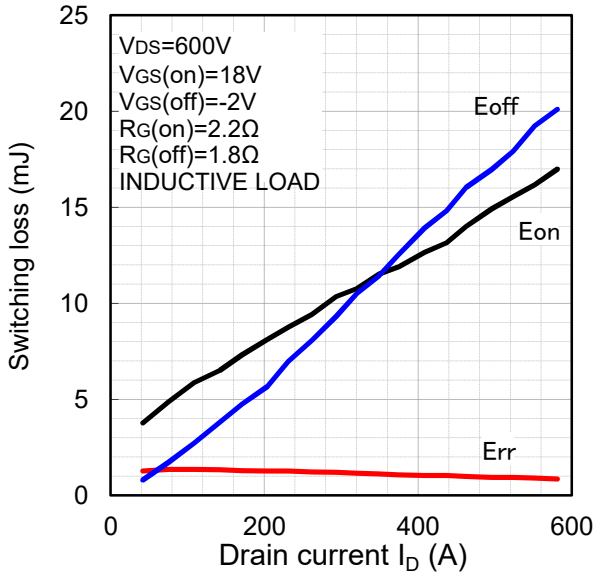


Fig.14 Switching loss vs drain current at 150°C (TYP)

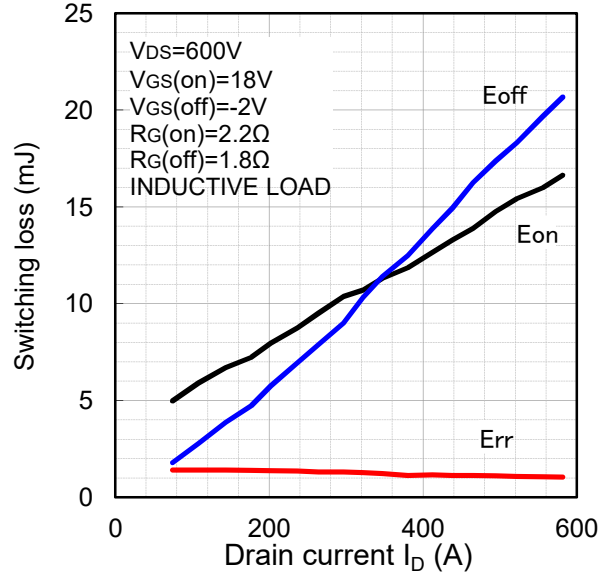


Fig.15 Recovery characteristic vs drain current at 25°C (TYP)

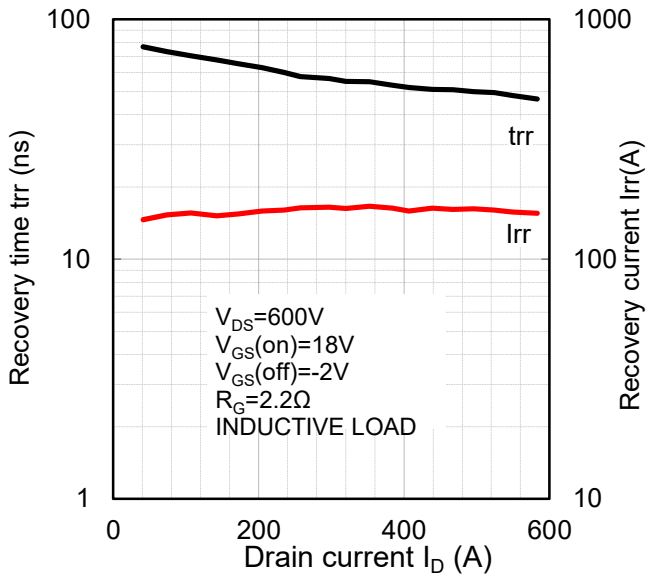
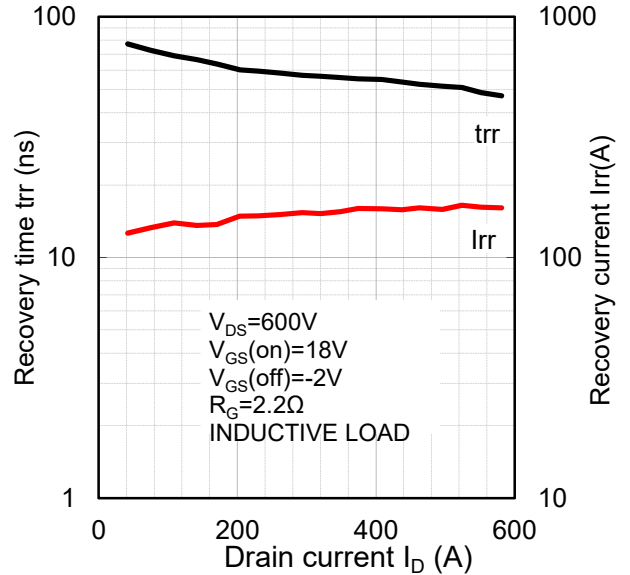


Fig.16 Recovery characteristic vs drain current at 125°C (TYP)



●Electrical characteristic curves (Typical)

Fig.17 Recovery characteristic vs drain current at 150°C (TYP)

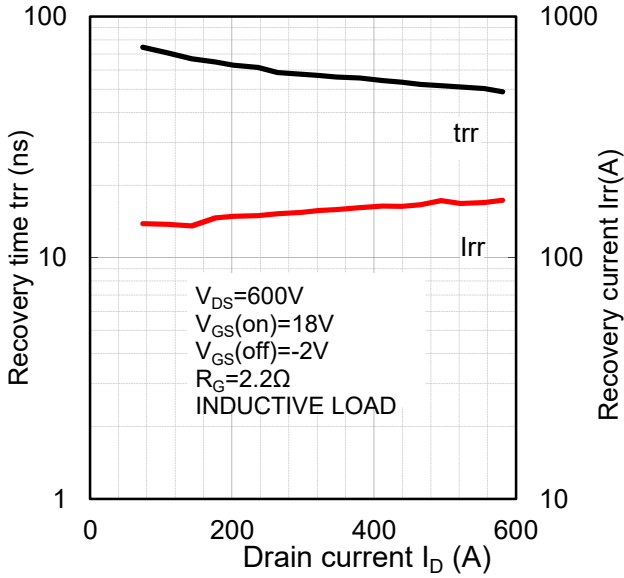


Fig.18 Switching time vs gate resistance at 25°C (TYP)

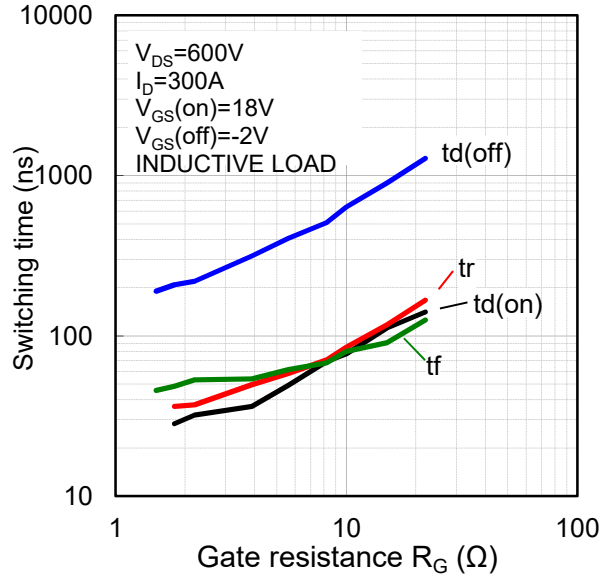


Fig.19 Switching time vs gate resistance at 125°C (TYP)

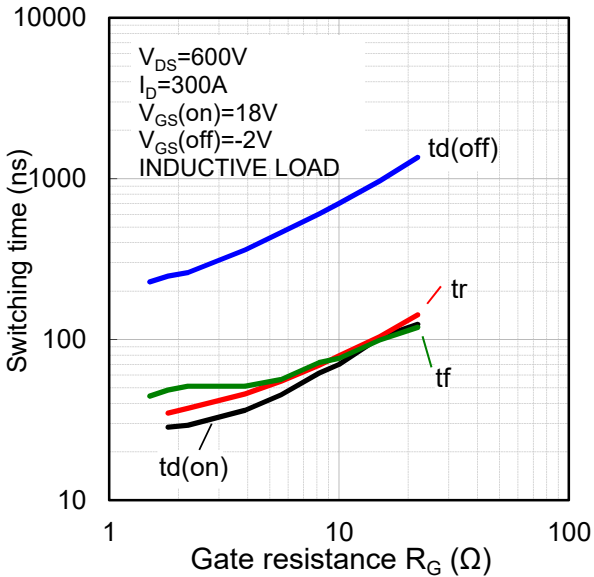
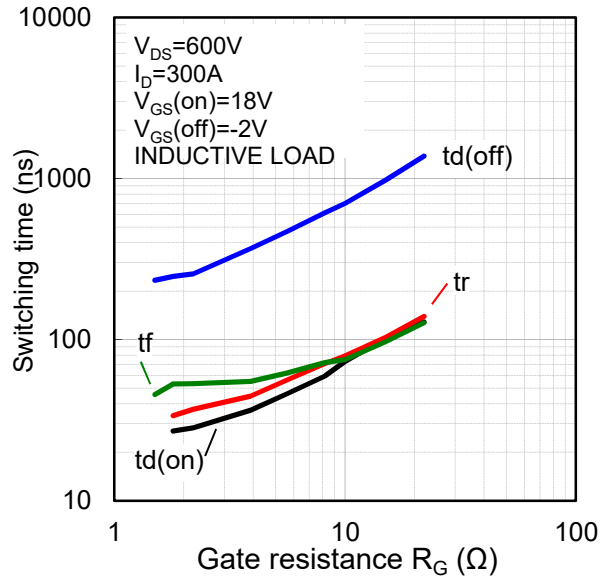


Fig.20 Switching time vs gate resistance at 150°C (TYP)



●Electrical characteristic curves (Typical)

Fig.21 Switching loss vs gate resistance at 25°C (TYP)

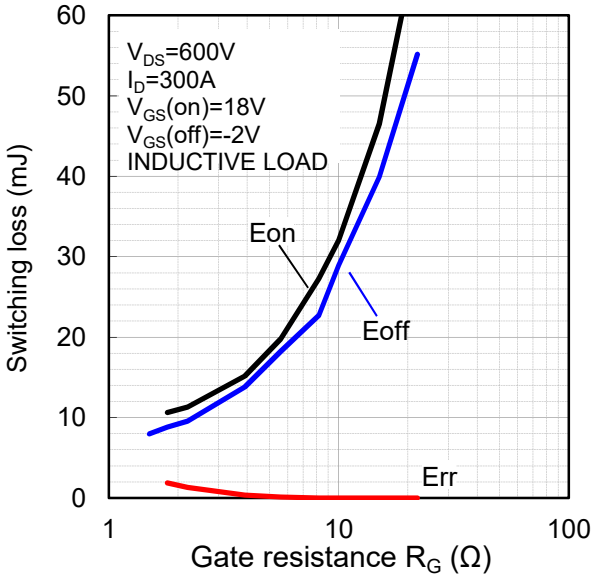


Fig.22 Switching loss vs gate resistance at 125°C (TYP)

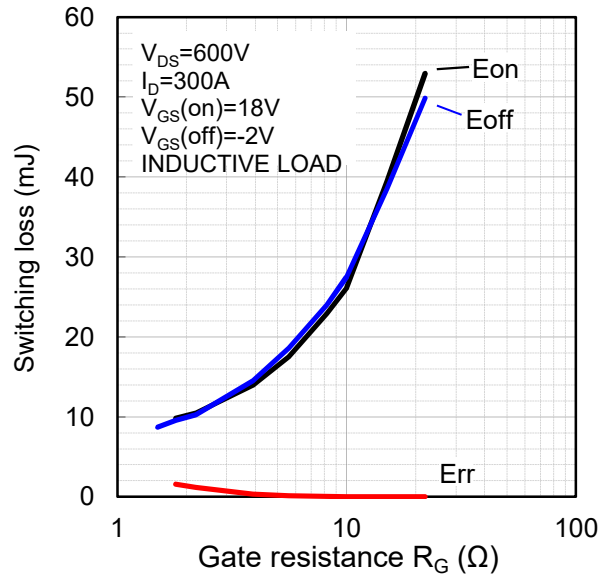
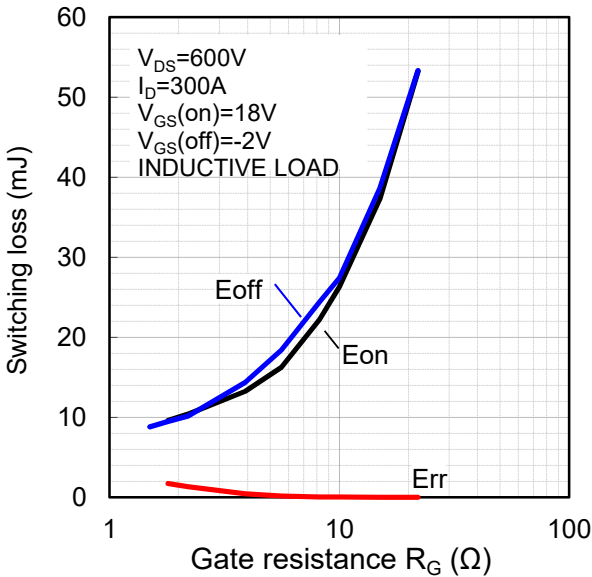


Fig.23 Switching loss vs gate resistance at 150°C (TYP)



●Electrical characteristic curves (Typical)

Fig.24 Capacitance vs Drain source voltage (TYP)

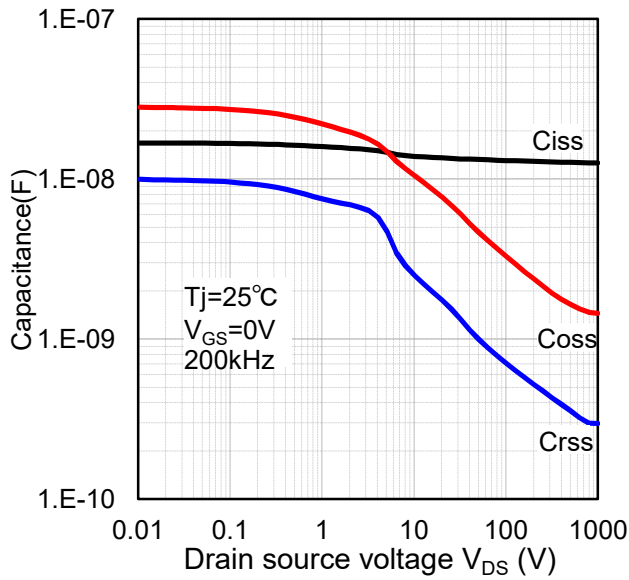


Fig.25 Gate charge characteristic (TYP)

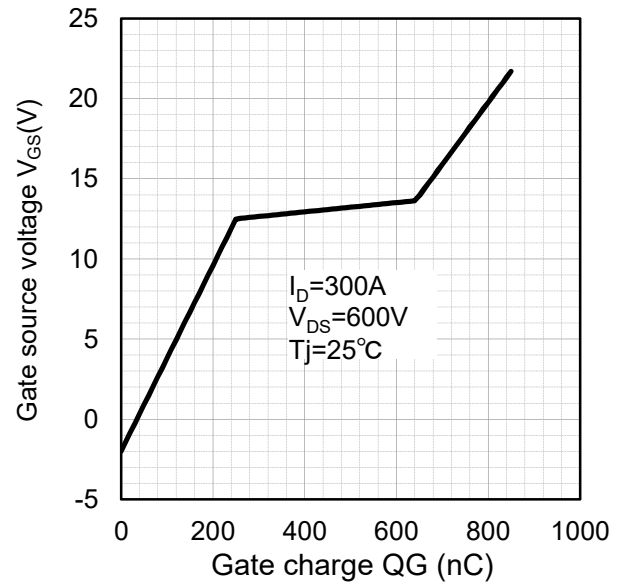
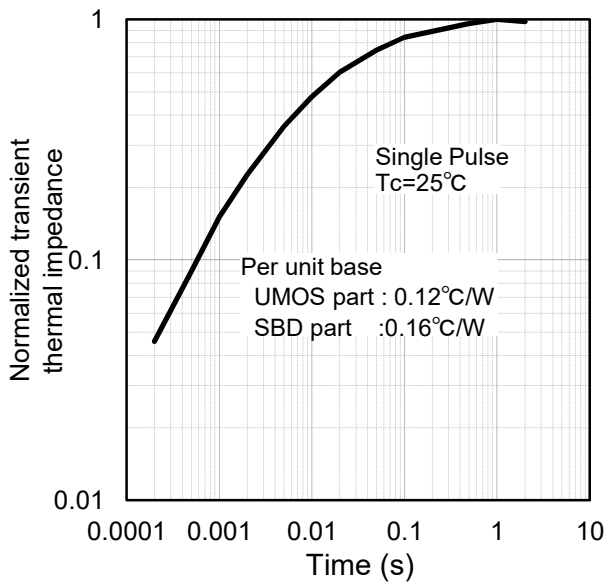


Fig.26 Transient thermal impedance (TYP)



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