

REF_5BR2280BZ_22W1

About this document

Scope and purpose

This document is a reference design for a 22 W auxiliary power supply for a residential air-conditioner unit with the latest fifth-generation Infineon fixed-frequency (FF) CoolSET[™] ICE5BR2280BZ. The power supply is designed with a universal input compatible with most geographic regions and three outputs (+12 V/1.4 A isolated, +5 V/0.3 A isolated, +15 V/150 mA non-isolated).

Highlights of the auxiliary power supply for indoor air-conditioner unit are:

- Tightly regulated output voltages, high efficiency under light load and low standby power
- Comprehensive protection for a robust system
- Auto-restart protection scheme to minimize interruption and enhance end-user experience

Intended audience

This document is intended for power supply design engineers who are designing auxiliary power supplies for residential air-conditioner units that are efficient, reliable and easy to design.

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System introduction

1 System introduction

With the growing household trend for internet-connected devices, the new generation of home appliances such as air-conditioners are equipped with advanced features such as wireless control and monitoring capability, smart sensors and touch screen display. These can transform a static product into an interactive and intelligent home appliance, capable of adapting to the smart-home theme. Infineon has introduced the latest fifthgeneration FF CoolSET[™] to address this need in an efficient and cost-effective manner.

An auxiliary SMPS is needed to power the various modules and sensors, which typically operate from a stable DC voltage source. The Infineon CoolSET[™] (as shown in Figure 1) forms the heart of the system, providing the necessary protection and AC-DC conversion from the mains to multiple regulated DC voltages to power the various blocks.

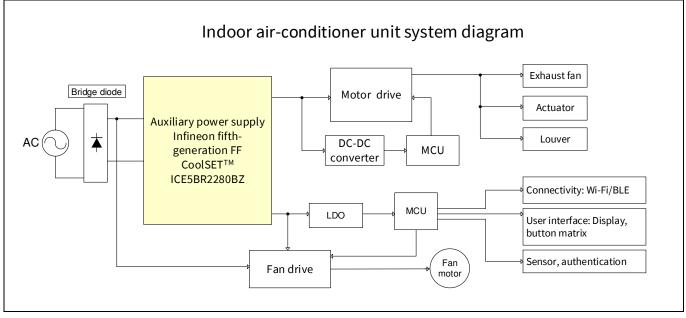


Figure 1 Simplified indoor air-conditioner system diagram

Table 1 lists the system requirements for auxiliary power supply for an indoor air-conditioner unit, and the corresponding Infineon solution is shown in the right-hand column.

Table	e i System requirements and immeon solution	15				
	System requirement for indoor air-conditioner unit power supply	Infineon solution - ICE5BR2280BZ				
1	High efficiency under light load and low standby power	Digital frequency reduction and active burst mode (ABM)				
2	Robust system and protection features	Comprehensive protection feature CoolSET™ in DIP-7 package				
3	Auto-restart protection scheme to minimize interruption to enhance end-user experience	All protections are in auto-restart				

 Table 1
 System requirements and Infineon solutions

1.1 High efficiency under light load and low standby power

During indoor air-conditioner operation, the power requirement fluctuates according to various use cases. However, in most cases where room temperature is already stabilized, the indoor and outdoor air-conditioner units will reside in an idle state, in which the loading toward the auxiliary power supply is low. It is crucial that



System introduction

the auxiliary power supply operates as efficiently as possible, because it will be in this particular state for most of the period. Under light-load conditions, losses incurred with the power switch are usually dominated by the switching operation. The choice of switching scheme and frequency play a crucial role in ensuring high conversion efficiency.

In this reference design, ICE5BR2280BZ was primarily chosen due to its frequency reduction switching scheme. Compared with a traditional FF flyback, the CoolSET[™] reduces its switching frequency from medium to light load, thereby minimizing switching losses. Therefore, an efficiency of more than 80 percent is achievable under 25 percent loading conditions and nominal input voltages.

1.2 Simplified circuitry with good integration of power and protection features

To relieve the designer of the complexity of PCB layout and circuit design, CoolSET[™] is a highly integrated device with both a controller and a HV MOSFET integrated into a single, space-saving DIP-7 package. These certainly help the designer to reduce component count as well as simplifying the layout into a simple PCB design for ease of manufacturing, using the traditional cost-effective wave-soldering process.

1.3 Auto-restart protection scheme to minimize interruption to enhance end-user experience

For a residential air-conditioner unit, it would be annoying to both the end user and the manufacturer if the system were to halt and latch after protection. Accessibility of the input AC plug may also be difficult; therefore, to minimize interruption, the CoolSET[™] implements auto-restart mode for all abnormal protections.



Reference board design

2 Reference board design

This document provides complete design details including specifications, schematics, bill of materials (BOM), PCB layout and transformer design. Performance results pertaining to line/load regulation, efficiency, transient load, thermal conditions, conducted EMI scans and so on are also included.



Figure 2 REF_5BR2280BZ_22W1



Power supply specifications

3 Power supply specifications

The table below shows the minimum acceptable performance of the design at 25°C ambient temperature. Actual performance is listed in the measurements section.

Description	Symbol	Min.	Тур.	Max.	Units	Comments
Input	0,		.,,,,	- Turki	• mes	
Voltage	V _{IN}	85	_	264	V AC	2 wires (no P.E.)
Frequency	f _{LINE}	47	50/60	64	Hz	
No-load input power	P _{stby_NL}	_	-	120	mW	
Output				120		
Output voltage 1	V ₀₁	_	12	_	v	± 1 percent
Output current 1	I ₀₁	_	-	1.4	Ā	1 percent
Output voltage ripple 1	V _{RIPPLE1}	_	_	120	mV	
Output voltage 2	V _{O2}	_	5	-	v	± 1 percent
Output current 2	I _{O2}	5	-	300	mA	
Output voltage ripple 2	V _{RIPPLE2}	-	_	75	mV	
Output voltage 3	V _{O3}	_	15		V	± 1 percent
Output current 3	I _{O3}	5	-	150	mA	
Output voltage ripple 3	V _{RIPPLE3}	_	_	100	mV	
Output power	P _{OUT_Nom}	-	19.75	_	w	
Overcurrent protection (+12 V)	I _{OCP}		1.5	-	А	0.15 A load on 15 V and 0.5 A load on 5 V load
Start-up time	$t_{\text{start}_{up}}$		-	250	ms	
Efficiency						
Maximum load	η	75	-	-	%	
Average efficiency	η_{avg}	75	-	-	%	115 V AC/230 V AC
Maximum load (single output)	η_{s}	83	-	-	%	,
Average efficiency (single output)	η_{avg_s}	83	-	-	%	
Environmental						
Conducted EMI			6		dB	Margin, CISPR 22 class B
ESD			±6		kV	EN 61000-4-2
Surge immunity						EN 61000-4-5
Differential mode			±2		kV	
Common mode			±4		kV	
PCB dimension			110 x 57		mm ²	LxW

Table 2 Specifications of REF_5BR2280BZ_22W1

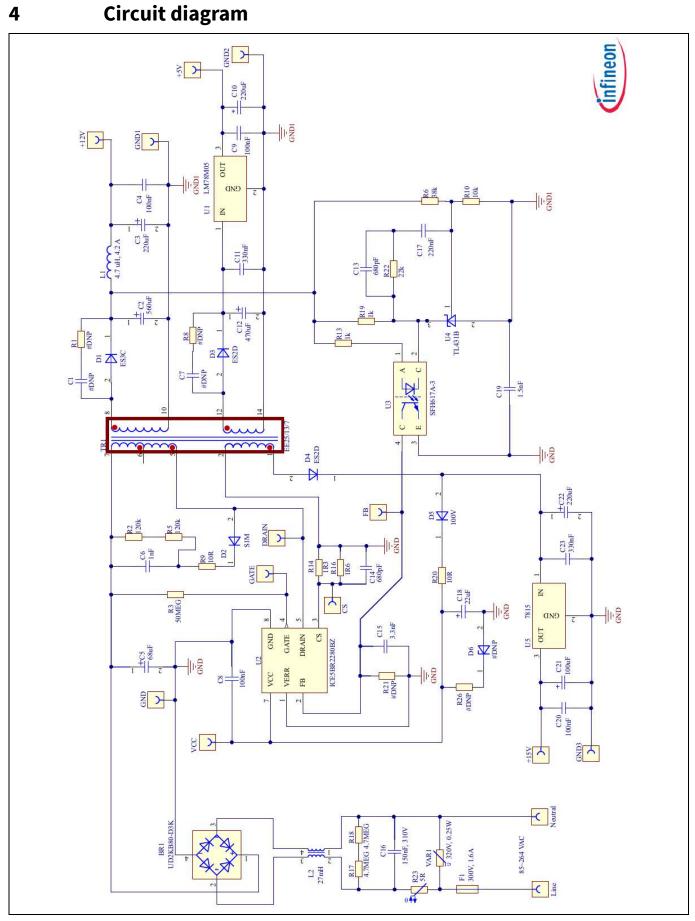


Figure 3 Schematic of REF_5BR2280BZ_22W1

4







Circuit description

5 Circuit description

In this section, the design circuit for the SMPS unit will be briefly described by the different functional blocks. For details of the design procedure and component selection for the flyback circuitry please refer to the IC design guide [2] and calculation tool [3].

5.1 EMI filtering and line rectification

The input of the power supply unit is taken from the AC power grid, which is in the range of 85 V AC ~ 264 V AC. The fuse F1 is directly connected to the input line to protect the system in case of excess current entering the system circuit due to any fault. Following is the varistor VAR1, which is connected across the input to absorb excess energy during line-surge transient. The X-capacitor C16 and common-mode choke (CMC) L2 reduce the EMI noise. R17 and R18 serve as the X-capacitor discharge resistor. The bridge rectifier BR1 rectifies the AC input into DC voltage, filtered by the bulk capacitor C5.

5.2 Flyback converter power stage

The flyback converter power stage consists of transformer TR1, CoolSET[™], secondary rectification diodes D1, D3 and D4, secondary output capacitors C2, C12 and C22 and output filter inductor L1.

When the primary HV MOSFET turns on, energy is stored in the transformer. When it turns off, the stored energy is discharged to the output capacitors and into the output load.

Secondary winding is sandwiched between two layers of primary winding to reduce leakage inductance. This improves efficiency and reduces voltage spikes.

For the output rectification, lower forward voltage and ultra-fast recovery diodes can improve efficiency. Capacitor C2, C12 stores the energy needed during output load jumps. LC filter L1/C3 reduces the high-frequency ripple voltage.

The +15 V output is from the 15 V low dropout (LDO) regulator (U5) with an input of +18 V, which also supplies V_{cc}. The +5 V output is from the 5 V LDO regulator (U1) with an input of +8 V. As such, these two outputs would not be affected by cross-regulation. However, their inputs should be maintained within the operating range of the LDO.

5.3 Control of flyback converter through fifth-generation FF CoolSET™ ICE5BR2280BZ

5.3.1 Current sensing

The ICE5BR2280BZ is a current mode controller. The primary peak current is controlled cycle-by-cycle through the CS resistors R14 and R16 in the CS pin (pin 4). Transformer saturation can be avoided through peak current limitation (PCL); therefore, the system is more protected and reliable.

5.3.2 Feedback and compensation network

Resistor dividers R24 and R27 are used to sense the V_{OUT} and send the reference voltage to the feedback (FB) pin (pin 2) via error amplifier TL431(U4) and optocoupler(U3). A Type II compensation network C13, C17 and R22 is implemented to stabilize the system.

The FB pin of ICE5BR2280BZ is a multifunction pin, which is used to select the entry burst power level (there are three levels available) through the resistor at the FB pin (R21) and also the burst-on/burst-off sense input during ABM.



Circuit description

Unique features of the fifth-generation FF CoolSET[™] ICE5BR2280BZ 5.4

5.4.1 Fast self-start-up and sustaining of V_{cc}

The IC uses a cascode structure to fast-charge the V_{cc} capacitor. Pull-up resistors R3, R6 and R10 connected to the GATE pin (pin 4) are used to initiate the start-up phase. At first, I_{VCC_Charge1} is used to charge the V_{CC} capacitor from 0 V to V_{VCC SCP}. This is a protection which reduces the power dissipation of the power MOSFET during V_{CC} short-to-GND condition. Thereafter, a much higher charging current of I_{VCC_Charge2} will charge the V_{cc} capacitor until the $V_{CC ON}$ is reached.

After start-up, the IC V_{cc} supply is usually sustained by the auxiliary winding of the transformer, which needs to support the V_{cc} to be above undervoltage lockout (UVLO) voltage (10 V typ.). In this reference board, the V_{cc} supply is tapped from the +18 V winding.

5.4.2 CCM, DCM operation with frequency reduction

ICE5BR2280BZ can be operated in either discontinuous conduction mode (DCM) or continuous conduction mode (CCM) with frequency-reduction features. This reference board is designed to operate in DCM at operating input voltage and load conditions. When the system is operating at high output load, the controller will switch at 65 kHz FF. In order to achieve a better efficiency between light load and medium load, frequency reduction is implemented as a function of V_{FB}, as shown in Figure 4. Switching frequency will not reduce further once the minimum switching frequency of 28 kHz is reached.

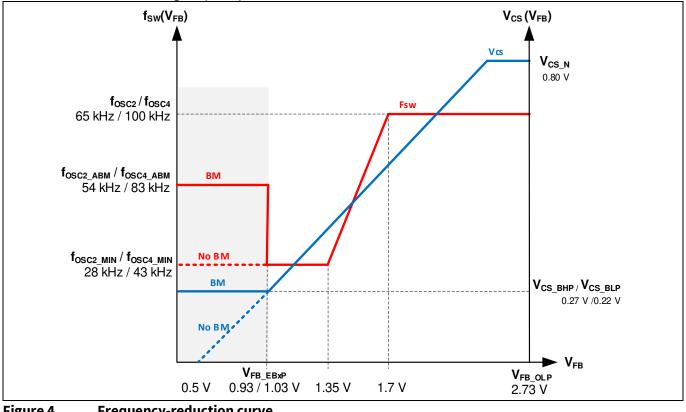


Figure 4 Frequency-reduction curve

5.4.3 Frequency jittering with modulated gate drive

The ICE5BR2280BZ has a frequency jittering feature with modulated gate drive to reduce the EMI noise. The jitter frequency is internally set at ± 4 percent of 65 kHz, and the jitter period is 4 ms.



Circuit description

5.4.4 System robustness and reliability through protection features

Protection is one of the major factors in determining whether the system is safe and robust – therefore sufficient protection is necessary. ICE5BR2280BZ provides comprehensive protection to ensure the system is operating safely. This includes V_{cc} overvoltage (OV) and undervoltage (UV), overload, overtemperature and V_{cc} short-to-GND. When those faults are found, the system will enter protection mode. Once the fault is removed, the system resumes normal operation. A list of protections and failure conditions is shown in the table below.

Protection function	Failure condition	Protection mode
V _{cc} OV	V_{VCC} greater than V_{VCC_OVP}	Odd-skip auto-restart
V _{cc} UV	V _{vcc} less than V _{vccoff}	Auto-restart
Overload	V_{FB} greater than $V_{\text{FB}_{\text{OLP}}}$ and lasts for $t_{\text{FB}_{\text{OLP}_{\text{B}}}}$	Odd-skip auto-restart
Overtemperature	T _J greater than T _{jcon_OTP} (T _{jHYS_OTP} hysteresis)	Non-switch auto-restart
V_{cc} short-to-GND (V_{vcc} = 0 V, start-up = 50 m Ω and V_{DRAIN} = 90 V)	V_{vcc} less than V_{vcc_scp} , $I_{vcc_charge1} \approx -$ 0.2 mA (typ.)	No start-up

Table 3 Protection functions of ICE5BR2280BZ

5.5 Clamper circuit

A clamper network (D2, C6, R2, R5, R9) is used to reduce the switching voltage spikes across the DRAIN pin of the integrated HV MOSFET of the CoolSET[™], which are generated by the leakage inductance of the transformer TR1. This is a dissipative circuit; therefore, R2 and R5 and C6 need to be fine-tuned depending on the voltage derating factor and efficiency requirement.

5.6 PCB design tips

For a good PCB design layout, there are several points to note.

• The switching power loop needs to be as small as possible (see Figure 5). There are four power loops in the reference design; one on the HV side and three on the output side. The HV side loop starts from the bulk capacitor (C5) positive terminal, primary transformer winding (pin 7 and pin 5 of TR1), CoolSET[™], CS resistors and back to the C5 negative terminal. The first output side loop (12 V output) starts at the transformer winding (pin 8 of TR1), output diode D1, output capacitor C2 and back to pin 10 of TR1. The second output side loop (8 V output) starts at the transformer winding (pin 12 of TR1), output diode D3, output capacitor C12 and back to pin 14 of TR1. The third output side loop (18 V output) starts at the transformer winding (pin 1 of TR1), output diode D4, output capacitor C18 and back to pin 2 of TR1.



Circuit description

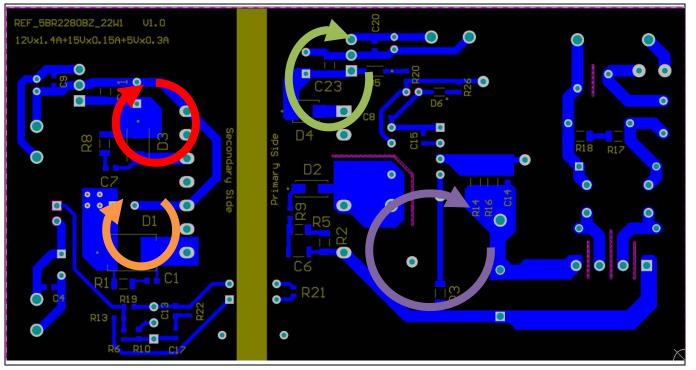


Figure 5 **PCB** layout tips

- Star-ground connection should be used to reduce high-frequency (HF) noise coupling that can affect the functional operation. The ground of the small-signal components should connect directly to the IC ground (pin 8 of U2).
- Separating the HV components and LV components, e.g., clamper circuit, main switching circuit; this can help to reduce spark-over chance of the high energy surge during a lightning surge test.
- The PCB copper pour on the DRAIN pin of the MOSFET can act as a heatsink, thus it can be widened if necessary.

5.7 **EMI reduction tips**

EMI compliance is always a challenge for the power supply designer. There are several critical points to consider in order to achieve a satisfactory EMI performance.

- A proper transformer design can significantly reduce EMI. Low leakage inductance can incur a low switching spike and HF noise. Interlaced winding technique is the most common practice to reduce leakage inductance. Winding shield, core shield and whole transformer shield are also some of the techniques used to reduce EMI.
- Input CMC and X-capacitor greatly reduce EMI, but this is costly and impractical especially for low-power applications.
- Short-switching power-loop design in the PCB (as described in section 5.6) can reduce radiated EMI due to the antenna effect.
- An output diode snubber circuit can reduce HF noise.
- Ferrite beads can reduce HF noise, especially on critical nodes such as the DRAIN pin, clamper diode and output diode terminals. There is no ferrite bead used in this design, as this can reduce the efficiency due to additional losses, especially on high-current terminals.

PCB layout

6 PCB layout



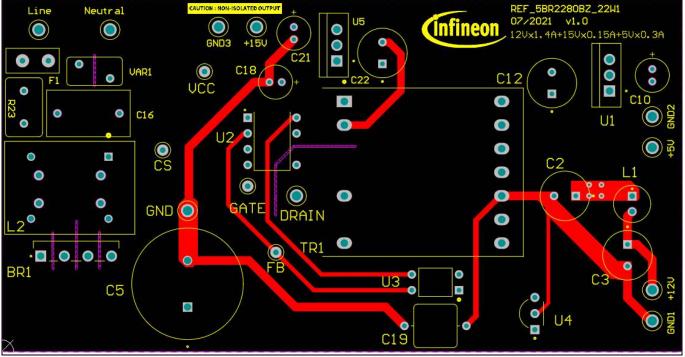


Figure 6

Top-side copper and component legend

6.2 Bottom side

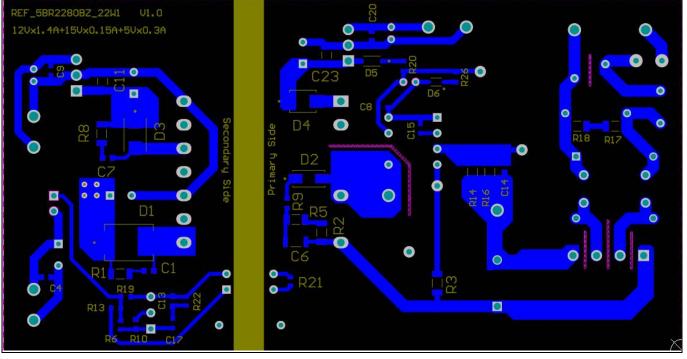


Figure 7 Bottom-side copper and component legend





Bill of materials

7 Bill of materials

No.	Designator	Description	Manufacturer	Part number	Quantity
1	BR1	Bridge diode 800 V 2 A		UD2KB80-7000	1
2	C2	Aluminum capacitor 560 μF 20% 25 V Rubycon 25ZLJ560M8X20		25ZLJ560M8X20	1
3	C3, C22	Aluminum capacitor 220 μF 20% 35 V radial	Rubycon	35ZLH220MEFCT78X11.5	2
4	C4, C8, C9, C20	Ceramic capacitor 0.1 μF 50 V X7R 0603			4
5	C5	Aluminum capacitor 68 μF 20% 400 V radial	Rubycon	400BXW68MEFC18X20	1
6	C6	Ceramic capacitor 1206 1 nF 500 V X7R 10% FL			1
7	C10, C21	Aluminum capacitor 100 μF 20% 25 V radial	Rubycon	25PX100MEFC5X11	2
8	C11, C23	Ceramic capacitor 0.33 μF 50 V X7R 1206			2
9	C12	Aluminum capacitor 470 μF 20% 16 V T/H	Rubycon	16ZLJ470M8X11.5	1
10	C13, C14	Ceramic capacitor 0603 680 pF 50 V X7R 10%			2
11	C15	Ceramic capacitor 0603 3.3 nF 50 V X7R 10%			1
12	C16	Film capacitor 0.15 μF 10% 310 V AC radial	Würth Elektronik	890334023025	1
13	C17	Ceramic capacitor 0.22 μF 50 V X7R 0603			1
14	C18	Aluminum capacitor 22 μF 20% 35 V radial	UVR1V220MDD Nichicon		1
15	C19	Ceramic capacitor 1500 pF 250 V radial	Murata	DE1E3KX152MA4BN01F	1
16	D1	General-purpose diode 150 V 3 A SMC	ES3C		1
17	D2	General-purpose diode 1 kV 1 A SMA	S1M		1
18	D3, D4	General-purpose diode 200 V 2 A DO214AA	ES2C		2
19	D5	General-purpose diode 100 V 150 mA SOD-123	Diodes Incorporated	BAV16W-7-F	1
20	F1	Time-lag fuse 300 V 1.6 A	Littelfuse	36911600000	1
21	L1	Inductor WE-TI size 5075 4.7 $\mu\text{H},$ 4.2 A	Würth Elektronik	7447462047	1
22	L2	CMC 27 mH 700 mA 2LN TH	TDK	B82731M2701A030	1
23	R2, R5	SMD resistor 120 k Ω 1% 1/4 W 1206			2
24	R3	SMD resistor 50 m Ω 1% 1206		CRHA1206AF50M0FKEF	3
25	R6	RES 38 kΩ 1% 1/10 W 0603			1
26	R9, R20	SMD resistor 10 Ω 1% 1/10 W 0603			2
27	R10	Resistor 10 k Ω 1% 1/10 W 0603			1
28	R14	SMD resistor 1.3 Ω 1% 1/4 W 1206			1
29	R16	SMD resistor 1.6 OΩ 1% 1/4 W 1206			1
30	R13, R19	SMD resistor 1 k Ω 1% 1/8 W 0603			2
31	R17, R18	SMD resistor 4.7 m Ω 1% 1/4 W 1206			2
32	R22	Resistor 22 kΩ 1% 1/10 W 0603			1
33	R23	ICL 5 Ω 20% 4.2 A 9.5 mm	ТДК	B57235S0509M000	1
34	TR1	EE25/13/7	Würth Elektronik	750344864	1
35	U1	L7805		L7805ABV	1
36	U2	FF 800 V CoolSET™	Infineon	ICE5BR2280BZ	1
37	U3	Optocoupler 5300 V _{RMS}		SFH617A-3	1
38	U4	IC V _{REF} shunt 36 V 0.4% TO92-3		TL431BVLPG	1



Bill of materials

39	U5	L7815		L7815ABV	1
40	VAR1	S07K320E2 320 V AC 10%	Epcos	B72207S2321K101	1
41	+15 V, +5 V, +12 V, DRAIN, neutral	Test point THT, red	Keystone	5010	5
42	CS, FB, GATE, Vcc	Test point THT, white	Keystone	5002	4
43	GND, GND1, GND2, line	Test point THT, black	Keystone	5011	4



Transformer specification

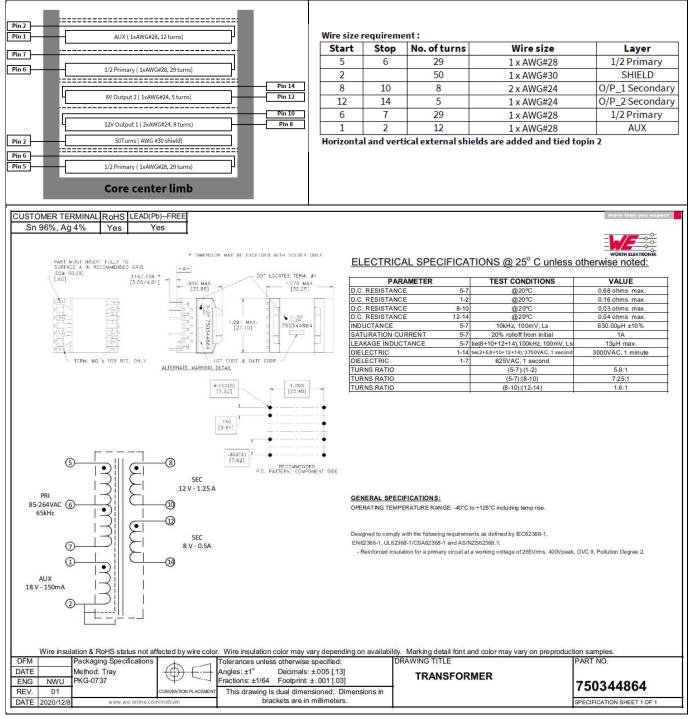
8 Transformer specification

Refer to Appendix A for transformer design and Appendix B for WE transformer specification.

Core name and material: EE25/13/7, TP4A (TDG)

Primary inductance: L_P = 630 µH (±10 percent), measured between pin 5 and pin 7

Manufacturer and part number: Würth Elektronik Midcom (750344864) Rev. 01





Measurement data and graphs

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Measurement data and graphs

Table 5 Electrical measurements

Input (V AC/Hz)	P _™ (W)	V ₀₁ (V)	I ₀₁ (A)	V _{o2} (V)	I ₀₂ (A)	V ₀₃ (V)	І _{оз} (А)	Р _{оит} (W)	Efficiency (%)	Average efficiency (%)	OLP P _{IN} (W)	OLP I ₀₁ (A) 5 V/0.3 A 15 V/0.15 A		
	0.289	11.997	0.00	5.067	0.005	15.109	0.005							
	6.539	11.990	0.35	5.062	0.075	15.078	0.038	5.14	78.63%					
85 V AC/ 60 Hz	13.076	11.977	0.70	5.060	0.150	15.059	0.075	10.27	78.56%	79 1406	32.94	1.82		
	19.700	11.970	1.05	5.057	0.226	15.043	0.113	15.40	78.19%	78.14%				
	26.580	11.962	1.40	5.052	0.300	15.050	0.150	20.52	77.20%					
	0.291	11.997	0.00	5.067	0.005	15.109	0.005							
	6.468	11.990	0.35	5.062	0.075	15.078	0.038	5.14	79.49%	79.51%	32.03	1.82		
115 V AC/ 60 Hz	12.882	11.977	0.70	5.060	0.150	15.059	0.075	10.27	79.74%					
00112	19.346	11.970	1.05	5.057	0.226	15.043	0.113	15.40	79.62%					
	25.920	11.962	1.40	5.052	0.300	15.050	0.150	20.52	79.17%					
	0.322	11.997	0.00	5.067	0.005	15.109	0.005					1.81		
	6.541	11.990	0.35	5.062	0.075	15.078	0.038	5.14	78.61%					
230 V AC/ 50 Hz	12.887	11.977	0.70	5.060	0.150	15.059	0.075	10.27	79.71%		31.11			
50112	19.070	11.970	1.05	5.057	0.226	15.043	0.113	15.40	80.77%	80.03%				
	25.320	11.962	1.40	5.052	0.300	15.050	0.150	20.52	81.04%					
	0.326	11.997	0.00	5.067	0.005	15.109	0.005							
	6.638	11.990	0.35	5.062	0.075	15.078	0.038	5.14	77.46%					
264 V AC/ 50 Hz	12.874	11.977	0.70	5.060	0.150	15.059	0.075	10.27	79.79%	70.000/	31.57	1.84		
30112	19.130	11.970	1.05	5.057	0.226	15.043	0.113	15.40	80.52%	79.60%				
	25.450	11.962	1.40	5.052	0.300	15.050	0.150	20.52	80.63%					

Minimum load condition: 12 V/0 A, 5 V/5 mA, 15 V/5 mA

25 percent load condition: 12 V/0.35 A, 5 V/75 mA, 15 V/37.5 mA

50 percent load condition: 12 V/0.70 A, 5 V/150 mA, 15 V/75 mA

75 percent load condition: 12 V/1.05 A, 5 V/225 mA, 15 V/112.5 mA

100 percent load condition: 12 V/1.4 A, 5 V/300 mA, 15 V/150 mA



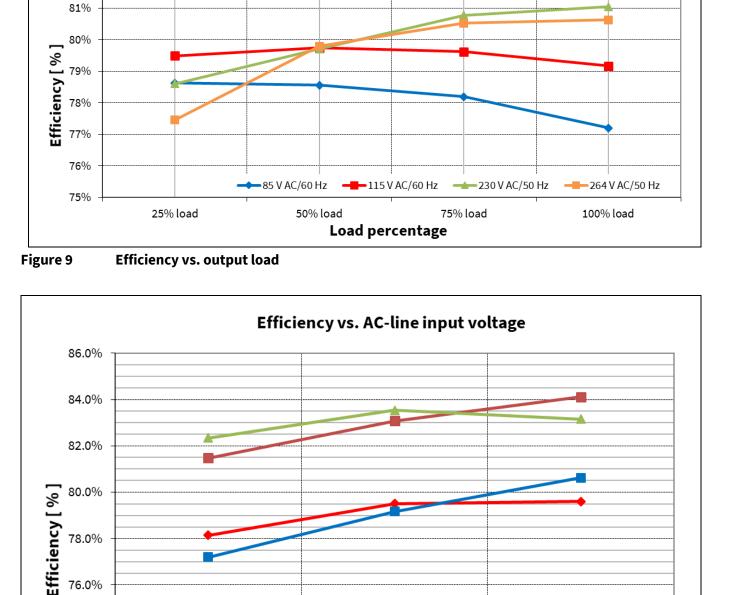


Measurement data and graphs

Table 6	Single-output electrical measurements
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Input (V AC/Hz)	P _{iN} (W)	V ₀₁ (V)	І _{о1} (А)	Р _{оит} (W)	Efficiency (%)	Average efficiency (%)
	0.039	12.000	0.00			
	5.059	11.990	0.35	4.20	82.95%	
85 V AC/ 60 Hz	10.115	11.980	0.70	8.39	82.91%	02.240/
00112	15.320	11.970	1.05	12.57	82.04%	82.34%
	20.550	11.960	1.40	16.74	81.48%	
	0.040	12.000	0.00			
	5.017	11.990	0.35	4.20	83.65%	
115 V AC/ 60 Hz	9.973	11.980	0.70	8.39	84.09%	02 540/
00112	15.081	11.970	1.05	12.57	83.34%	83.54%
	20.156	11.960	1.40	16.74	83.07%	
	0.050	12.000	0.00			
	5.076	11.990	0.35	4.20	82.67%	
230 V AC/ 50 Hz	10.093	11.980	0.70	8.39	83.09%	02 550/
50112	14.930	11.970	1.05	12.57	84.18%	83.55%
	19.880	11.962	1.40	16.75	84.24%	
	0.056	12.000	0.00			
	5.144	11.990	0.35	4.20	81.58%	
264 V AC/ 50 Hz	10.097	11.980	0.70	8.39	83.05%	02.100/
50112	14.984	11.970	1.05	12.57	83.88%	83.16%
	19.910	11.962	1.40	16.75	84.11%]

Note: Single-output (+12 V) efficiency measurement was done by removing two LDO regulators and adding a Zener clamp circuit ($R26 = 10 \Omega$, D6 = 22 V Zener). The reference board is not optimized for single-output configuration. The above efficiency data is for illustration only.



Active mode efficiency vs. load

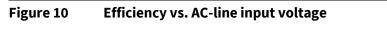
22 W auxiliary power supply for indoor air-conditioner using ICE5BR2280BZ

infineon

Measurement data and graphs

82%

9.1 Efficiency curve



85 V AC/60 Hz

74.0%

72.0%

115 V AC/60 Hz

Input voltage [VAC]

264 V AC/50 Hz



Measurement data and graphs

9.2 Standby power

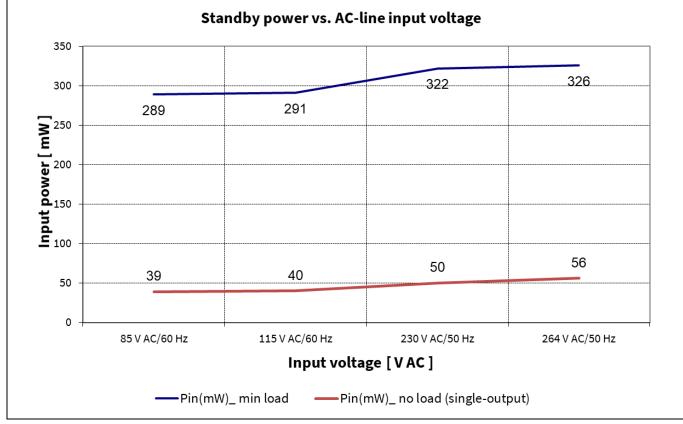
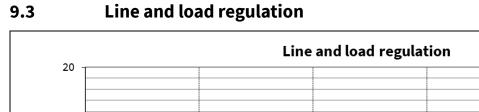


Figure 11 Standby power vs. AC-line input voltage



15 -Output voltage [V] 1 10 5 0 Min load 25% load 50% load 75% load 100% load Load percentage → 12 V, 115 V AC → 12 V, 230 V AC -12 V, 85 V AC → 12 V, 264 V AC → 5 V, 85 V AC **---**5 V, 115 V AC →5 V, 230 V AC —5 V, 264 V AC -15 V, 85 V AC → 15 V, 115 V AC →15 V, 264 V AC

Figure 12 Line and load regulation



Measurement data and graphs



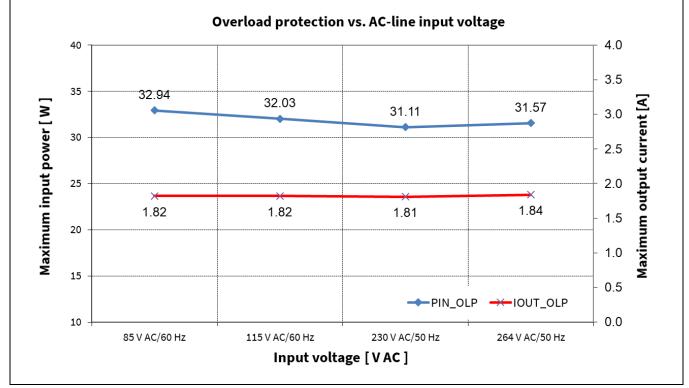


Figure 13 Maximum input power and 12 V output current before overload protection vs. AC-line input voltage (5 V/300 mA and 15 V/150 mA)

9.5 ESD immunity (EN 61000-4-2)

The system was subjected to a ±8 kV air and ±6 kV contact discharge ESD test according to EN 61000-4-2. A test failure was defined as non-recoverable.

Table 7System ESD test result

Description		1	Nu	mber of strik	es	To at an and
Description	ESD test	Level	V ₀₁ V ₀₂		GND1	Test result
	Contact	±6 kV	10	10	10	Pass
115 V AC, 22 W	Air	±8 kV	10	10	10	Pass
	Contact	±6 kV	10	10	10	Pass
230 V AC, 22 W	Air	±8 kV	10	10	10	Pass

9.6 Surge immunity (EN 61000-4-5)

The reference board was subjected to a surge immunity test (±2 kV DM and ±4 kV CM) according to EN 61000-4-5. It was tested at full load (resistive load). A test failure was defined as non-recoverable.

Description	Test		Loval			Number of strikes			
Description	Test	L	Level		90°	180°	270°	Test result	
	DM	±2 kV	$L \rightarrow N$	3	3	3	3	Pass	
115 V AC	СМ	±4 kV	$L \rightarrow G$	3	3	3	3	Pass	
		±4 kV	$N \rightarrow G$	3	3	3	3	Pass	
230 V AC	DM	±2 kV	$L \rightarrow N$	3	3	3	3	Pass	



Measurement data and graphs

	СМ	±4 kV	$L \rightarrow G$	3	3	3	3	Pass
		±4 kV	$N \rightarrow G$	3	3	3	3	Pass

9.7 Conducted emissions (EN 55022 class B)

The conducted EMI was measured by Schaffner (SMR4503) and followed the test standard of EN 55022 (CISPR 22) class B. The reference board was tested at full load (resistive load) at input voltage of 115 V AC and 230 V AC.

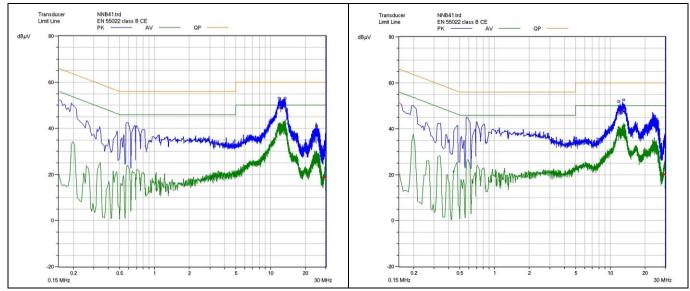


Figure 14 Conducted emissions at 115 V AC and full load on line (left) and neutral (right)

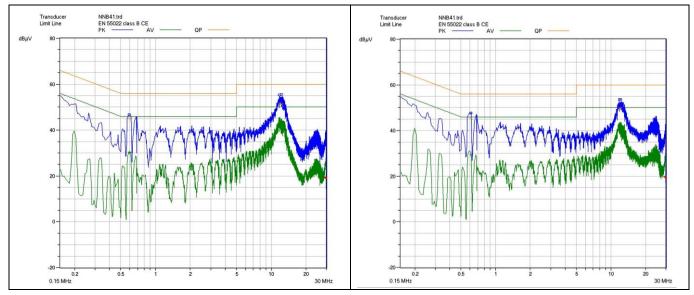


Figure 15 Conducted

Conducted emissions at 230 V AC and full load on line (left) and neutral (right)

9.8 Thermal measurement

Thermal measurement was done by using an infrared thermography camera (FLIR-T62101) at an ambient temperature of 25°C taken after one hour running at full load. The temperature of the components was taken in an open-frame set-up.



Measurement data and graphs

Table 8 Thermal measurement of components (open-frame)								
No.	Components	Temperature at 85 V AC (°C)	Temperature at 264 V AC (°C)					
1	U2 (ICE5BR2280BZ)	73.6	68.8					
2	U5 (L7815)	71.2	69.4					
3	U1 (L7805)	73.6	68.1					
4	TR1 (transformer)	56.3	57.6					
5	D1 (output 1 diode)	91.3	90.4					
6	D3 (output 2 diode)	67.0	67.6					
7	D4 (output 3 diode)	63.8	63.9					

 Table 8
 Thermal measurement of components (open-frame)



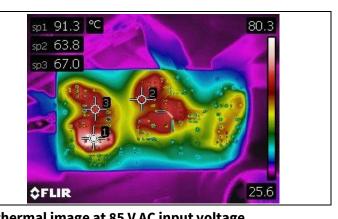


Figure 16 Top-side (left) and bottom-side (right) thermal image at 85 V AC input voltage

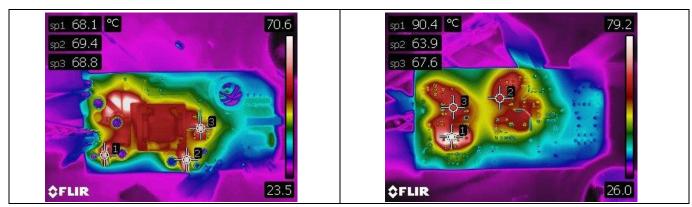


Figure 17 Top-side (left) and bottom-side (right) thermal image at 264 V AC input voltage



Measurement data and graphs

9.9 +18 V rail regulation (LDO input)

As the +15 V output via LDO is derived from the +18 V rail from the transformer which is also shared by the CoolSET[™] V_{cc}, there are several design goals during normal operating conditions:

- Avoid V_{cc} UVLO (10 V typ.)
- Avoid V_{cc} OVP (25.5 V typ.)

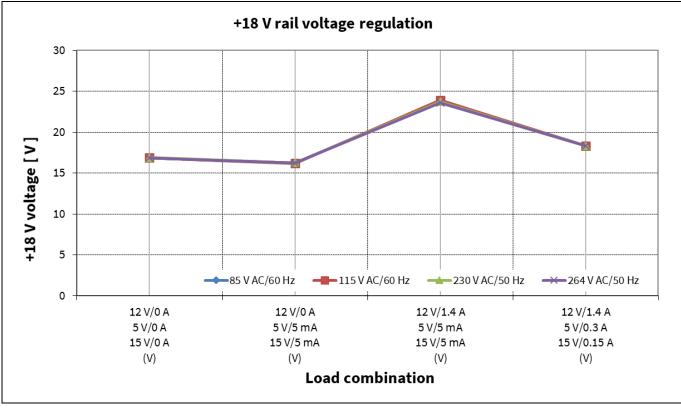
Table 9

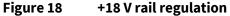
• Meet the specification of the LDO: $(V_{OUT} + 1 \sim 2 V) \le V_{IN} \le 30 V$; load dependent

+18 V rail line and load regulation

From the chart and table below, the +18 V rail is operating between 16.2 V and 23.90 V under different load combination and line conditions, which is well within the design objectives outlined above.

Conditions	12 V/0 A 5 V/0 A 15 V/0 A (V)	12 V/0 mA 5 V/5 mA 15 V/5 mA (V)	12 V/1.4 A 5 V/5 mA 15 V/5 mA (V)	12 V/1.4 A 5 V/0.3 A 15 V/0.15 A (V)
85 V AC/60 Hz	16.87	16.22	23.75	18.34
115 V AC/60 Hz	16.86	16.20	23.90	18.32
230 V AC/50 Hz	16.89	16.26	23.73	18.33
264 V AC/50 Hz	16.84	16.20	23.58	18.32







Waveforms and oscilloscope plots

10 Waveforms and oscilloscope plots

All waveforms and scope plots were recorded with a Teledyne LeCroy Waverunner 8054 oscilloscope.

10.1 Start-up at full load

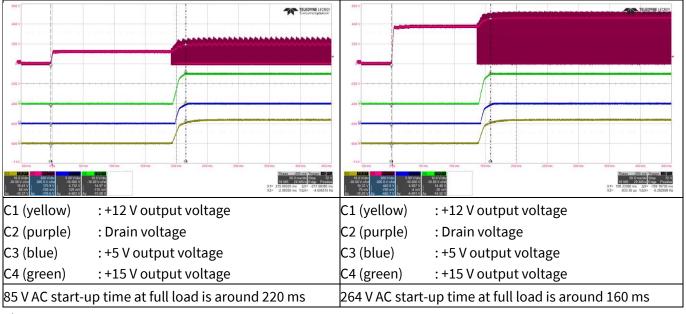
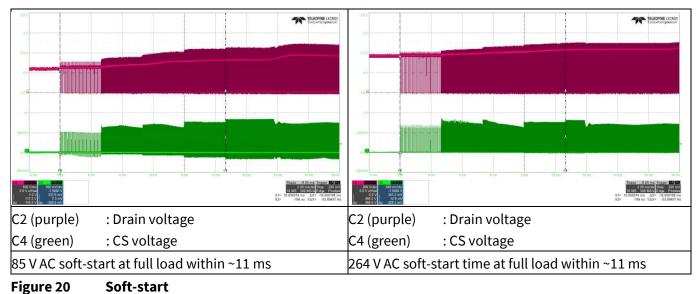


Figure 19 Start-up

10.2 Soft-start at full load





Waveforms and oscilloscope plots

10.3

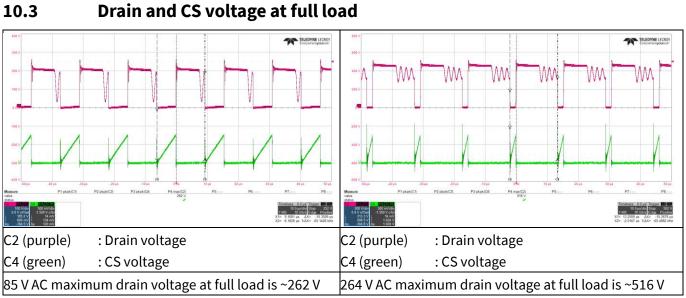


Figure 21 **Drain and CS voltage**



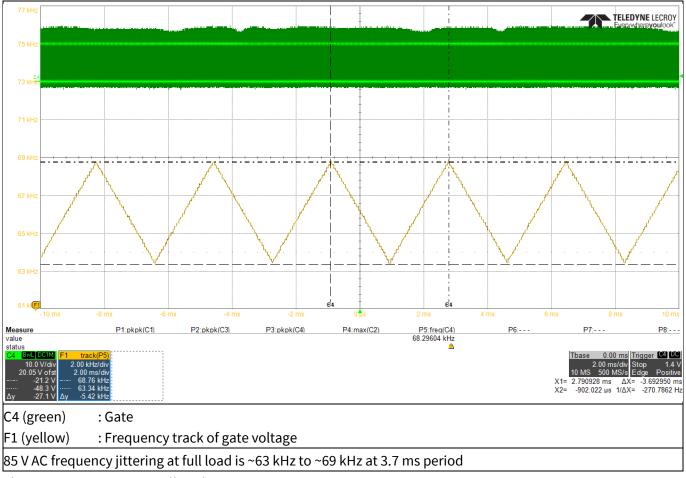
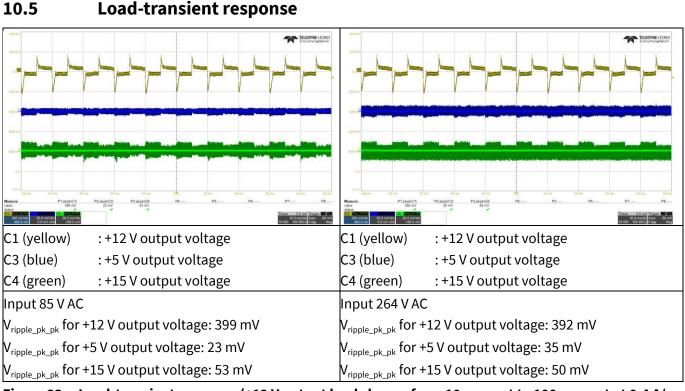


Figure 22 **Frequency jittering**

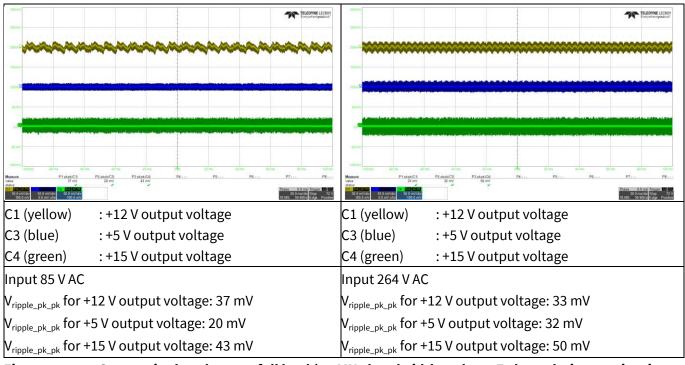


Waveforms and oscilloscope plots

10.5



Load-transient response (+12 V output load change from 10 percent to 100 percent at 0.4 A/ μ s Figure 23 slew rate, 100 Hz, +15 V output and +5 V output load are fixed at full load; 20 MHz bandwidth and 10 µF electrolytic capacitor in parallel with 0.1 µF ceramic capacitor)



10.6 Output ripple voltage at full load

Output ripple voltage at full load (20 MHz bandwidth and 10 µF electrolytic capacitor in Figure 24 parallel with 0.1 µF ceramic capacitor)

Output ripple voltage at ABM



Waveforms and oscilloscope plots

Entering ABM

10.7

10.8

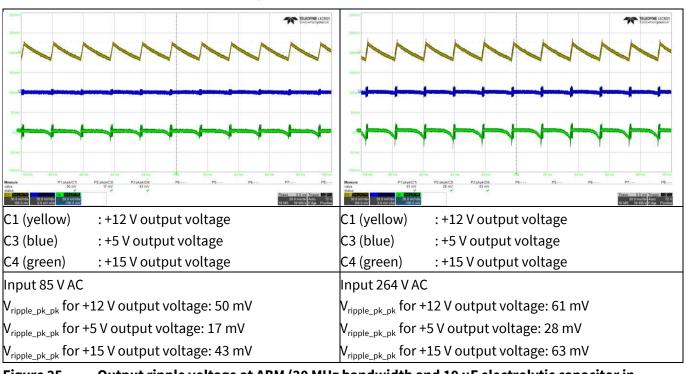


Figure 25 Output ripple voltage at ABM (20 MHz bandwidth and 10 µF electrolytic capacitor in parallel with 0.1 µF ceramic capacitor). Minimum load

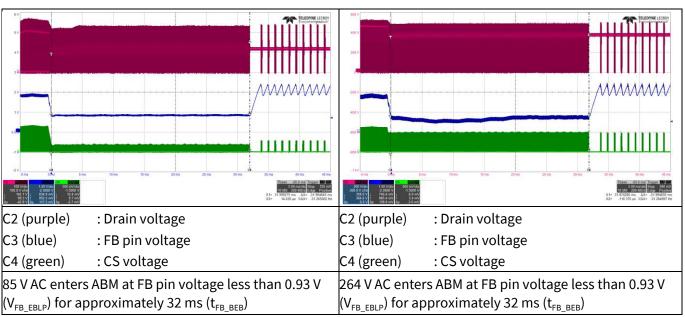


Figure 26 Entering ABM (+12 V output load change from 1.25 A to 50 mA; +15 V and +5 V outputs have minimum load)



Waveforms and oscilloscope plots

10.9 During ABM

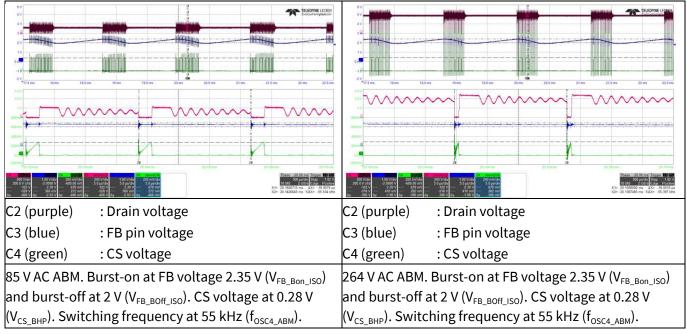


Figure 27 During ABM

10.10 Leaving ABM

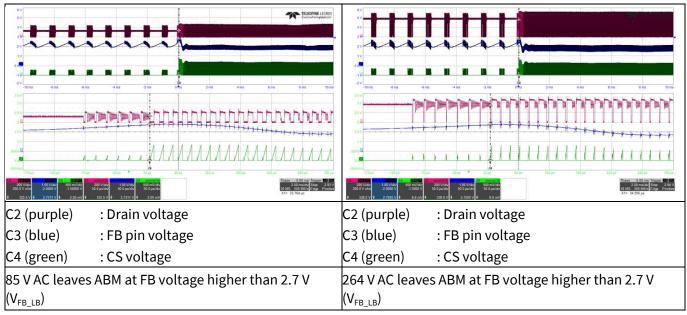


Figure 28 Leaving ABM (+12 V output load change from 50 mA to 1.4 A, +15 V and +5 V outputs have minimum load)



Waveforms and oscilloscope plots

10.11 V_{cc} OV/UV protection

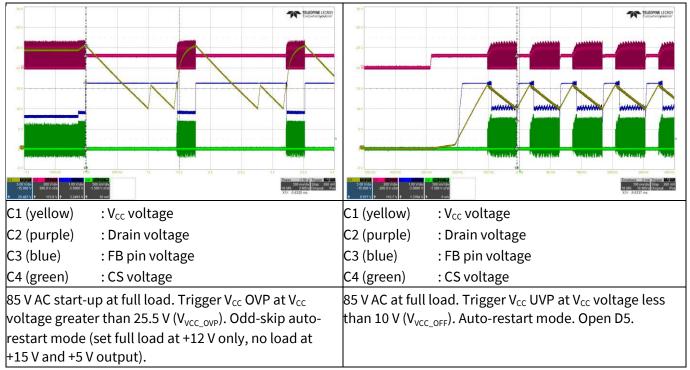


Figure 29 V_{cc} OV/UV protection

10.12 Overload protection

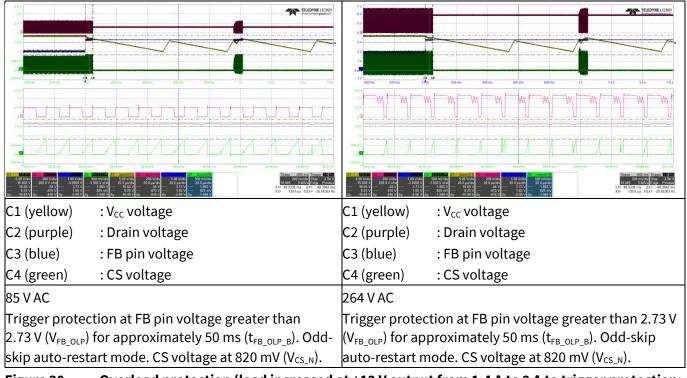


Figure 30Overload protection (load increased at +12 V output from 1.4 A to 2 A to trigger protection;
+15 V output and +5 V output fixed at full load)



Appendix A: Transformer design and spreadsheet [3]

11 Appendix A: Transformer design and spreadsheet [3]

Calculation tool for FF flyback converter using fifth-generation CoolSET[™] (Version 1.1)

Project:	REF_5BR2280BZ_22W1
Application:	Aux for residential air-conditioner unit
CoolSET™:	ICE5BR2280BZ
Date:	16 April 2021
Revision:	V1.1

Notes:

Enter design variables in orange-colored cells

Read design results in green-colored cells

Equation numbers are according to the design guide

Component designators below refer to the calculation tool

Select component values based on standard values available

 $Voltage/current\ rating\ does\ not\ include\ design\ margin,\ voltage\ spikes\ and\ transient\ currents$

In "Output regulation", only fill in either isolated or non-isolated, whichever is applicable

		Description	Eq. #	Parameter	Unit
Inpu	ıt, output, C	oolSET™ specs			
	Line input				
	Innut	Minimum AC insurtous literat		N	БИ

Input	Minimum AC input voltage	V _{ACMin}	[V]	85
Input	Maximum AC input voltage	V _{ACMax}	[V]	264
Input	Line frequency	f _{AC}	[Hz]	60
Input	Bus capacitor DC ripple voltage	V _{DCRipple}	[V]	24

F~ #

Value

Output 1 specs

Input	Output voltage 1		V _{Out1}	[V]	12
Input	Output current 1		I _{Out1}	[A]	1.40
Input	Forward voltage of output diode 1		V _{FOut1}	[V]	0.6
Input	Output ripple voltage 1		V _{OutRipple1}	[V]	0.2
Result	Output power 1	Eq. 001	P _{Out1}	[W]	16.8
Result	Output load weight 1	Eq. 004	K _{L1}		0.77

Output 2 specs

Input	Output voltage 2		V _{Out2}	[V]	7.5
Input	Output current 2		I _{Out2}	[A]	0.3
Input	Forward voltage of output diode 2		V _{FOut2}	[V]	0.2
Input	Output ripple voltage 2		V _{OutRipple2}	[V]	0.2
Result	Output power 2	Eq. 002	P _{Out2}	[W]	2.25
Result	Output load weight 2	Eq. 005	K _{L2}		0.10

Auxiliary

Input	V _{cc} voltage		Vvcc	[V]	18
Input	V _{cc} current			[A]	0.15
Input	Forward voltage of output diode 3		V _{FOut3}	[V]	0.4
Input	Forward voltage of V _{cc} diode(D2)		V _{FVcc}	[V]	0.6
Result	Output power 3	Eq. 002	P _{Out2}	[W]	2.7

Power

Fower					
Input	Efficency		η		0.83
Result	Nominal output power	Eq. 003	PoutNom	[W]	21.75
Input	Maximum output power for overload protection		P _{OutMax}	[W]	22
Result	Maximum input power for overload protection	Eq. 006	P _{InMax}	[W]	26.51
Input	Minimum output power		PoutMin	[W]	3



Appendix A: Transformer design and spreadsheet [3]

Controller/CoolSET™

	Controller/ CoolSET™			ICE5BR2280BZ
Input	Switching frequency	fs	[Hz]	65000
Input	Targeted max. drain source voltage	V _{DSMax}	[V]	700
Input	Max. ambient temperature	T _{amax}	[°C]	50

Diode bridge and input capacitor

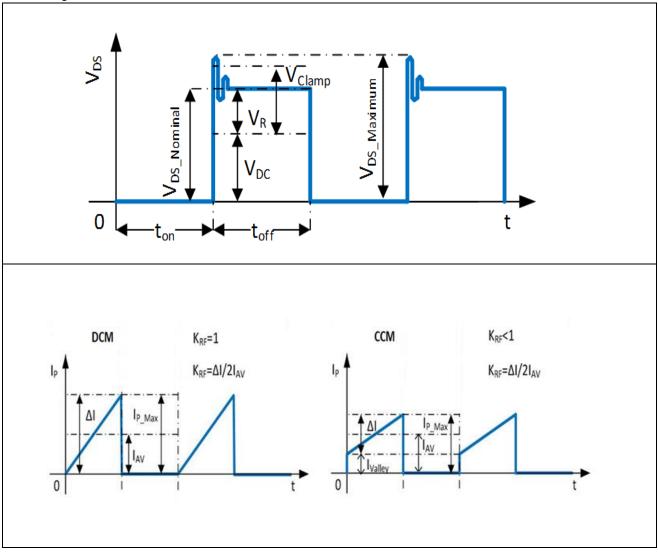
Diode brid	Diode bridge						
Input	Powerfactor		cosφ		0.6		
Result	Maximum AC input current	Eq. 007	I _{ACRMS}	[A]	0.520		
Result	Peak voltage at V _{ACMax}	Eq. 008	V _{DCMaxPk}	[V]	373.35		

Input capacitor

pat capa					
Result	Peak voltage at V _{ACMin}	Eq. 009	V _{DCMinPk}	[V]	120.21
Result	Selected minimum DC input voltage	Eq. 010	VDCMinSet	[V]	96.21
Result	Discharging time at each half-line cycle	Eq. 011	TD	[ms]	6.63
Result	Required energy at discharging time of input capacitor	Eq. 012	Win	[Ws]	0.18
Result	Calculated input capacitor	Eq. 013	CINCal	[µF]	67.65
Input	Select input capacitor (C1)		C _{in}	[µF]	68
Result	Calculated minimum DC input voltage	Eq. 015	V _{DCMin}	[V]	96.35

Transformer design

Drain voltage and current waveform





Appendix A: Transformer design and spreadsheet [3]

Primary i	Primary inductance and winding currents								
Input	Reflection voltage		V _{RSET}	[V]	90				
Result	Maximum duty cycle	Eq. 016	D _{Max}		0.48				
Input	Select current ripple factor		K _{RF}		1				
Result	Primary inductance	Eq. 017	LP	[H]	6.28E-04				
Result	Primary turn-on average current	Eq. 018	I _{AV}	[A]	0.57				
Result	Primary peak-to-peak current	Eq. 019	ΔI	[A]	1.14				
Result	Primary peak current	Eq. 020	I _{PMax}	[A]	1.14				
Result	Primary valley current	Eq. 021	I _{Valley}	[A]	0.00				
Result	Primary RMS current	Eq. 022	I _{PRMS}	[A]	0.457				

Select core type

	<i>/</i> /			
Input	Select core type			2
Result	Core type			E25/13/7
Result	Core material			N87
Result	Maximum flux density	В _{мах}	[T]	0.3
Result	Cross-sectional area	Ae	[mm²]	52
Result	Bobbin width	BW	[mm]	15.6
Result	Winding cross-section	A _N	[mm ²]	61
Result	Average length of turn	l _N	[mm]	50

Winding calculation

Result	Calculated minimum number of primary turns	Eq. 023	N _{PCal}	Turns	45.89
Input	Select number of primary turns		Np	Turns	58
Result	Calculated number of secondary 1 turns	Eq. 024	N _{S1Cal}	Turns	8.12
Input	Select number of secondary 1 turns		Ns1	Turns	8
Result	Calculated number of secondary 2 turns	Eq. 025	N _{S2Cal}	Turns	4.96
Input	Select number of secondary 2 turns		N _{S2}	Turns	5
Result	Calculated number of auxiliary turns	Eq. 026	NvccCal	Turns	11.81
Input	Select number of auxiliary turns		N _{Vcc}	Turns	12
Result	Calculated V _{cc} voltage	Eq. 027	V _{VccCal}	[V]	18.30

Post calculation

Result	Primary to secondary 1 turns ratio	Eq. 028	N _{PS1}		7.25
Result	Primary to secondary 2 turns ratio	Eq. 029	N _{PS2}		11.60
Result	Post calculated reflected voltage	Eq. 030	V _{RPost}	[V]	91.35
Result	Post calculated maximum duty cycle	Eq. 031	D _{MaxPost}		0.49
Result	Duty-cycle prime	Eq. 032	D _{Max} '		0.51
Result	Actual flux density	Eq. 033	B _{MaxAct}	[T]	0.237
Result	Maximum DC input voltage for CCM operation	Eq. 034	VDCmaxCCM	[V]	94.85

Transformer winding design

	5 5				
Input	Margin according to safety standard		М	[mm]	0
Input	Copper space factor		f _{Cu}		0.4
Result	Effective bobbin window	Eq. 035	BW _E	[mm]	15.6
Result	Effective winding cross-section	Eq. 036	A _{Ne}	[mm ²]	61.0
Input	Primary winding area factor		AF _{NP}		0.45
Input	Secondary 1 winding area factor		AF _{NS1}		0.30
Input	Secondary 2 winding area factor		AF _{NS2}		0.15
Input	Auxiliary winding area factor		AF _{NVcc}		0.10



Appendix A: Transformer design and spreadsheet [3]

Primary	Primary winding							
Result	Calculated wire copper cross-sectional area	Eq. 037	A _{PCal}	[mm ²]	0.1893			
Result	Calculated maximum wire size	Eq. 038	AWG _{PCal}		24			
Input	Select wire size		AWG _P		28			
Input	Select number of parallel wires		nw _P		1			
Result	Wire copper diameter	Eq. 039	d₽	[mm]	0.32			
Result	Wire copper cross-sectional area	Eq. 040	Ap	[mm ²]	0.0821			
Result	Wire current density	Eq. 041	Sp	[A/mm ²]	5.57			
Input	Insulation thickness		INS _P	[mm]	0.01			
Result	Turns per layer	Eq. 042	NLp	Turns/layer	45			
Result	Number of layers	Eq. 043	Ln _P	Layers	2			

Secondary 1 winding

Result	Calculated wire copper cross-sectional area	Eq. 044	A _{NS1Cal}	[mm ²]	0.9150
Result	Calculated maximum wire size	Eq. 045	AWG _{S1Cal}		18
Input	Select wire size		AWG _{S1}		24
Input	Select number of parallel wires		nws1		2
Result	Wire copper diameter	Eq. 046	ds1	[mm]	0.5131
Result	Wire copper cross-sectional area	Eq. 047	A _{S1}	[mm ²]	0.4136
Result	Peak current	Eq. 048	I _{S1Max}	[A]	6.3797
Result	RMS current	Eq. 049	I _{S1RMS}	[A]	2.6289
Result	Wire current density	Eq. 050	S _{S1}	[A/mm ²]	6.36
Input	Insulation thickness		INS _{S1}	[mm]	0.02
Result	Turns per layer	Eq. 051	NL _{S1}	Turns/layer	8
Result	Number of layers	Eq. 052	Ln _{S1}	Layers	1

Secondary 2 winding

Result	Calculated wire copper cross-sectional area	Eq. 053	A _{NS2Cal}	[mm ²]	0.7320
Result	Calculated maximum wire size	Eq. 054	AWG _{S2Cal}		19
Input	Select wire size		AWG _{S2}		24
Input	Select number of parallel wires		nw _{s2}		1
Result	Wire copper diameter	Eq. 055	d _{s2}	[mm]	0.5131
Result	Wire copper cross-sectional area	Eq. 056	A _{S2}	[mm ²]	0.2068
Result	Peak current	Eq. 057	I _{S2Max}	[A]	1.3671
Result	RMS current	Eq. 058	I _{S2RMS}	[A]	0.5633
Result	Wire current density	Eq. 059	S _{S2}	[A/mm ²]	2.72
Input	Insulation thickness		INS ₅₂	[mm]	0.02
Result	Turns per layer	Eq. 060	NL _{S2}	Turns/layer	28
Result	Number of layers	Eq. 061	Ln _{s2}	Layers	1

RCD clamper and CS resistor

RCD clamper circuit							
Input	Leakage inductance percentage		Llk%	[%]	2		
Result	Leakage inductance	Eq. 062	Llk	[H]	1.26E-05		
Result	Clamping voltage	Eq. 063	V _{Clamp}	[V]	235.30		
Result	Calculated clamping capacitor	Eq. 064	CclampCal	[nF]	0.21		
Input	Select clamping capacitor value (C2)		C _{clamp}	[nF]	1		
Result	Calculated clamping resistor	Eq. 065	R _{clampCal}	[kΩ]	185.5		
Input	Select clamping resistor value (R4)		R _{clamp}	[kΩ]	240		

CS resistor

Input	Current sense threshold value from datasheet		V _{CS_N}	[V]	0.8		
Result	Calculated current sense resistor (R8A, R8B)	Eq. 066	R _{sense}	[Ω]	0.70		



Appendix A: Transformer design and spreadsheet [3]

Output rectifier

Secondar	ry 1 output rectifier				
Result	Diode reverse voltage	Eq. 067	V _{RDiode1}	[V]	63.50
Result	Diode RMS current		I _{S 1RMS}	[A]	2.63
Input	Max. voltage undershoot at output capacitor		ΔV _{Out1}	[V]	0.4
Input	Number of clock periods		n _{cp1}		10
Result	Output capacitor ripple current	Eq. 068	I _{Ripple1}	[A]	2.23
Result	Calculated minimum output capacitor	Eq. 069	Cout1Cal	[µF]	538
Input	Select output capacitor value (C152)		C _{Out1}	[μF]	560
Input	ESR (Z _{max}) value from datasheet at 100 kHz		R _{ESR1}	[Ω]	0.032
Input	Number of parallel capacitors		nc _{COut1}		1
Result	Zero frequency of output capacitor	Eq. 070	f _{ZCOut1}	[kHz]	8.88
Result	First stage ripple voltage	Eq. 071	V _{Ripple1}	[V]	0.204151
Input	Select LC filter inductor value (L151)		L _{out1}	[µH]	4.7
Result	Calculated LC filter capacitor	Eq. 072	C _{LCCal1}	[μF]	68.3
Input	Select LC filter capacitor value (C153)		CLC1	[μF]	220
Result	LC filter frequency	Eq. 073	f _{LC1}	[kHz]	4.95
Result	Second stage ripple voltage	Eq. 074	V2ndRipple1	[mV]	1.18

Secondary 2 output rectifier

Result	Diode reverse voltage	Eq. 075	V _{RDiode2}	[V]	39.69
Result	Diode RMS current		I _{S2RMS}	[A]	0.56
Input	Max. voltage undershoot at output capacitor		ΔV_{Out1}	[V]	0.1
Input	Number of clock periods		n _{cp2}		10
Result	Output capacitor ripple current	Eq. 076	I _{Ripple2}	[A]	0.48
Result	Calculated minimum output capacitor	Eq. 077	C _{Out2Cal}	[µF]	462
Input	Select output capacitor value (C152)		C _{Out2}	[µF]	470
Input	ESR (Z _{max}) value from datasheet at 100 kHz		R _{ESR2}	[Ω]	0.032
Input	Number of parallel capacitors		NC _{COut2}		1
Result	Zero frequency of output capacitor	Eq. 078	f _{ZCOut2}	[kHz]	10.58
Result	First stage ripple voltage	Eq. 079	V _{Ripple2}	[V]	0.04
Input	Select LC filter inductor value (L151)		L _{out}	[µH]	na
Result	Calculated LC filter capacitor	Eq. 080	C _{LCCal2}	[µF]	na
Input	Select LC filter capacitor value (C153)		C _{LC2}	[µF]	na
Result	LC filter frequency	Eq. 081	f _{LC2}	[kHz]	na
Result	Second stage ripple voltage	Eq. 082	V2ndRipple2	[mV]	na

Vcc diode and capacitor

Vcc diode and capacitor

The aroue					
Result	Auxiliary diode reverse voltage (D2)	Eq. 083	V _{RDiodeVCC}	[V]	95.55
Input	Soft-start time from datasheet		tss	[ms]	12
Input	IvCC,Charge3 from datasheet		Ivcc_Charge3	[mA]	3
Input	V _{cc} on-threshold		Vvcc_on	[V]	16
Input	V _{cc} off-threshold		Vvcc_off	[V]	10
Result	Calculated V _{cc} capacitor	Eq. 084	C _{VCCCal}	[µF]	6.00
Input	Select Vcc capacitor (C3)		Cvcc	[uF]	22
Input	V _{cc} short threshold from datasheet		Vvcc_scp	[V]	1.1
Input	Ivcc_Charge1 from datasheet		I _{VCC_Charge1}	[mA]	0.2
Result	Start-up time	Eq. 085	t _{StartUp}	[ms]	230.267

Calculation of losses

Input diod	e bridge				
Input	Diode bridge forward voltage		V _{FBR}	[V]	1
Result	Diode bridge power loss	Eq. 086	P _{DIN}	[W]	1.04



Appendix A: Transformer design and spreadsheet [3]

Transformer copper

Result	Primary winding copper resistance	Eq. 087	R _{PCu}	[mΩ]	607.52
Result	Secondary 1 winding copper resistance	Eq. 088	R _{S1Cu}	[mΩ]	16.63
Result	Secondary 2 winding copper resistance	Eq. 089	R _{s2Cu}	[mΩ]	20.79
Result	Primary winding copper loss	Eq. 090	P _{PCu}	[mW]	126.94
Result	Secondary 1 winding copper loss	Eq. 091	P _{S1Cu}	[mW]	114.95
Result	Secondary 2 winding copper loss	Eq. 092	P _{S2Cu}	[mW]	6.60
Result	Total transformer copper loss	Eq. 093	P _{Cu}	[W]	0.2485

Output rectifier diode

Result	Secondary 1 diode loss	Eq. 094	P _{Diode1}	[W]	1.58
Result	Secondary 2 diode loss	Eq. 095	P _{Diode2}	[W]	0.11

RCD clamper circuit

Result	RCD clamper loss	Eq. 096	P _{Clamper}	[W]	0.74			
Current se	Current sense resistor							

	Result	Current sense resistor loss	Eq. 097	Pcs	[W]	0.15
--	--------	-----------------------------	---------	-----	-----	------

MOSFET

11001 E1					
Input	R _{DS(on)} from datasheet		R _{DS(on)} at T _A = 125°C	[Ω]	4.31
Input	C _{o(er)} from datasheet		C _{o(er)}	[pF]	7
Input	External drain-to-source capacitance		C _{DS}	[pF]	0
Result	Switch-on loss at minimum AC input voltage	Eq. 098	PSONMinAC	[W]	0.0080
Result	Conduction loss at minimum AC input voltage	Eq. 099	PcondMinAC	[W]	0.9005
Result	Total MOSFET loss at minimum AC input voltage	Eq. 100	P _{MOSMinAC}	[W]	0.9086
Result	Switch-on loss at maximum AC input voltage	Eq. 101	P _{SONMaxAC}	[W]	0.0491
Result	Conduction loss at maximum AC input voltage	Eq. 102	P _{condMaxAC}	[W]	0.2324
Result	Total MOSFET loss at maximum AC input voltage	Eq. 103	P _{MOSMaxAC}	[W]	0.2815
Result	Total MOSFET loss (from minimum or maximum AC)		P _{MOS}	[W]	0.9086

Controller

Input	Controller current consumption		Ivcc_Normal	[mA]	5.0
Result	Controller loss	Eq. 104	P _{Ctrl}	[W]	0.0915

Efficiency after losses

Result	Total power loss	Eq. 105	P _{Losses}	[W]	4.86
Result	Post calculated efficiency	Eq. 106	η _{Post}	%	81.90%

CoolSET[™]/MOSFET temperature

CoolSET™/MOSFET temperature

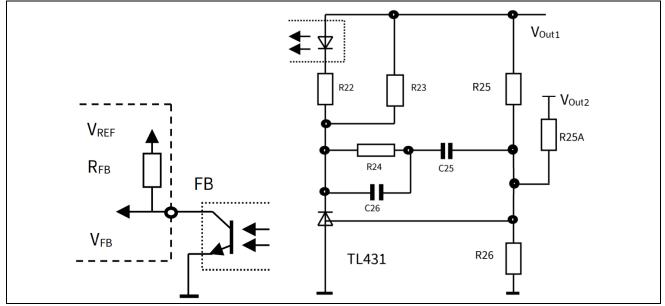
Input	Enter thermal resistance junction-ambient (include copper pour)		R _{thJA_As}	[°K/W]	65.0
Result	Temperature rise	Eq. 107	ΔT	[°K]	59.1
Result	Junction temperature at T _{amax}	Eq. 108	Tjmax	°C	109.1



Appendix A: Transformer design and spreadsheet [3]

Output regulation (isolated using TL431 and optocoupler)

Isolated feedback circuit



Output regulation

outputies	5				
Input	TL431 reference voltage		V _{REF_TL}	[V]	2.5
Input	Weighted regulation factor of V _{Out1}		W ₁		1
Input	Current for voltage divider resistor R26		I _{R26}	[mA]	0.25
Result	Calculated voltage divider resistor	Eq. 111	R26 _{Cal}	[kΩ]	10
Input	Select voltage divider resistor value		R26	[kΩ]	10
Result	Calculated voltage divider resistor	Eq. 112	R25 _{Cal}	[kΩ]	38.00
Input	Select voltage divider resistor value		R25	[kΩ]	38.0
Result	Calculated voltage divider resistor	Eq. 113	R25A _{Cal}	[kΩ]	na
Input	Select voltage divider resistor value		R25A	[kΩ]	na

Optocoupler and TL431 bias

Input	Current transfer ratio (CTR)		Gc	[%]	200%
Input	Optocoupler diode forward voltage		V _{FOpto}	[V]	1.25
Input	Maximum current for optocoupler diode		I _{Fmax}	[mA]	50
Input	Minimum current for TL431		I _{KAmin}	[mA]	1
Result	Calculated minimum optocoupler bias resistance	Eq. 114	R22 _{Cal}	[kΩ]	0.1650
Input	Select optocoupler bias resistor		R22	[kΩ]	1
Input	FB pull-up reference voltage V _{REF} from datasheet		V _{REF}	[V]	3.3
Input	V _{FB_OLP} from datasheet		V _{FB_OLP}	[V]	2.75
Input	R _{FB} from datasheet		R _{FB}	[kΩ]	15
Result	Calculated maximum TL431 bias resistance	Eq. 115	R23 _{Cal}	[kΩ]	1.27
Input	Selected TL431 bias resistor		R23	[kΩ]	1

Regulation loop

Result	Feedback transfer characteristic	Eq. 116	K _{FB}		30.00
Result	Gain of feedback transfer characteristic	Eq. 117	Gfb	[db]	29.54
Result	Voltage divider transfer characteristic	Eq. 118	K _{VD}		0.208333
Result	Gain of voltage divider transfer characteristic	Eq. 119	G _{VD}	[db]	-13.62
Result	Resistance at maximum load pole	Eq. 120	R _{LH}	[Ω]	6.55
Result	Resistance at minimum load pole	Eq. 121	R _{LL}	[Ω]	48.00
Result	Poles of power stage at maximum load pole	Eq. 122	f _{он}	[Hz]	86.84
Result	Poles of power stage at minimum load pole	Eq. 123	fol	[Hz]	11.84
Result	Zero frequency of the compensation network	Eq. 124	f _{ом}	[Hz]	32.07



Appendix A: Transformer design and spreadsheet [3]

Input	Zero dB crossover frequency		fg	[kHz]	3
Input	PWM-OP gain from datasheet		Av		2.03
Result	Transient impedance	Eq. 117	Z _{PWM}	[V/A]	1.8
Result	Power stage at crossover frequency	Eq. 118	F _{PWR} (fg)		0.171
Result	Gain of power stage at crossover frequency	Eq. 119	G _{PWR} (fg)	[db]	-15.34
Result	Gain of the regulation loop at fg	Eq. 120	Gs(ω)	[db]	0.580
Result	Separated components of the regulator	Eq. 121	Gr(ω)	[db]	-0.580
Result	Calculated resistance value of compensation network	Eq. 122	R24 _{Cal}	[kΩ]	7.41
Input	Select resistor value of compensation network		R24	[kΩ]	22
Result	Calculated capacitance value of compensation network	Eq. 123	C26 _{Cal}	[nF]	2.411
Input	Select capacitor value of compensation network		C26	[nF]	0.68
Result	Calculated capacitance value of compensation network	Eq. 124	C25 _{Cal}	[nF]	224.91
Input	Select capacitor value of compensation network		C25	[nF]	220

Final design

Electrical

icat		
Minimum AC voltage	[V]	85
Maximum AC voltage	[V]	264
Maximum input current	[A]	0.31
Minimum DC voltage	[V]	96
Maximum DC voltage	[V]	373
Maximum output power	[W]	22.0
Output voltage 1	[V]	12.0
Output ripple voltage 1	[mV]	1.2
Output voltage 2	[V]	7.5
Output ripple voltage 2	[mV]	na
Transformer peak current	[A]	1.14
Maximum duty cycle		0.49
Reflected voltage	[V]	91
Copper losses	[W]	0.25
MOSFET losses	[W]	0.91
Sum losses	[W]	4.86
Efficiency	[%]	81.90%

Transformer

or mer		
Core type		E25/13/7
Core material		N87
Effective core area	[mm ²]	52
Maximum flux density	[mT]	237
Inductance	[μH]	628
Margin	[mm]	0
Primary turns	Turns	58
Primary copper wire size	AWG	28
Number of primary copper wires in parallel		1
Primary layers	Layer	2
Secondary 1 turns(Ns1)	Turns	8
Secondary 1 copper wire size	AWG	24
Number of secondary 1 copper wires in parallel		2
Secondary 1 layers	Layer	1
Secondary 2 turns (N _{S2})	Turns	5
Secondary 2 copper wire size	AWG	24
Number of secondary 2 copper wires in parallel		1
Secondary 2 layers	Layer	1
Auxiliary turns	Turns	12
Leakage inductance	[μH]	12.6
nents		
Input capacitor (C1)	[uF]	68.0

Input capacitor (C1)		[μF]	68.0
Secondary 1 output capacitor (C152)		[μF]	560.0
Secondary 1 output capacitor in parallel			1.0



Appendix A: Transformer design and spreadsheet [3]

	Secondary 1 LC filter inductor (L151)		[µH]	4.7
	Secondary 1 LC filter capacitor (C153)		[µF]	220.0
	Secondary 2 output capacitor (C102)		[μF]	470.0
	Secondary 2 output capacitor in parallel			1.0
	Secondary 2 LC filter inductor (L101)		[µH]	na
	Secondary 2 LC filter capacitor (C103)		[µF]	na
	V _{cc} capacitor (C3)		[μF]	22.0
	Sense resistor (R8A, R8B)		[Ω]	0.70
	Clamping resistor (R4)		[kΩ]	240.0
	Clamping capacitor (C2)		[nF]	1
	Input capacitor (C1)		[μF]	68.0
	Secondary 1 output capacitor (C152)		[μF]	560.0
Regulat	ion components (isolated using TL431 and optocoupler)			
	Voltage divider	R26	[kΩ]	10.0
	Voltage divider (V ₀₁ sense)	R25	[kΩ]	38.0
	Voltage divider (V ₀₂ sense)	R25A	[kΩ]	na
	Optocoupler bias resistor	R22	[kΩ]	1.00
	TL431 bias resistor	R23	[kΩ]	1.0
	Compensation network resistor	R24	[kΩ]	22.0
	Compensation network capacitor	C26	[nF]	0.68
	Compensation network capacitor	C25	[nF]	220.0



Appendix B: WE transformer specification

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Appendix B: WE transformer specification

Sn 96%, Ag 4% Yes 'DIMENSION WAY BE EXCEEDED WITH SOLDER ONLY		CATIONS @ 25° C unless	
PART MUST INSERT FULLY TO SURFACE A IN RECOMMENDED OR D .024.50.(9) 118/158 DOT LOCATES TERM. #1	PARAMETER	TEST CONDITIONS	VALUE
[3.00/4.01] [3.00/4.01] [3.00/4.01] [3.2.25] [3.2.25]	D.C. RESISTANCE	5-7 @20°C	0.68 ohms max.
	D.C. RESISTANCE	1-2 @20°C	0.16 ohms max.
		3-10 @20°C	0.03 ohms max.
		2-14 @20°C	0.04 ohms max.
	INDUCTANCE	5-7 10kHz, 100mV, Ls	630.00µH ±10%
	SATURATION CURRENT	5-7 20% rolloff from initial	1A
	LEAKAGE INDUCTANCE	5-7 tie(8+10+12+14),100kHz, 100mV, Ls	s 13µH max.
	DIELECTRIC	1-14 tie(2+5,8+10+12+14), 3750VAC, 1 second	3000VAC, 1 minute
TERM, NO.'S FOR REF. ONLY	DIELECTRIC	1-7 625VAC, 1 second	
ALTERNATE MARKING DETAIL	TURNS RATIO	(5-7):(1-2)	5.8:1
	TURNS RATIO	(5-7):(8-10)	7.25:1
# 052(8) [1.32] - 1.000 [25.40] -	TURNS RATIO	(8-10):(12-14)	1.6:1
S P.C. PATERN, COMPONENT SIDI	t.		
P.C. PATTERN, COMPONENT SID	GENERAL SPECIFICATIONS: OPERATING TEMPERATURE RANGE: Designed to comply with the following requ EN62368-1, UL62368-1/CSA62368-1 and	irements as defined by IEC62368-1,	k, OVC II, Pollution Degree 2.
PRI 85-264VAC © 65kHz 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	GENERAL SPECIFICATIONS: OPERATING TEMPERATURE RANGE: -1 Designed to comply with the following requ EN62368-1, UL62368-1/CSA62368-1 and - Reinforced insulation for a primary circ and ing on availability. Marking detail for the provide the primary circ and the the primary cir	irements as defined by IEC62368-1, AS/NZS62368.1: uit at a working voltage of 265Vrms, 400Vpeak uit at a working voltage of 265Vrms, 400Vpeak	

Figure 31 WE transformer specification



References

13 References

- [1] Infineon Technologies AG: ICE5BR2280BZ Datasheet (V 1.0); 2022-02-22; ICE5BR2280BZ Datasheet
- [2] Infineon Technologies AG: Fifth-generation fixed-frequency design guide (V 1.1); 2019-07-24; Fifthgeneration fixed-frequency design guide
- [3] Infineon Technologies AG: Calculation tool for fixed-frequency flyback converter using fifth-generation CoolSET[™] (V 1.1); 2018-02-26; Calculation tool fixed-frequency CoolSET[™] 5th generation



Revision history

Revision history

Document version	Date of release	Description of changes
V 1.0	2022-06-15	First release

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